



1/79

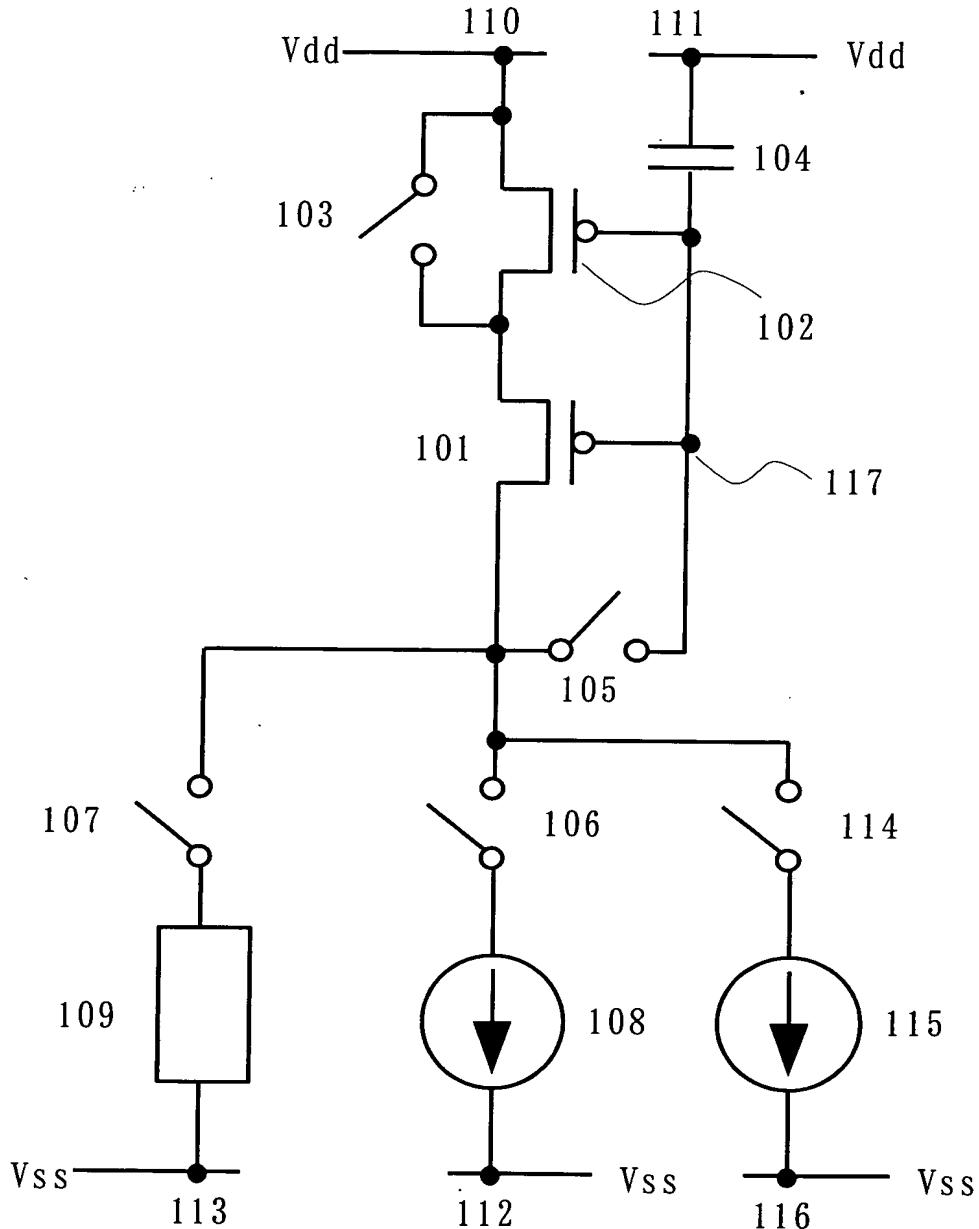


FIG. 1

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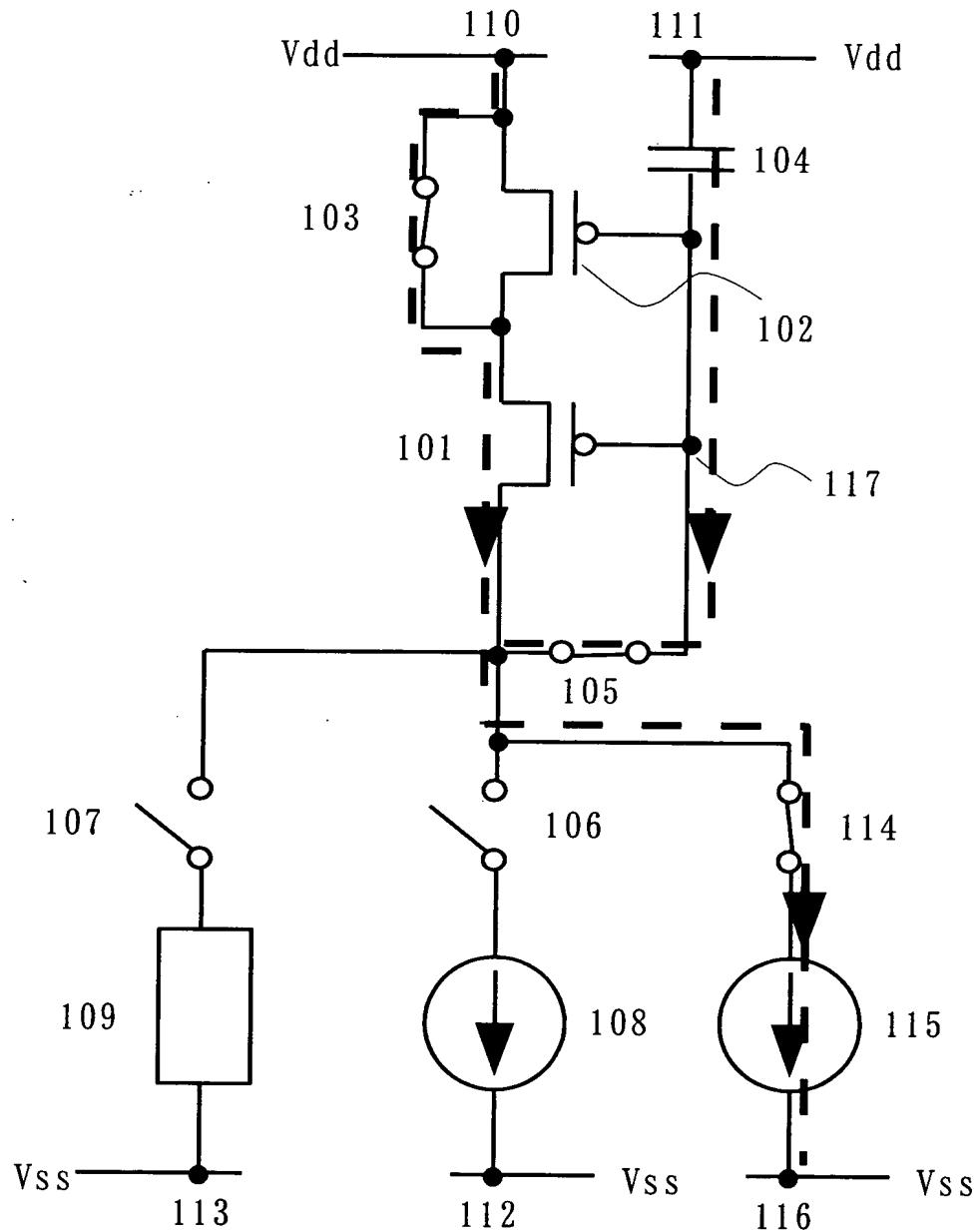


FIG. 2

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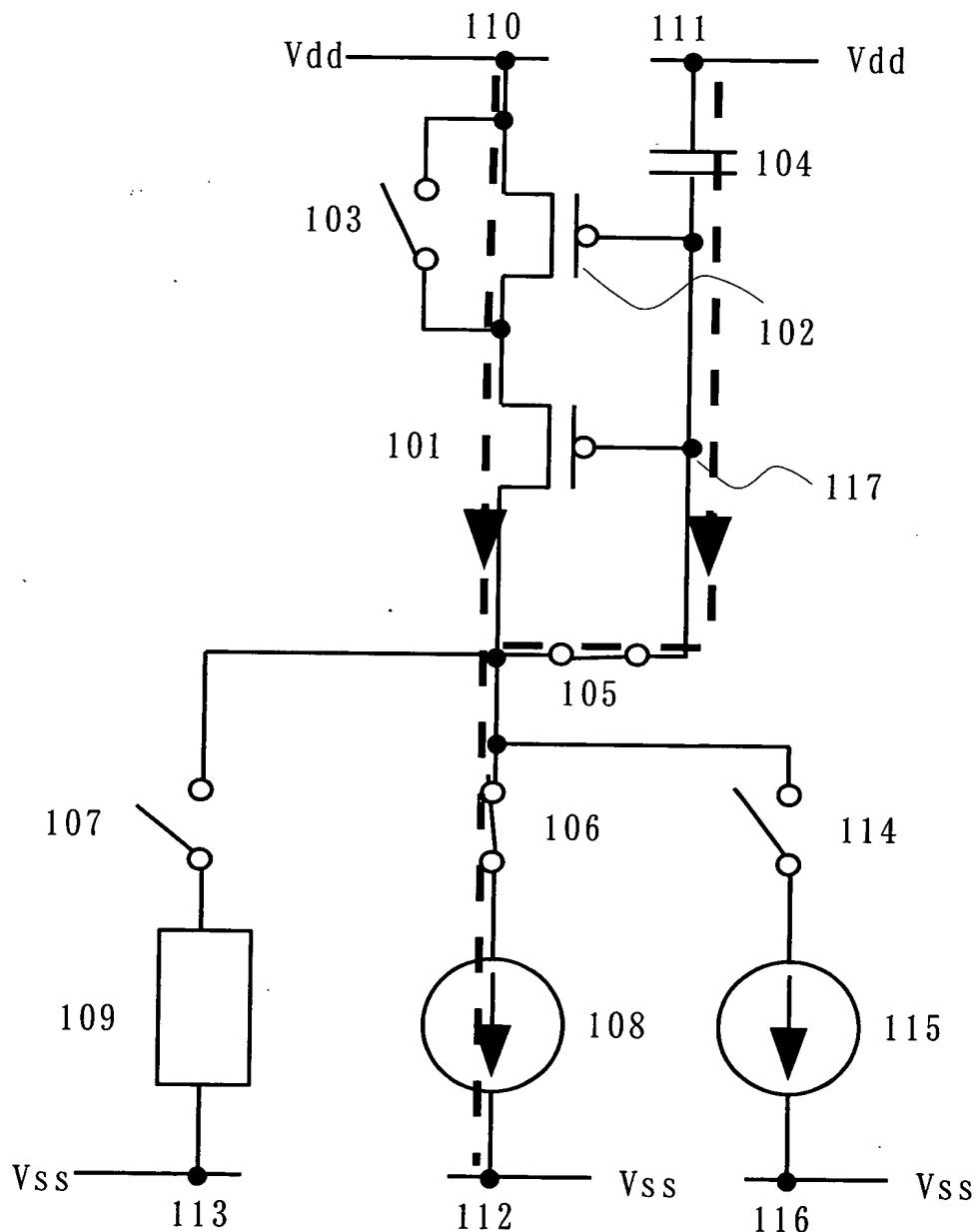


FIG. 3

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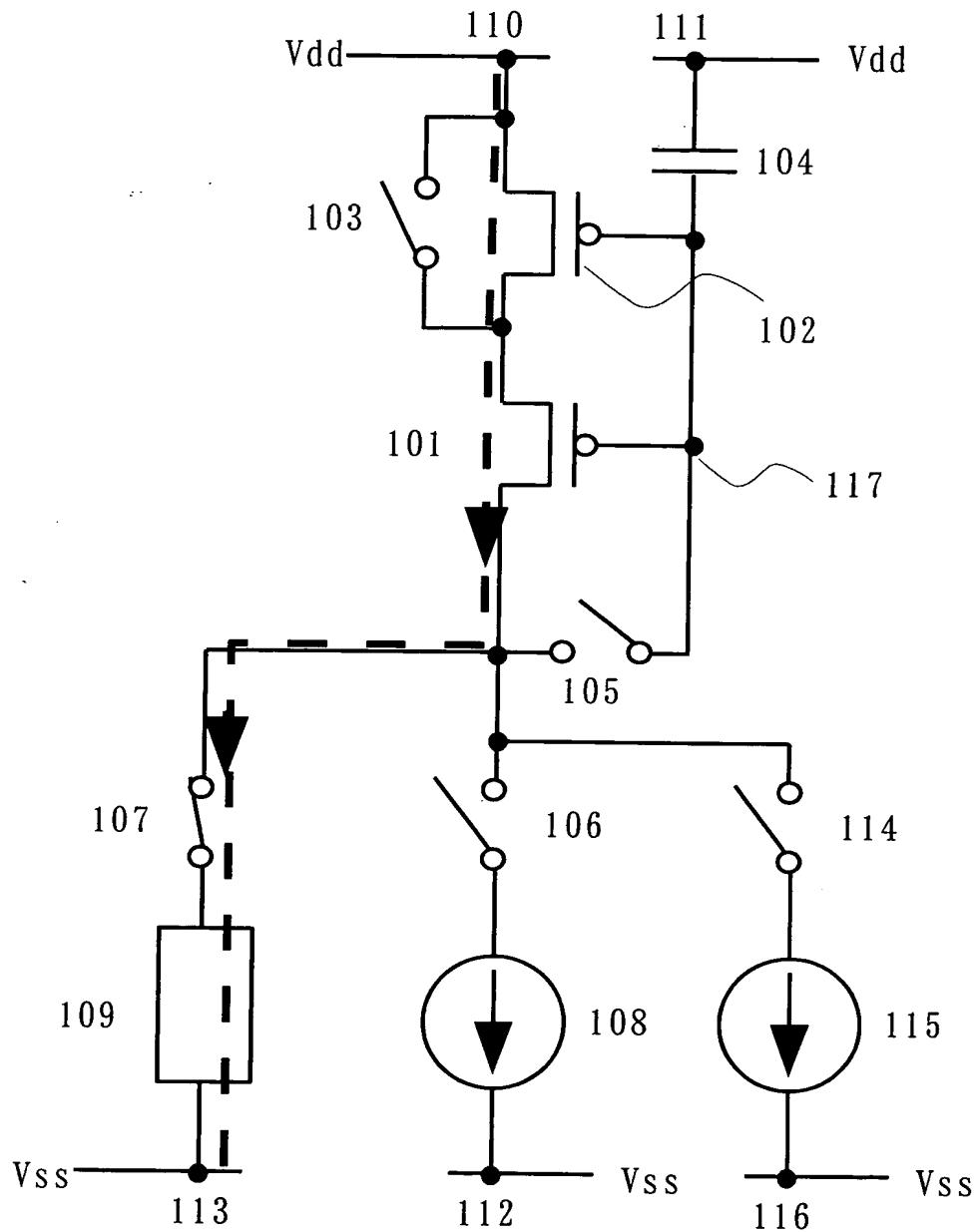
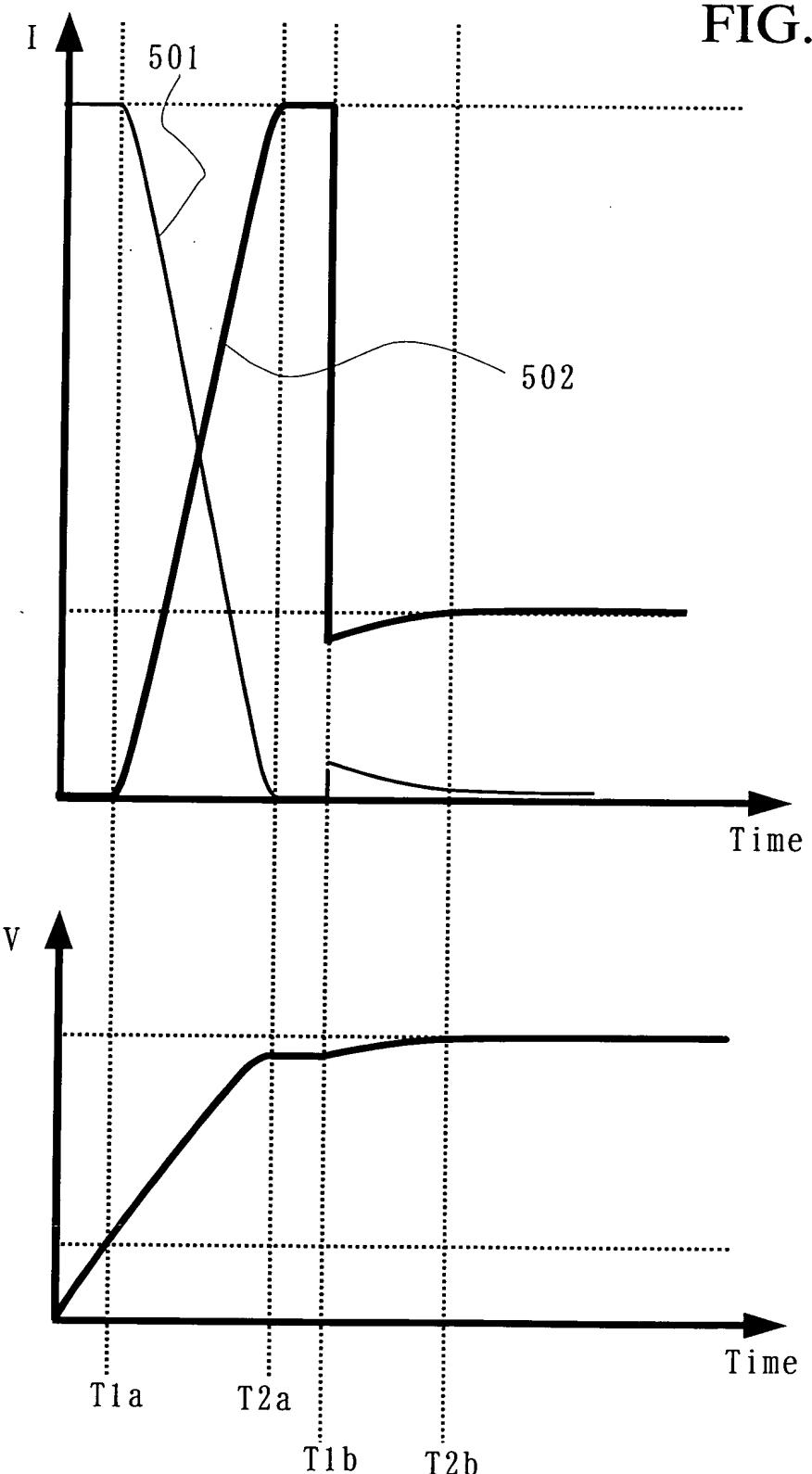


FIG. 4

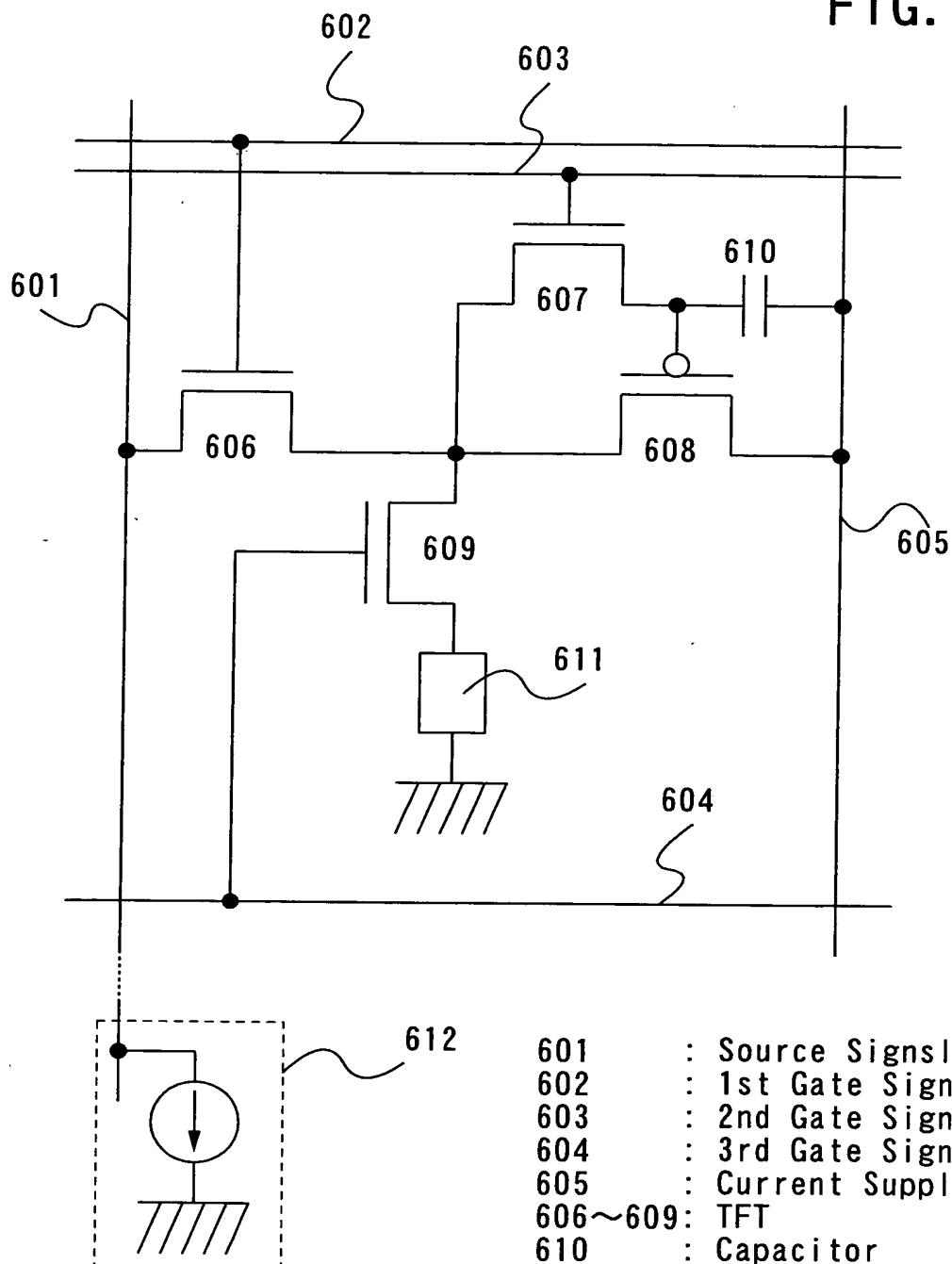
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FIG. 5



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FIG. 6

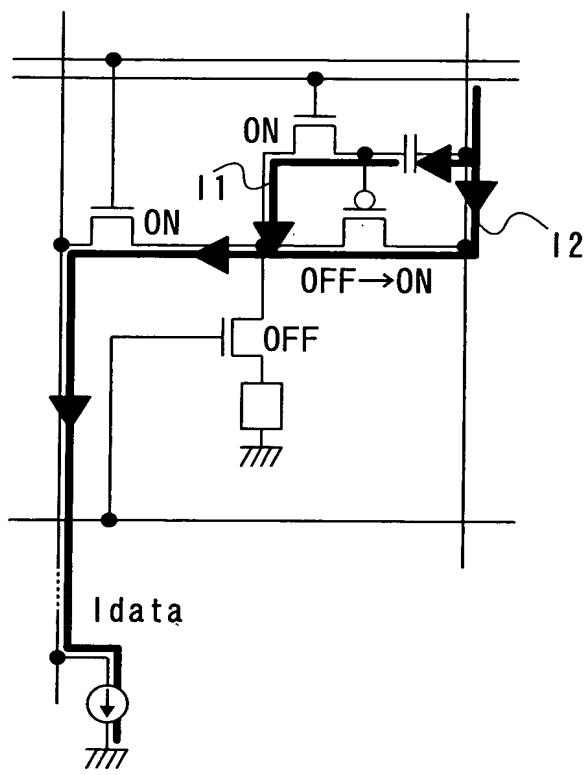


- | | |
|---------|---|
| 601 | : Source Signal Line |
| 602 | : 1st Gate Signal Line |
| 603 | : 2nd Gate Signal Line |
| 604 | : 3rd Gate Signal Line |
| 605 | : Current Supply Line |
| 606~609 | : TFT |
| 610 | : Capacitor |
| 611 | : EL Element |
| 612 | : Current Source
for Inputting
a Signal Current |

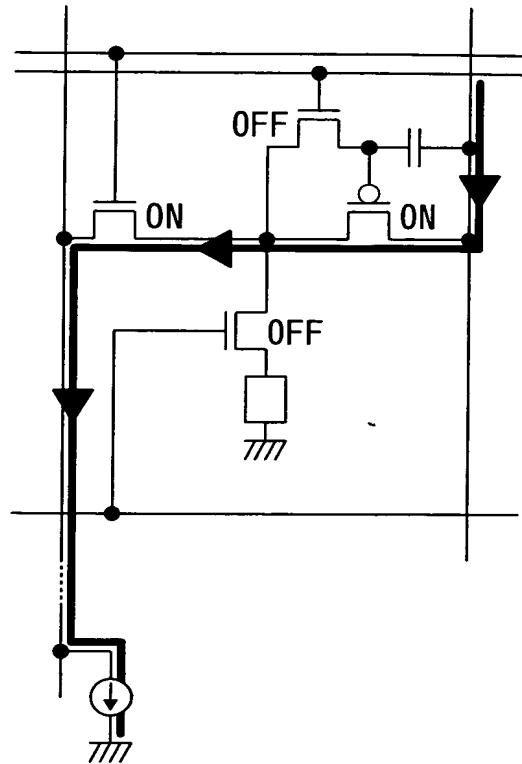
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FIG. 7

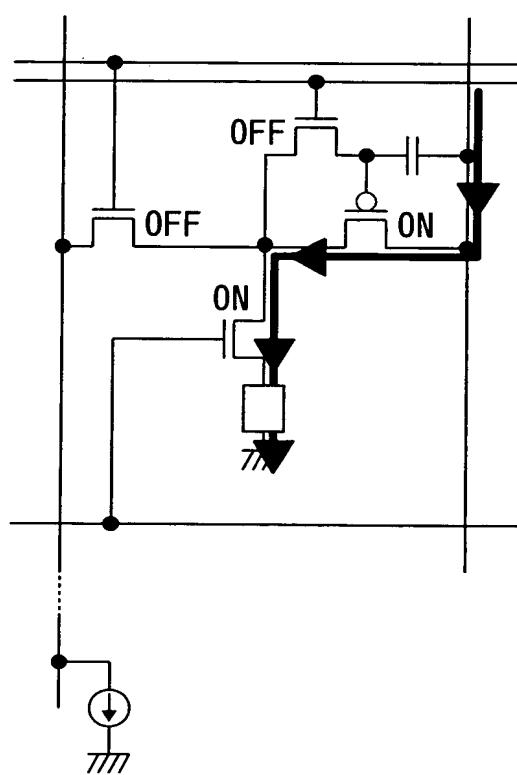
(A) SIGNAL INPUTTING PERIOD



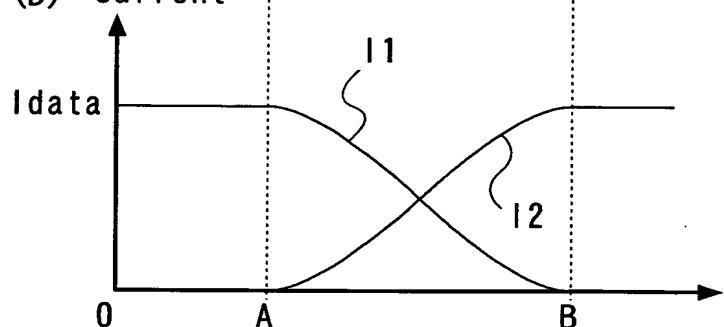
(B) SIGNAL INPUTTING COMPLETED PERIOD



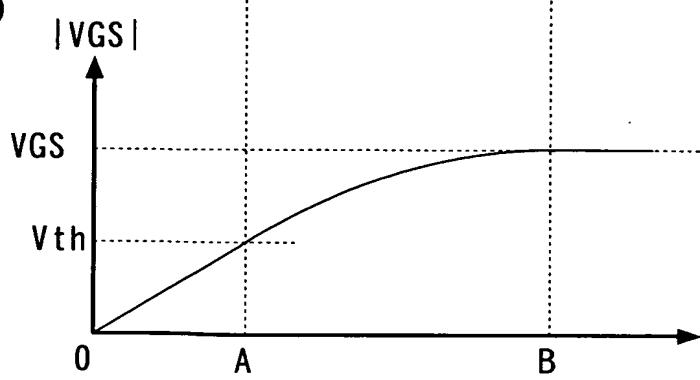
(C) LIGHT EMITTING PERIOD



(D) Current



(E)



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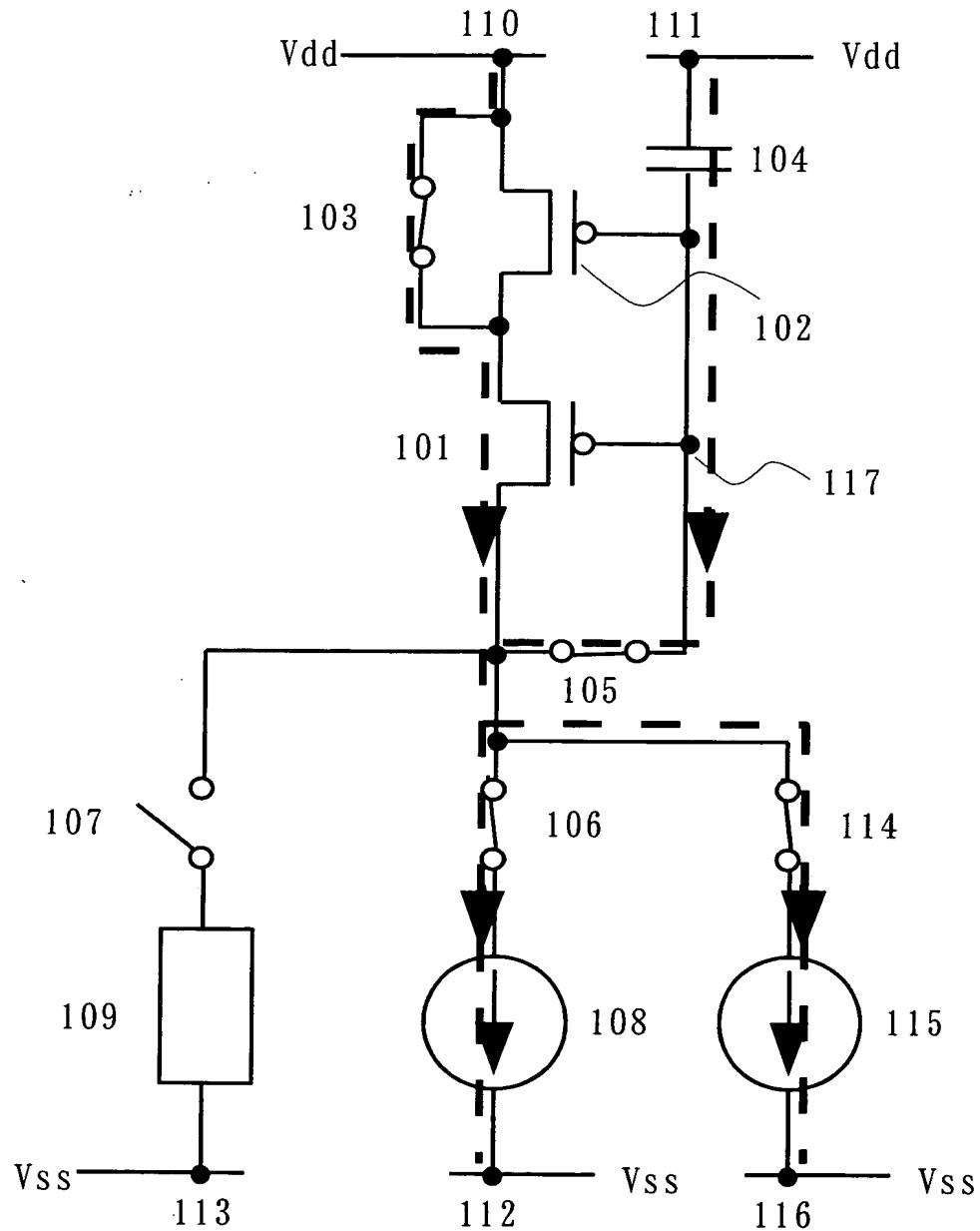


FIG. 8

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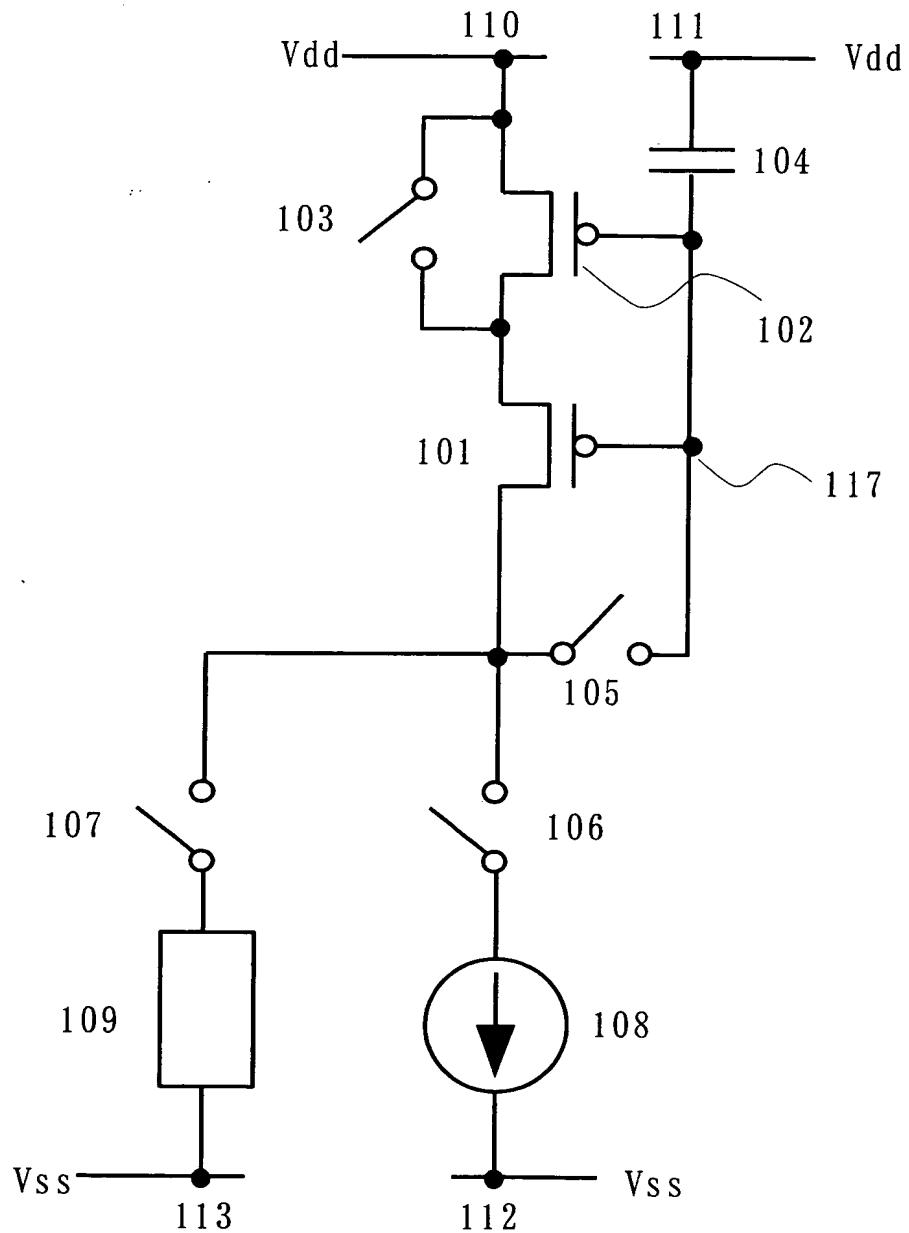


FIG. 9

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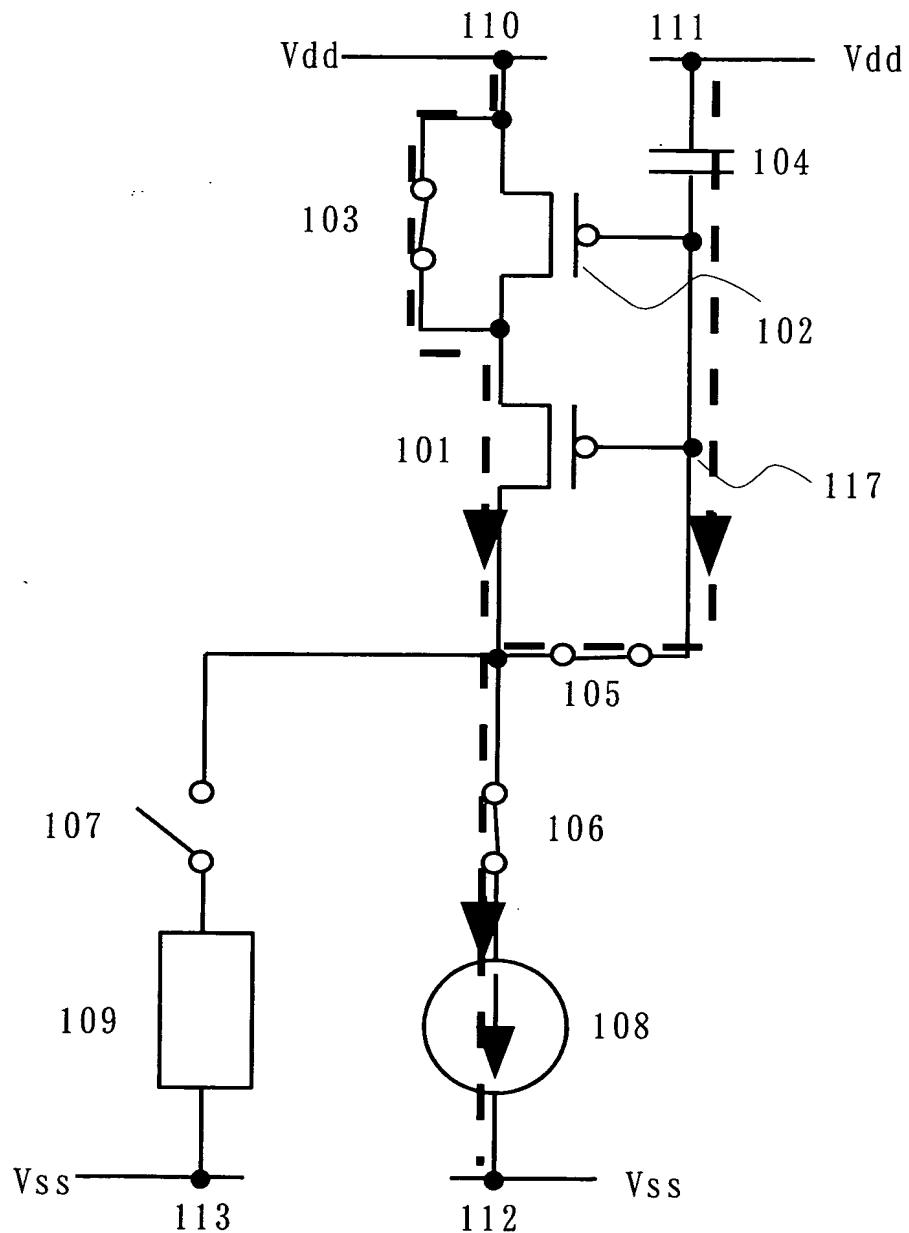


FIG. 10

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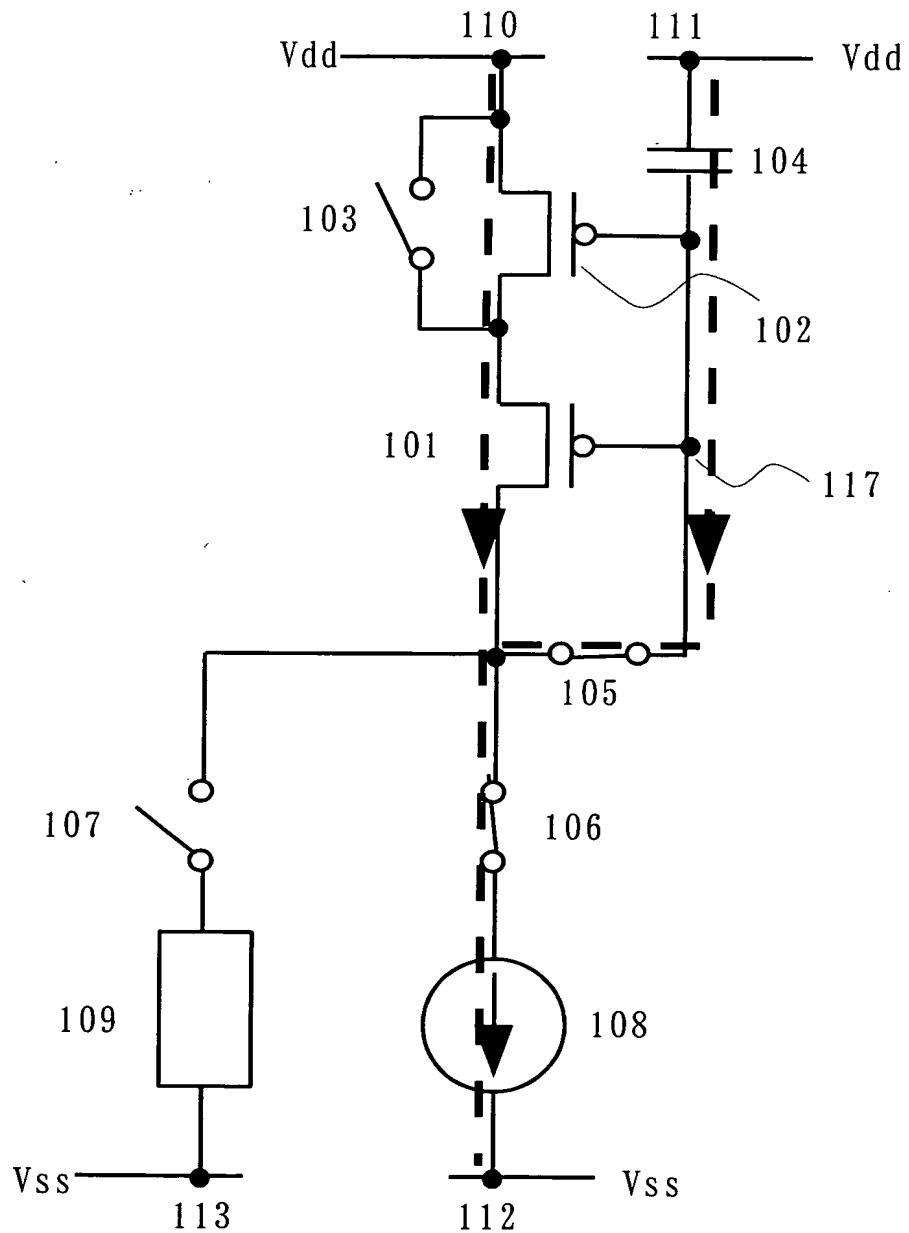


FIG. 11

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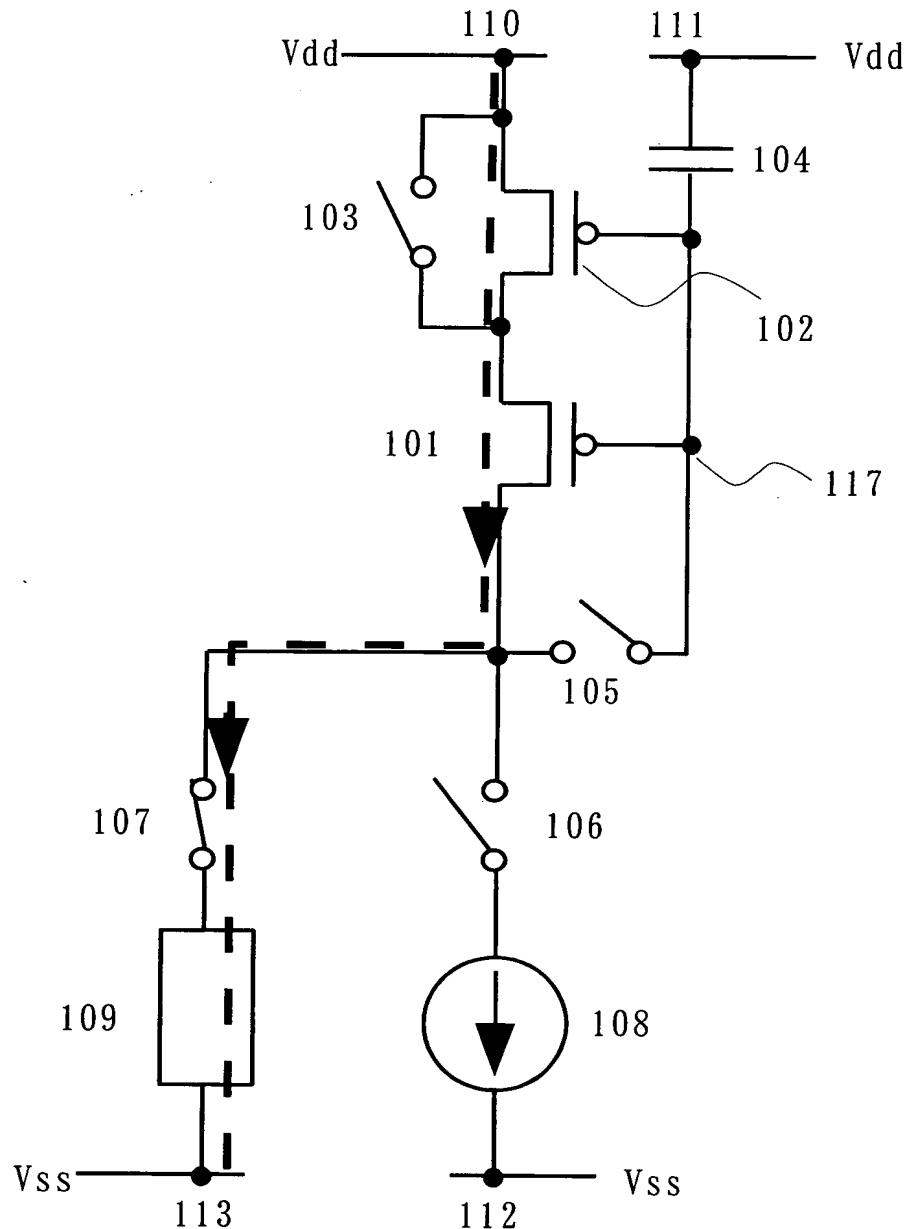


FIG. 12

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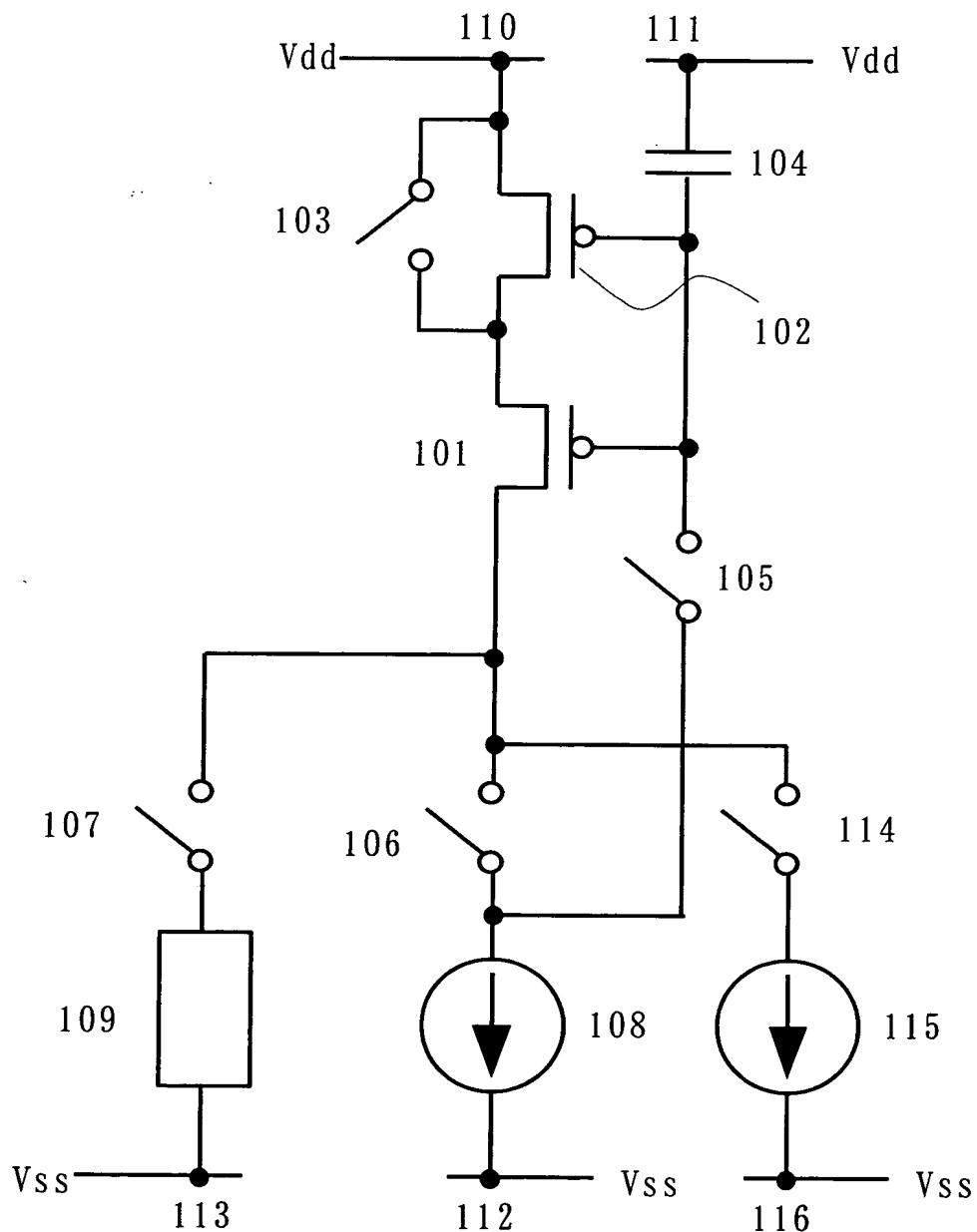


FIG. 13

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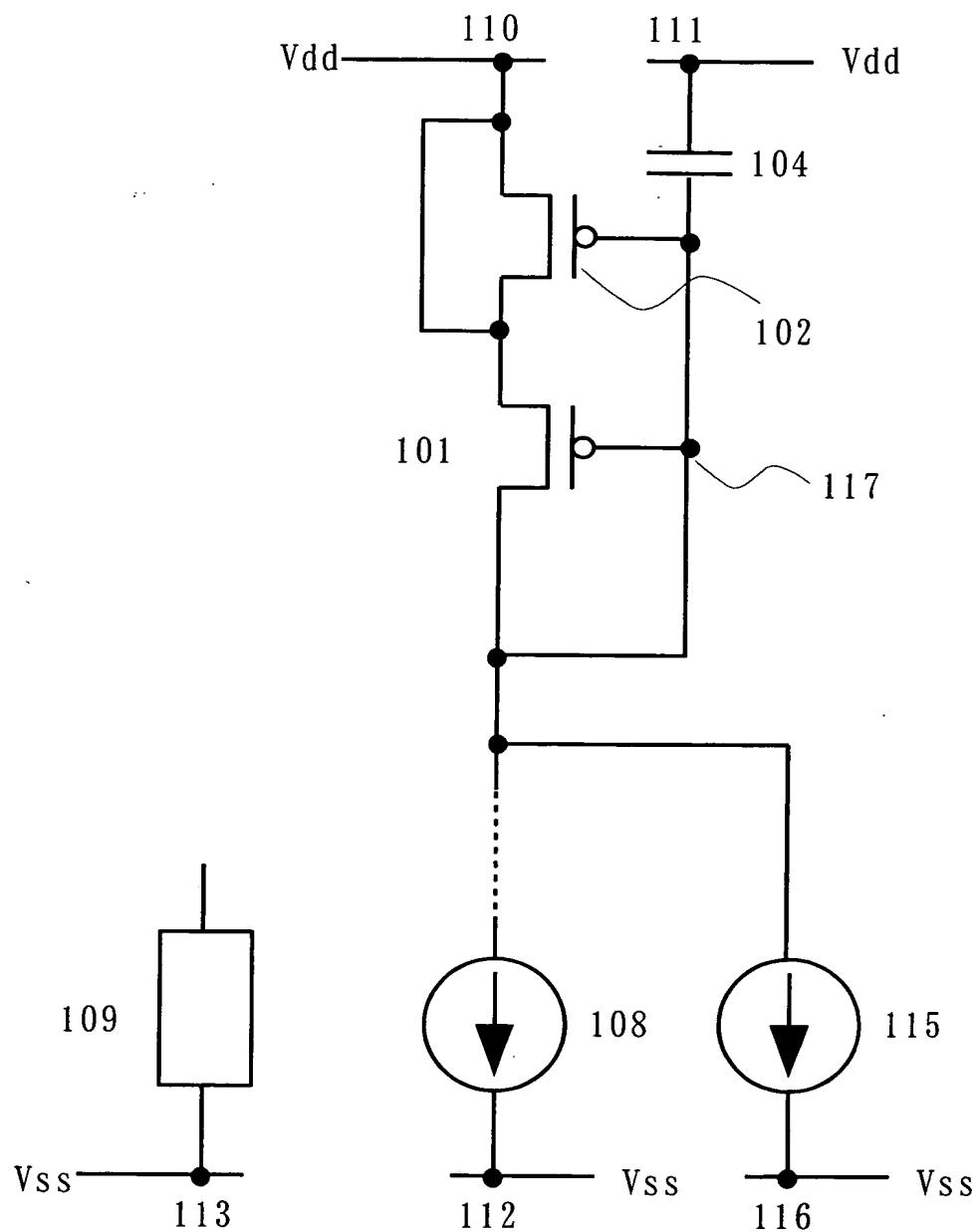


FIG. 14

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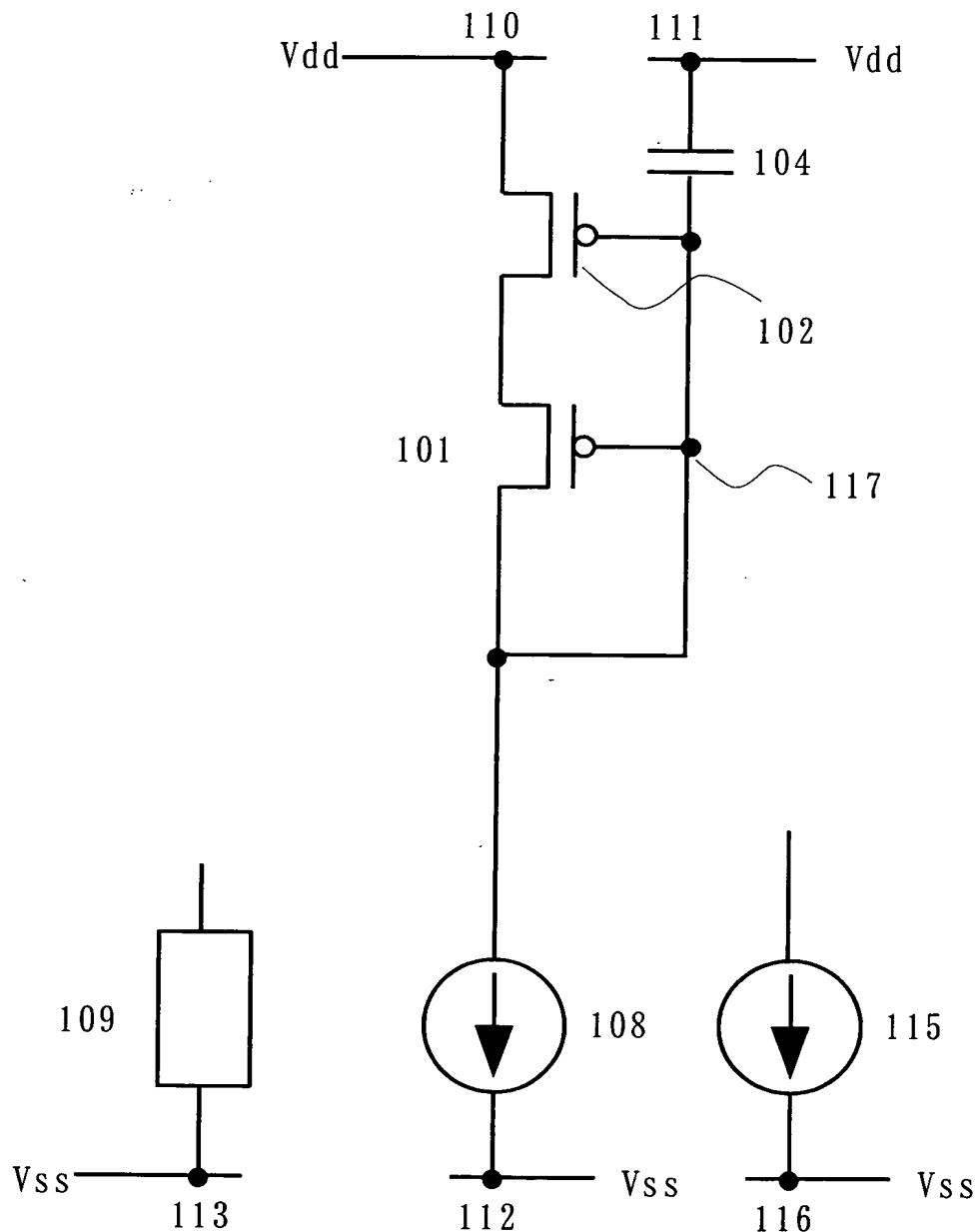


FIG. 15

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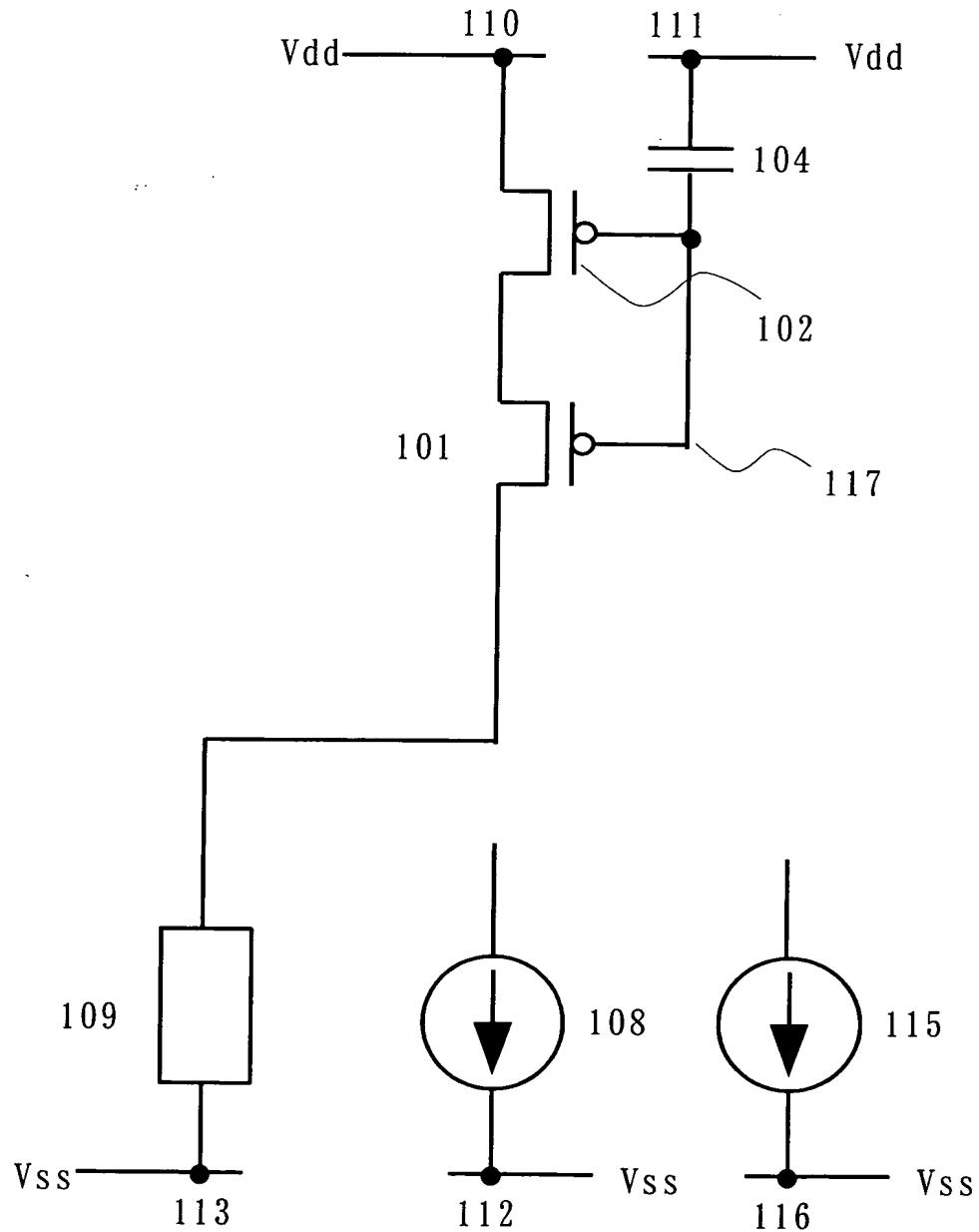


FIG. 16

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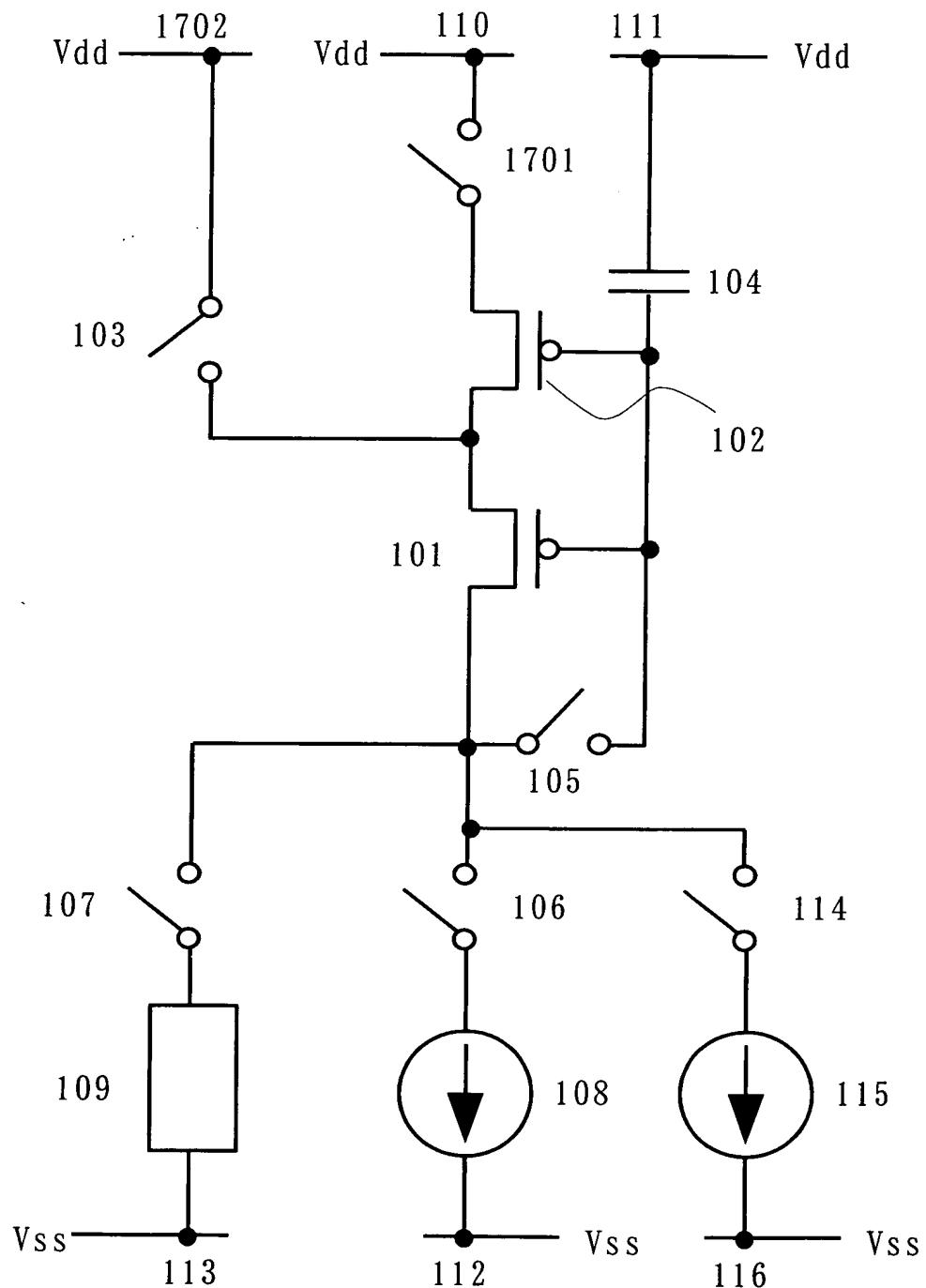


FIG. 17

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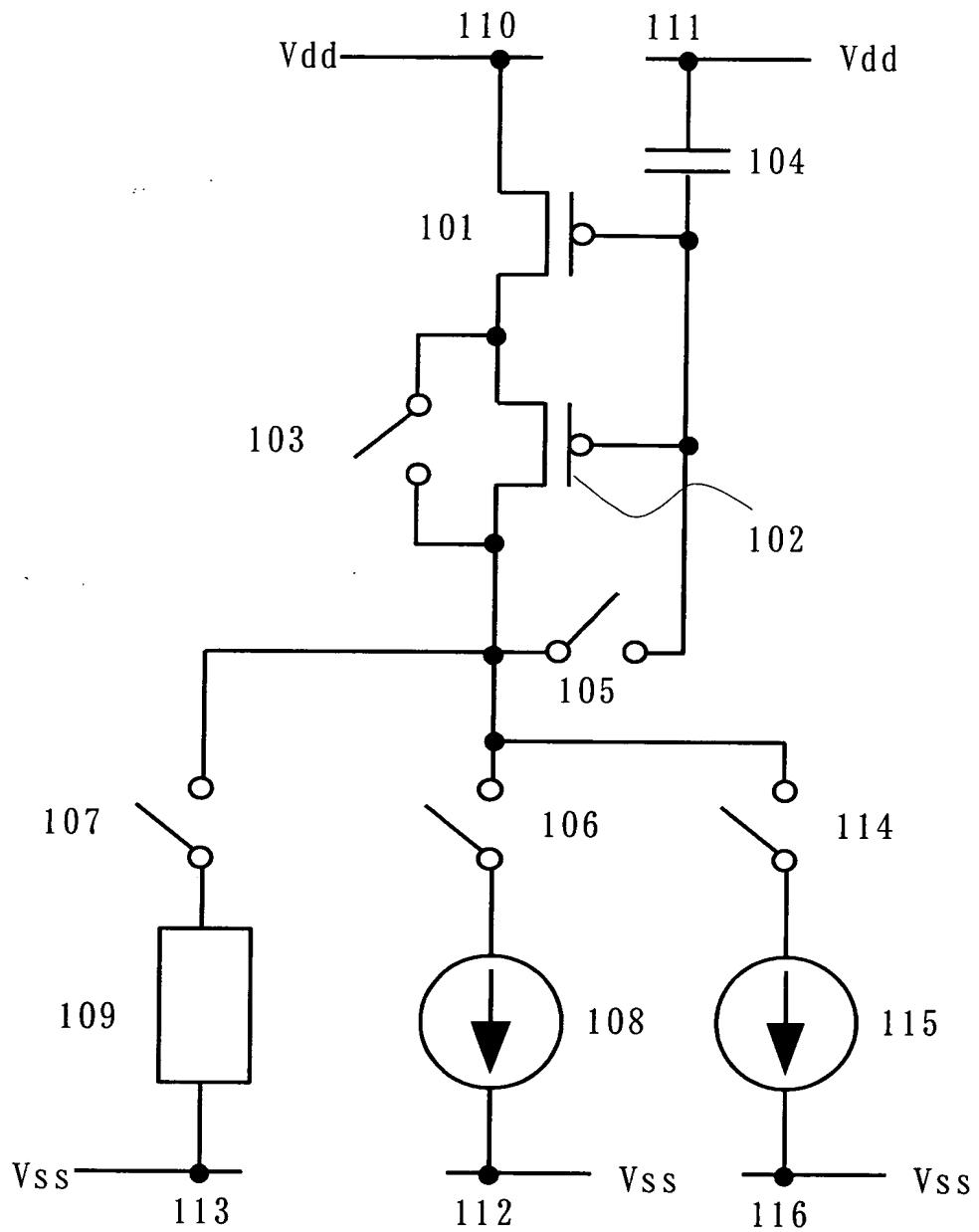


FIG. 18

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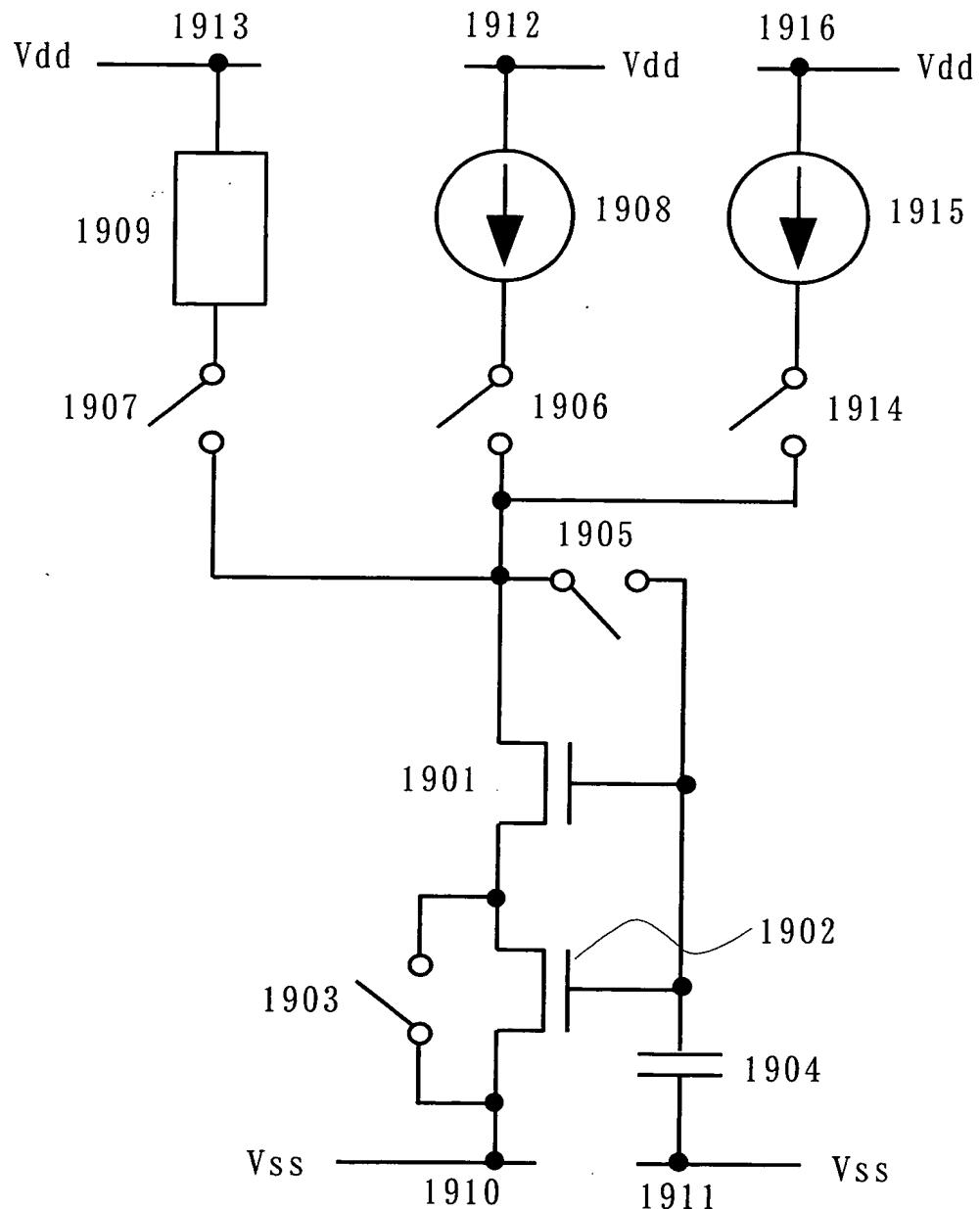


FIG. 19

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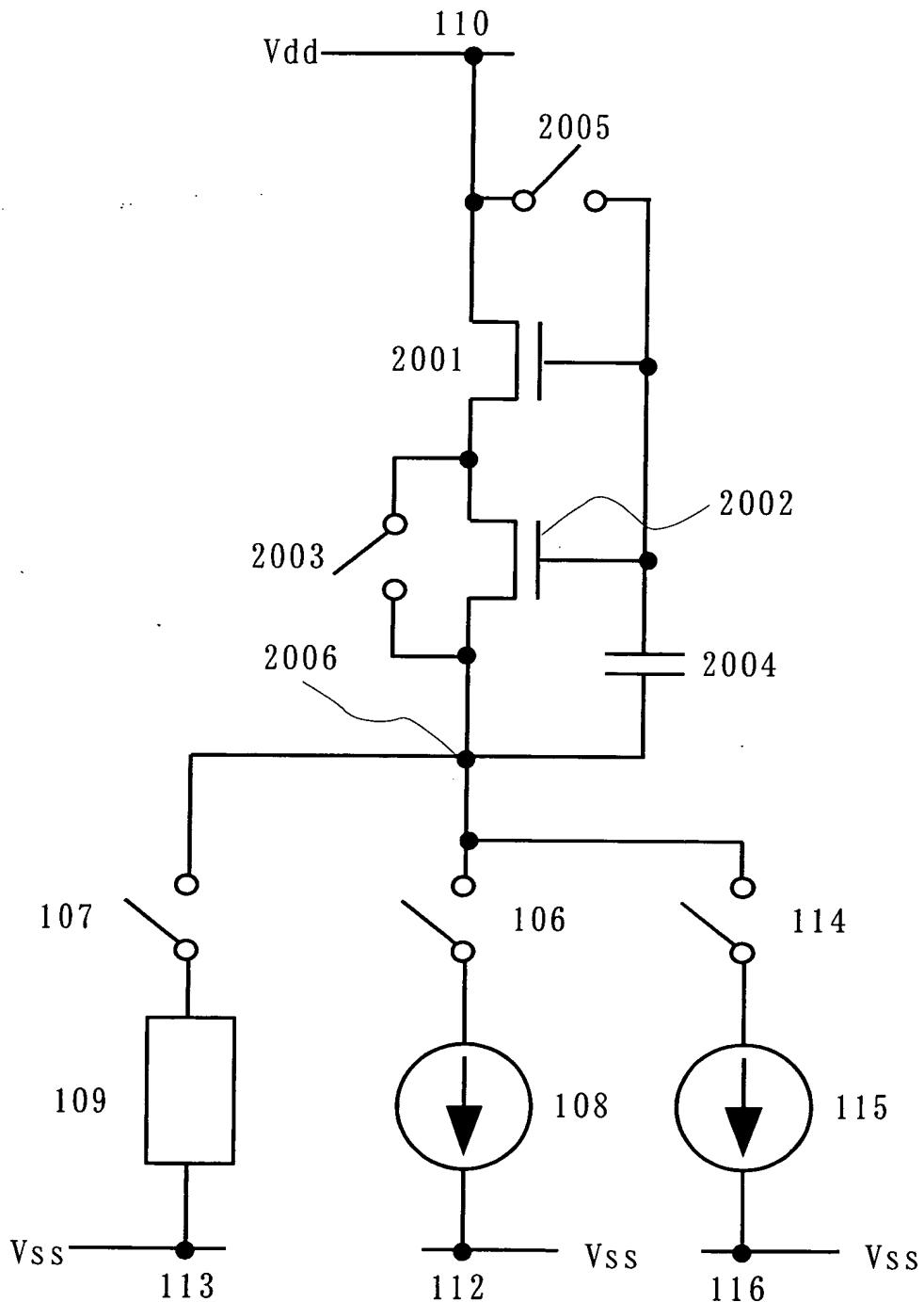


FIG. 20

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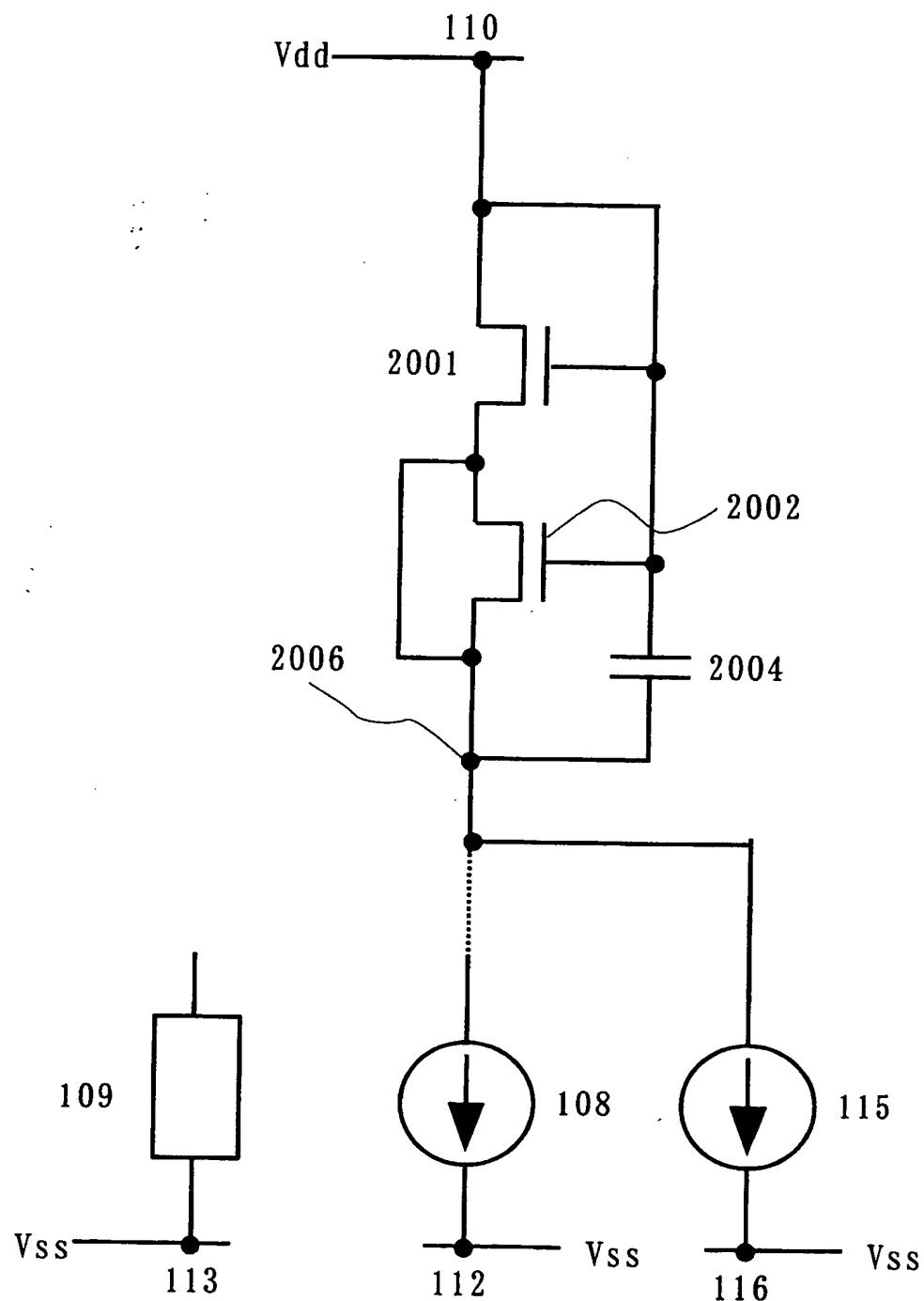


FIG. 21

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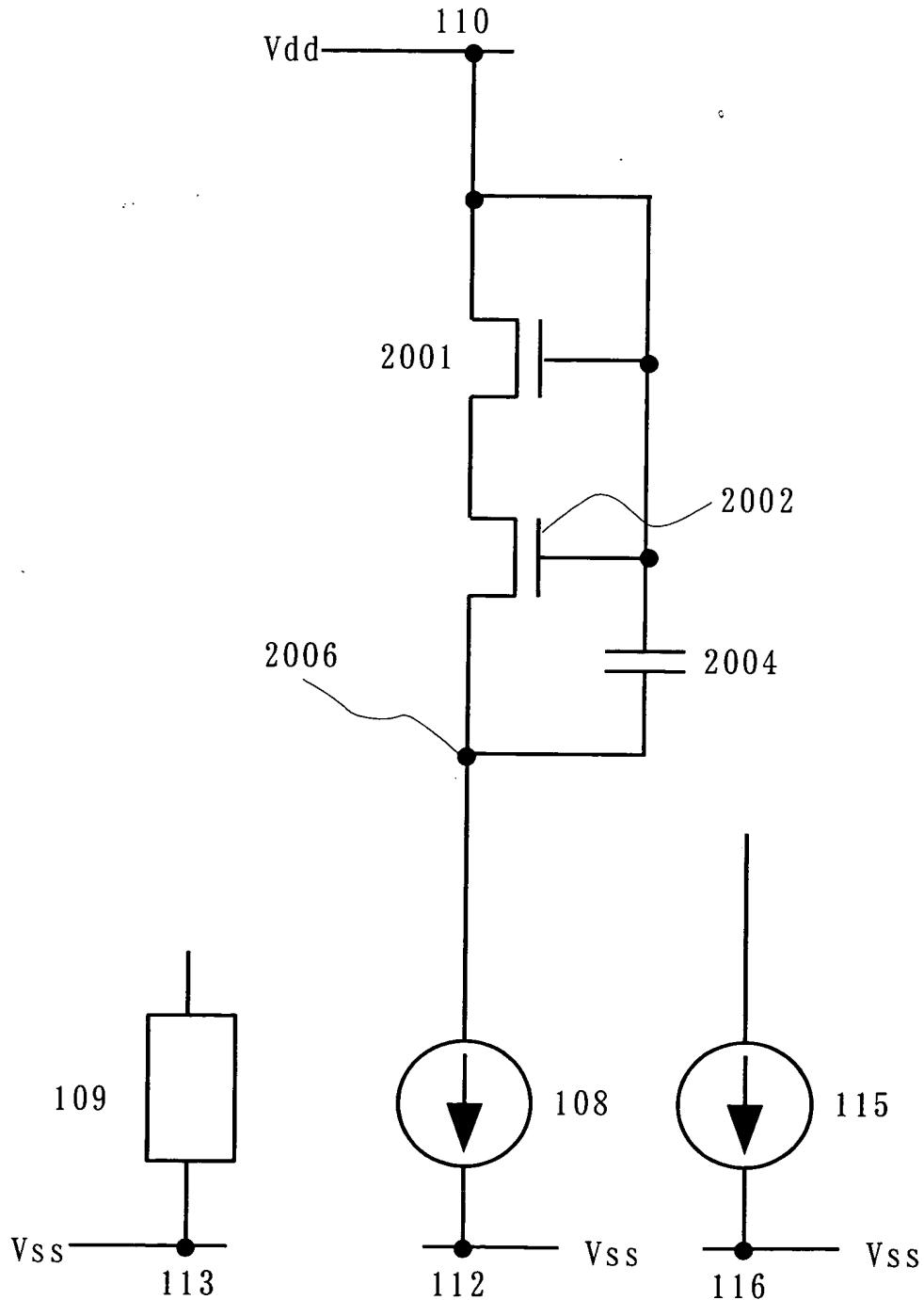


FIG. 22

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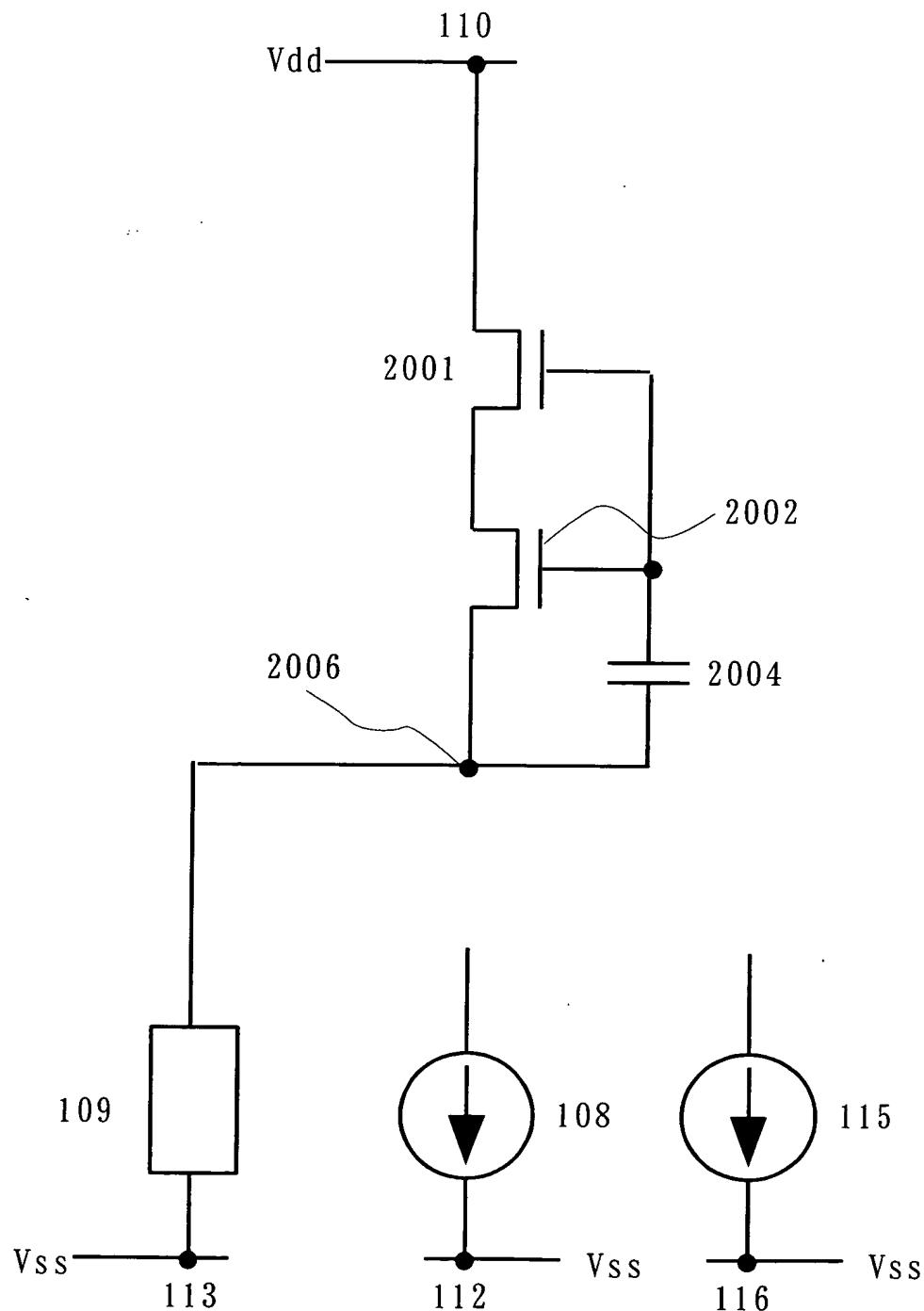


FIG. 23

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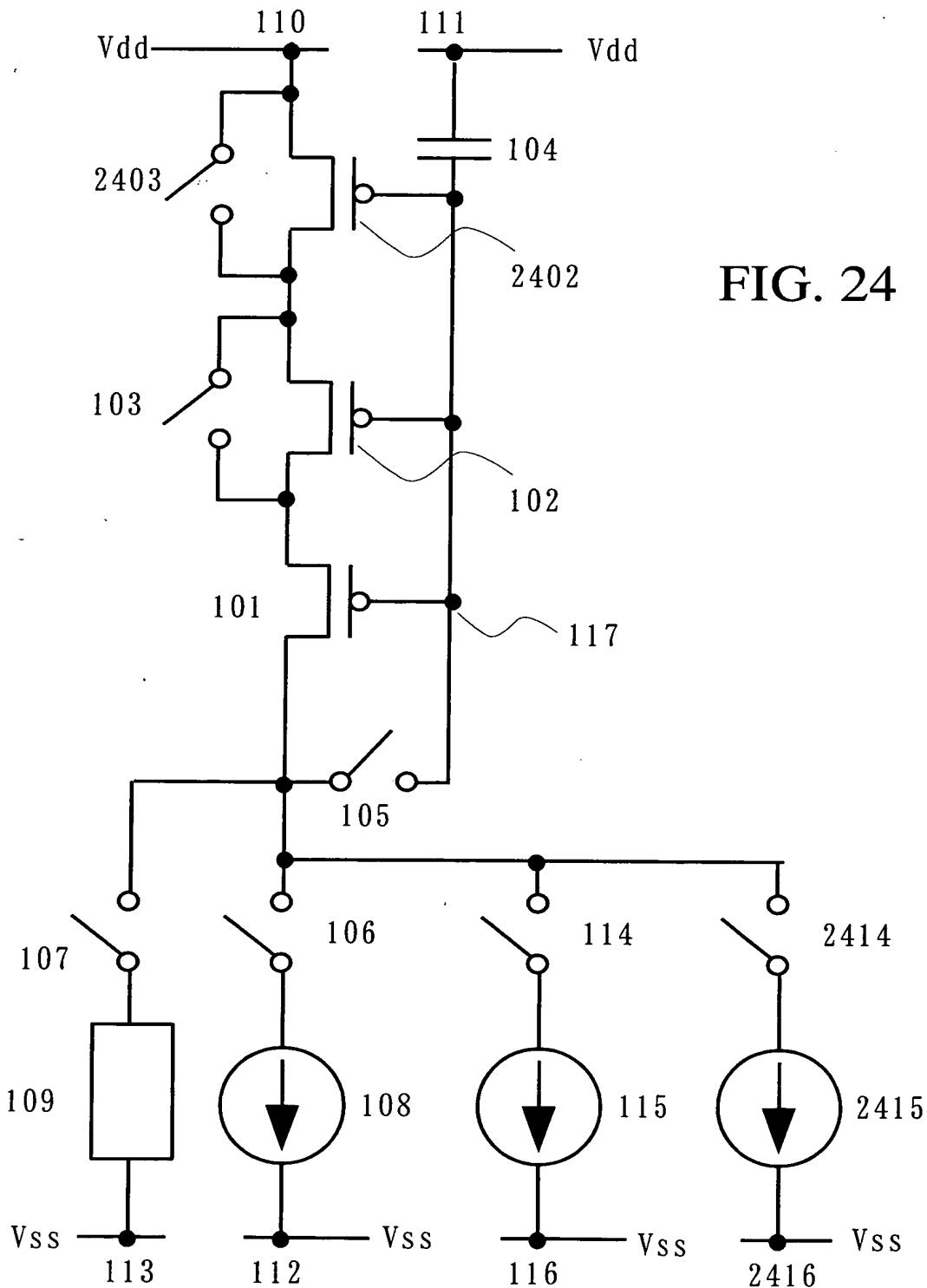


FIG. 24

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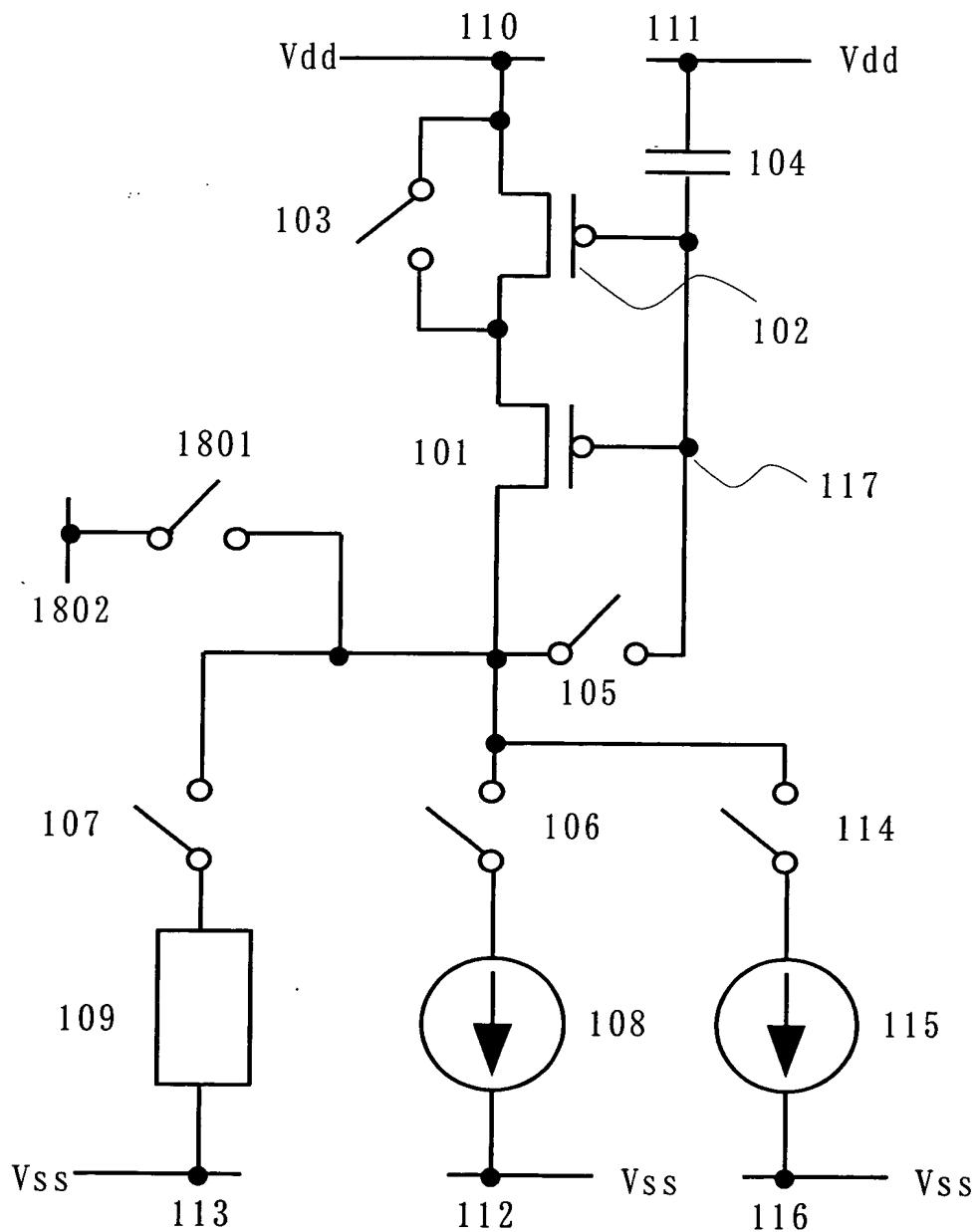


FIG. 25

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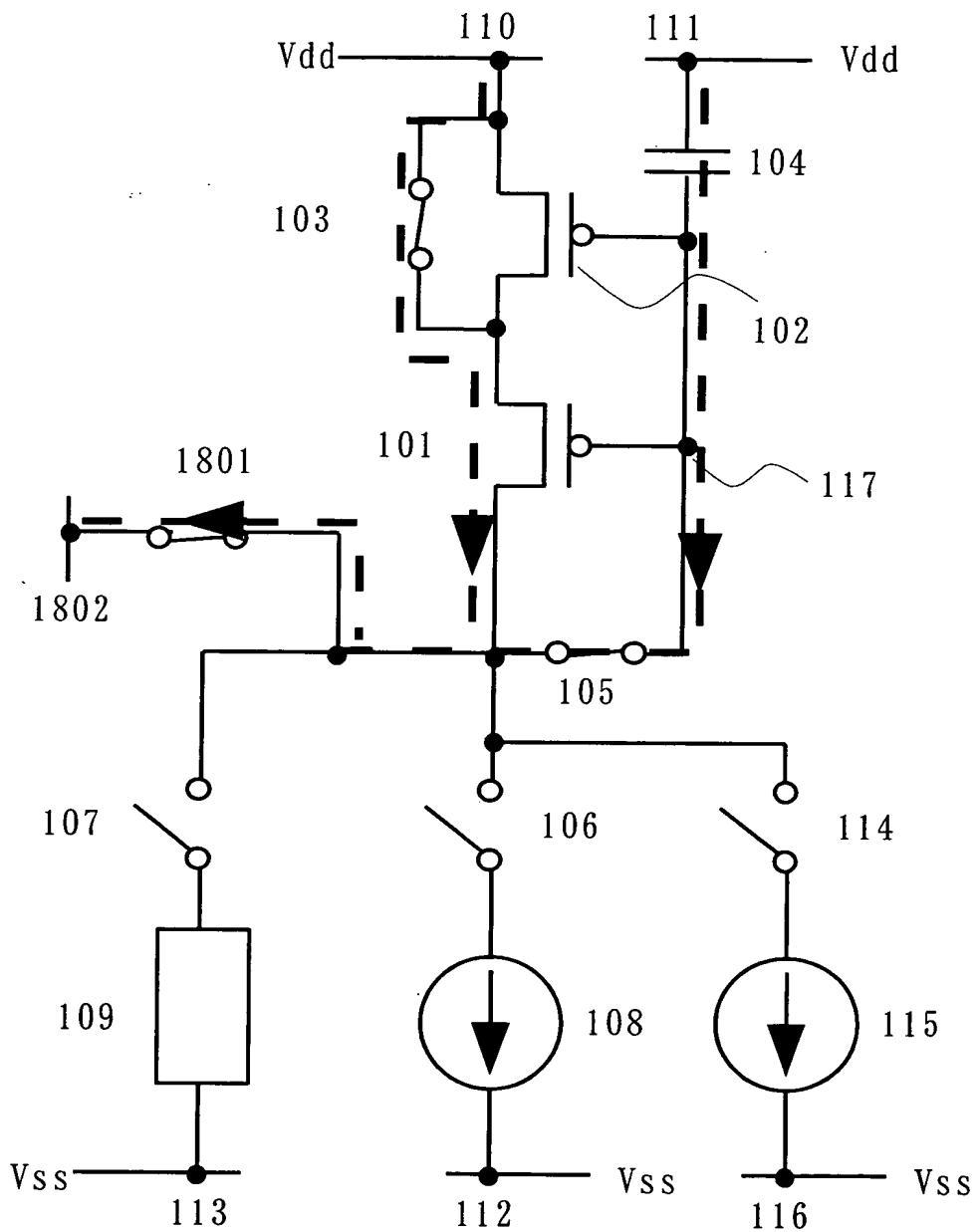


FIG. 26

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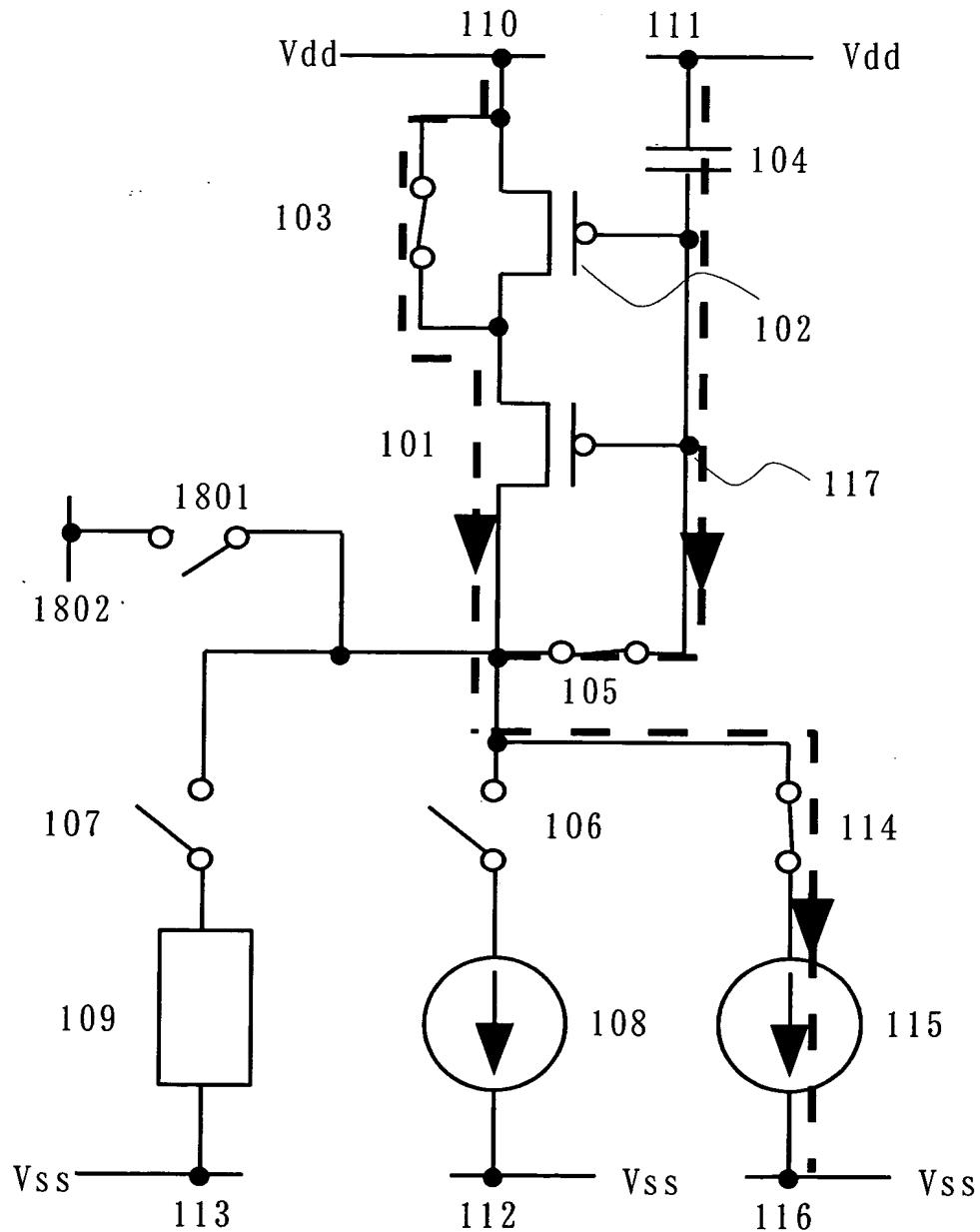


FIG. 27

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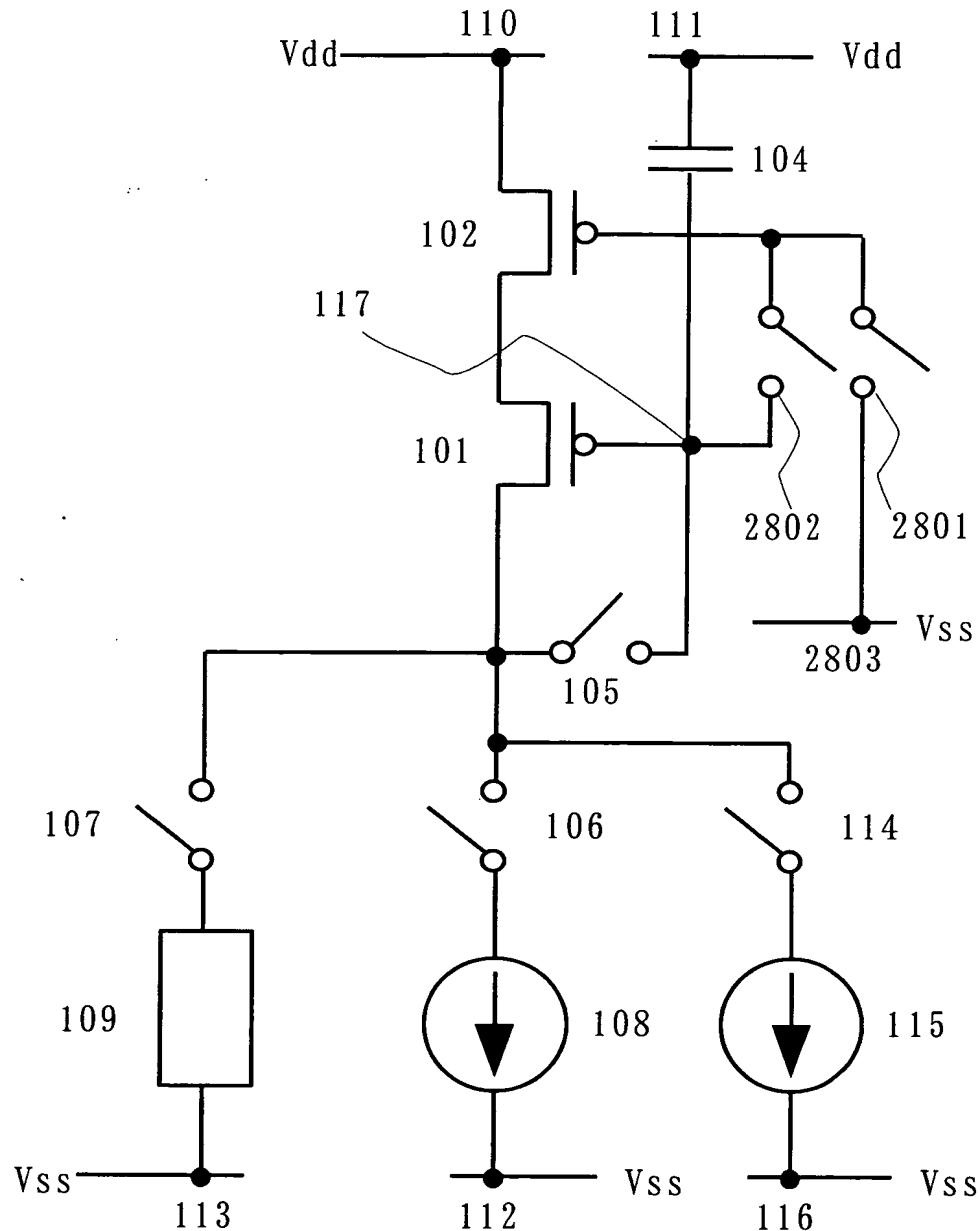


FIG. 28

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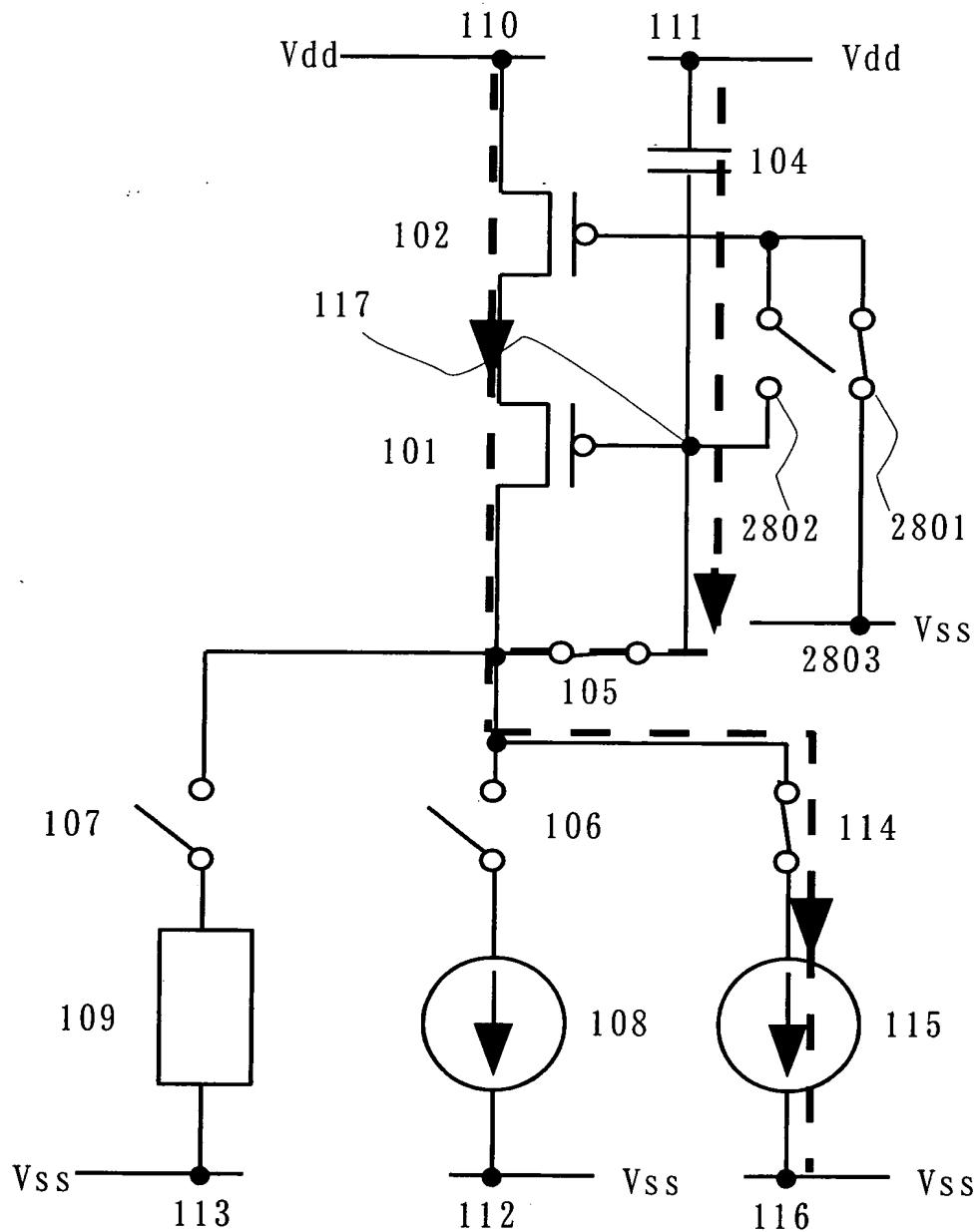


FIG. 29

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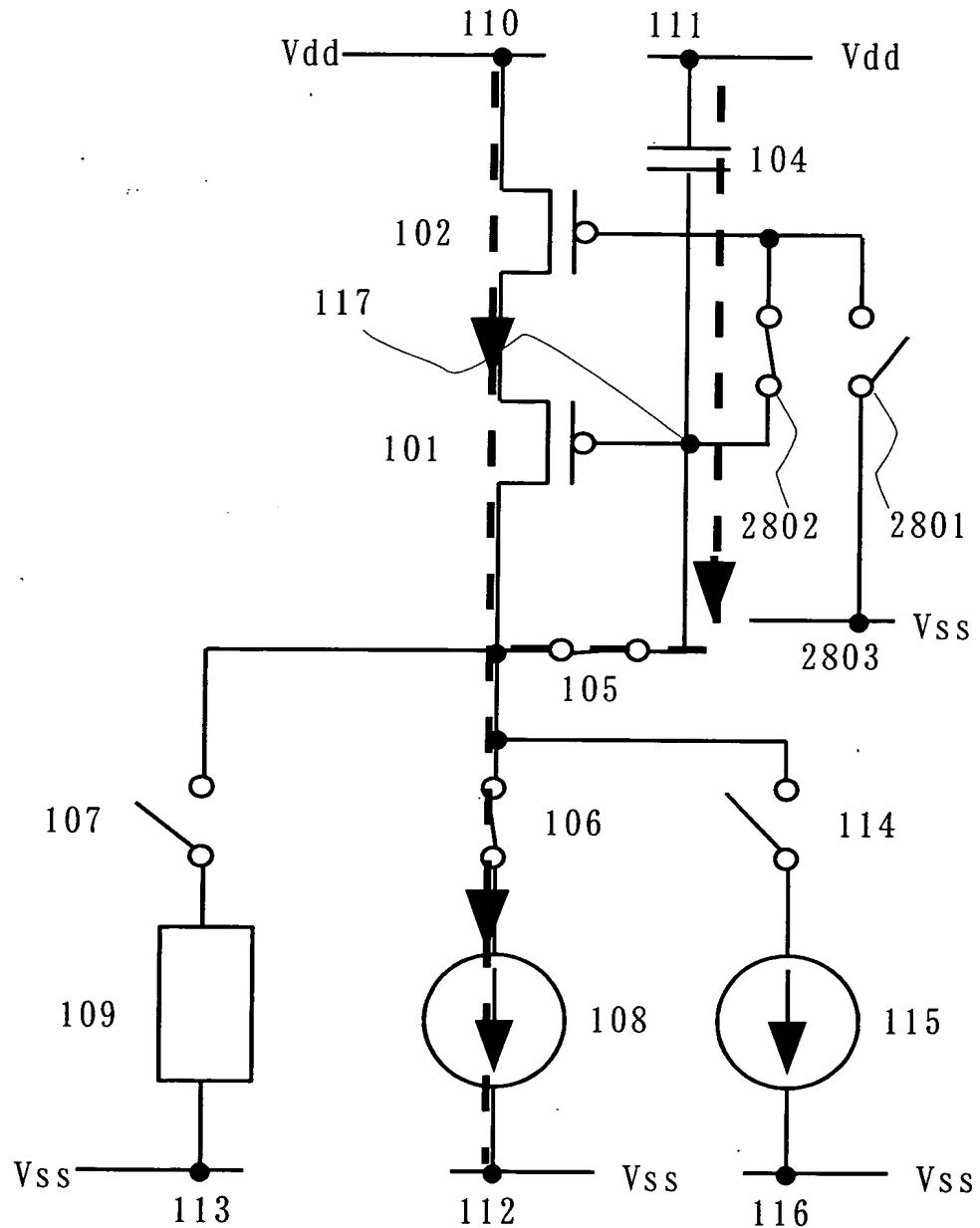


FIG. 30

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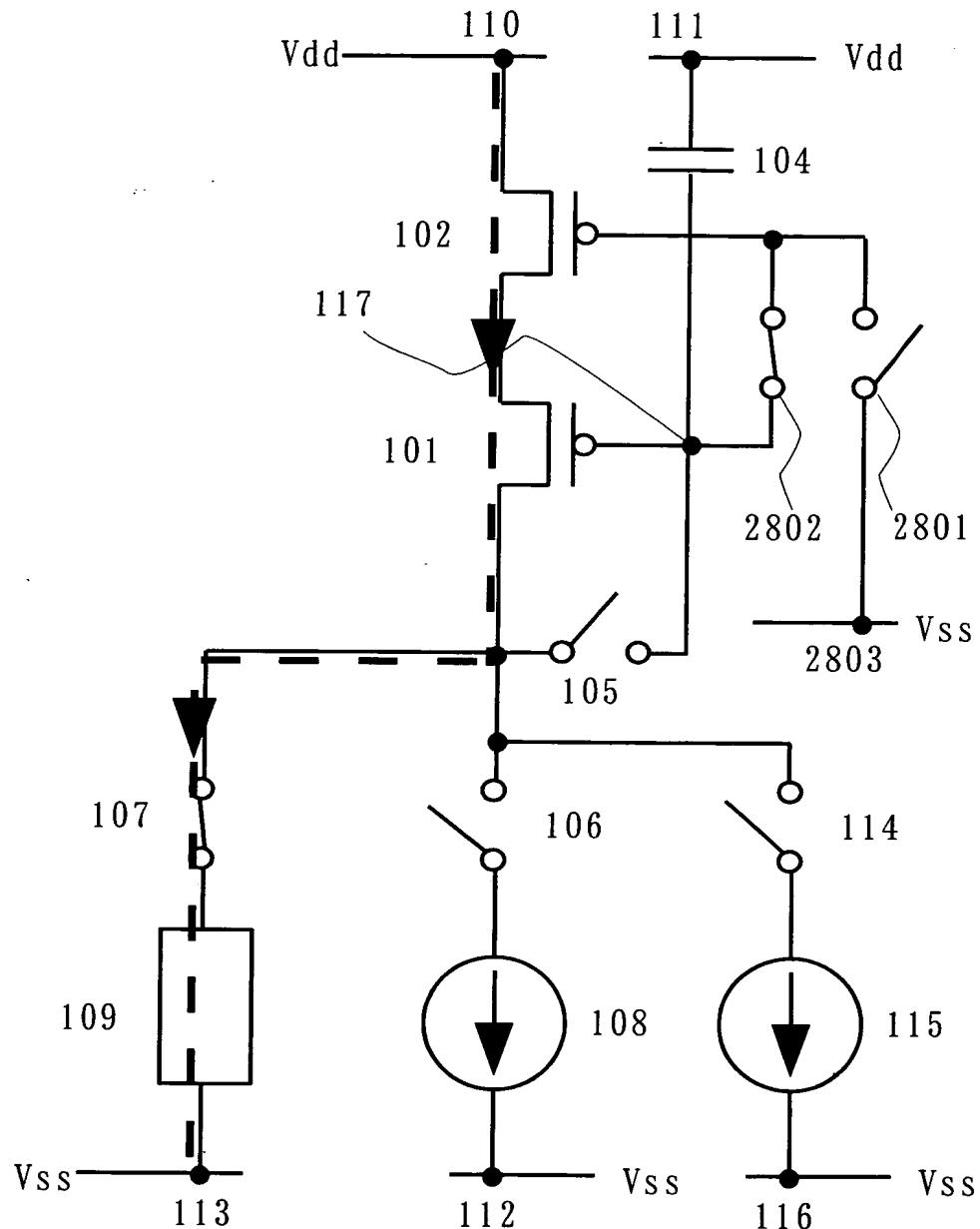


FIG. 31

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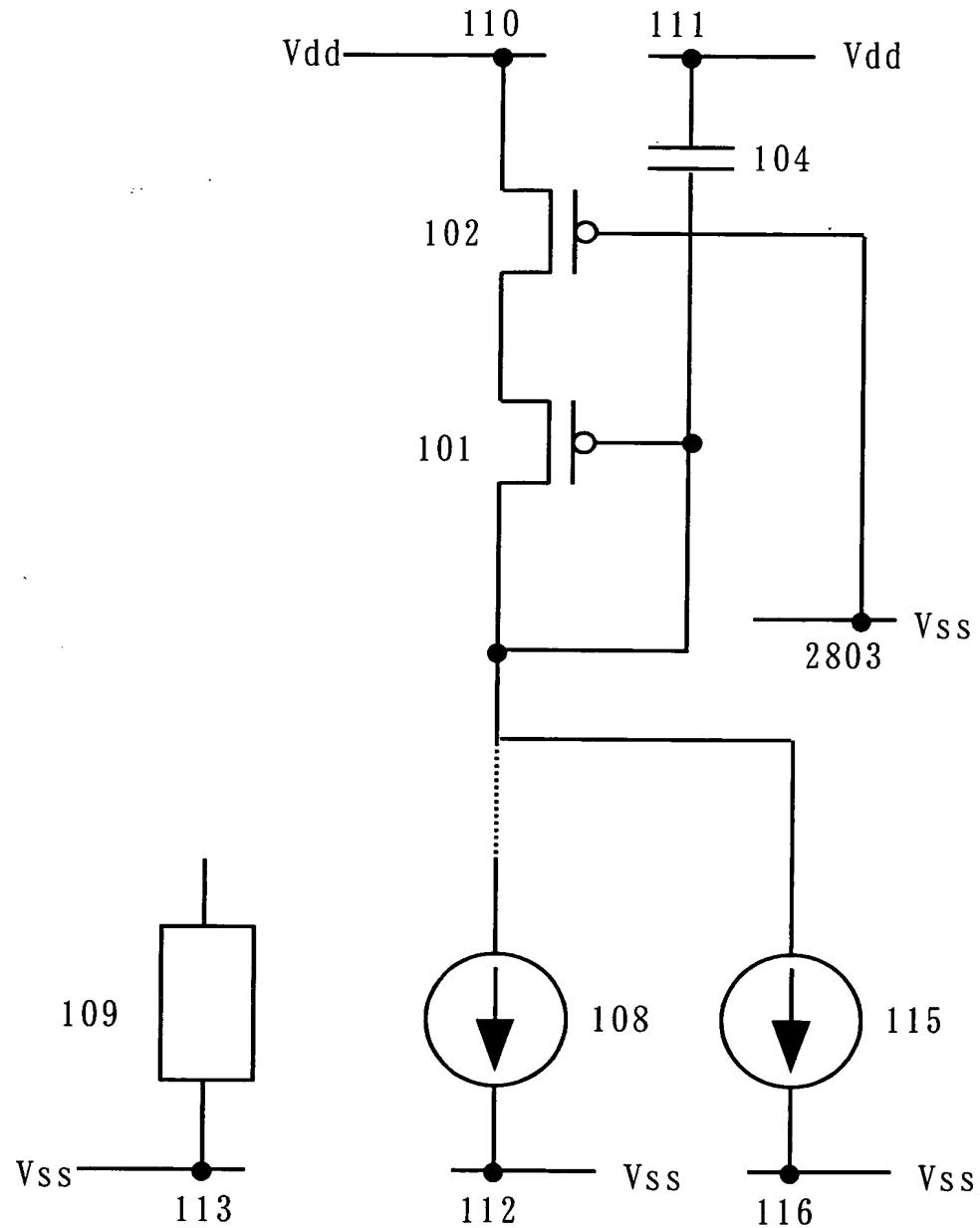


FIG. 32

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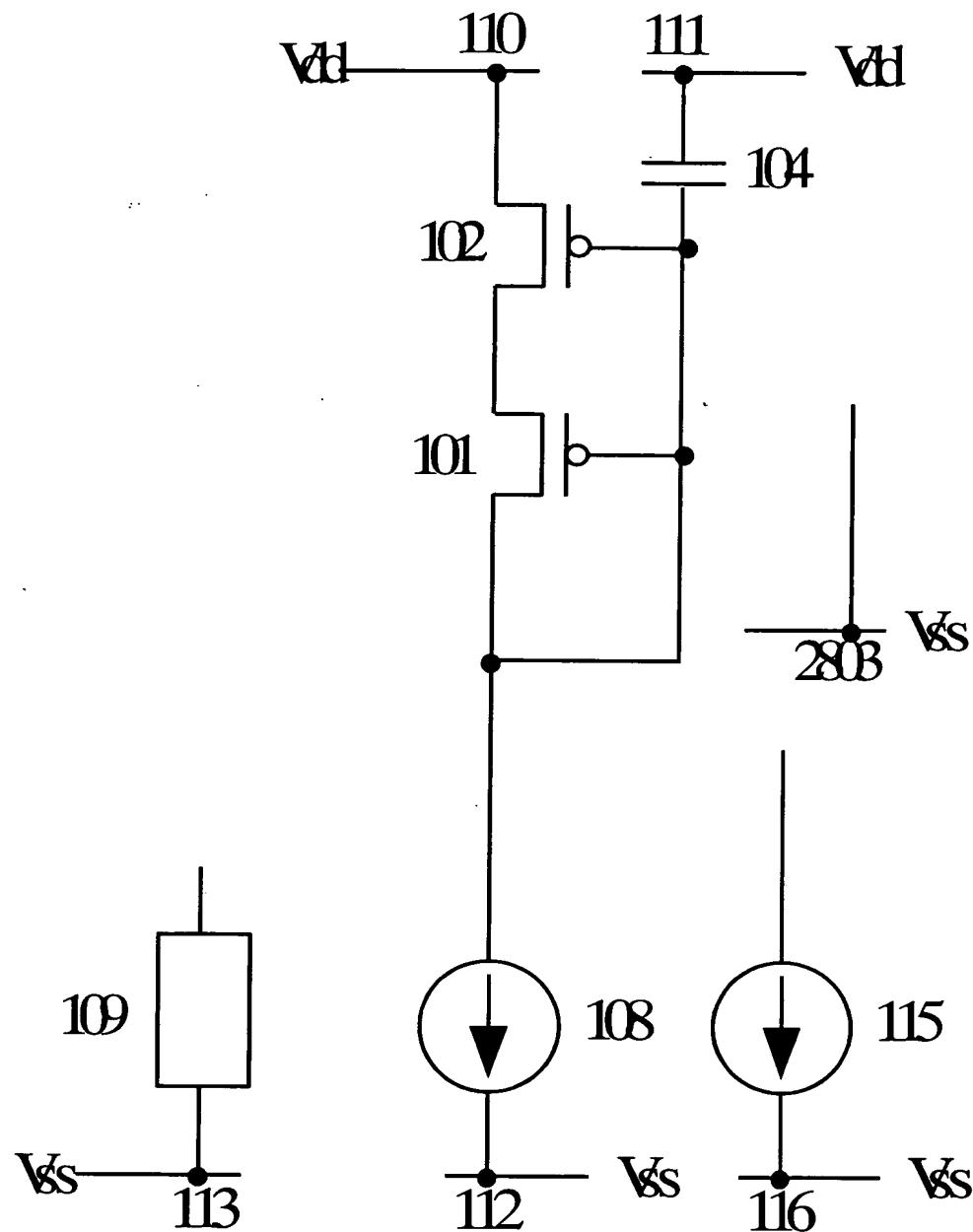


FIG. 33

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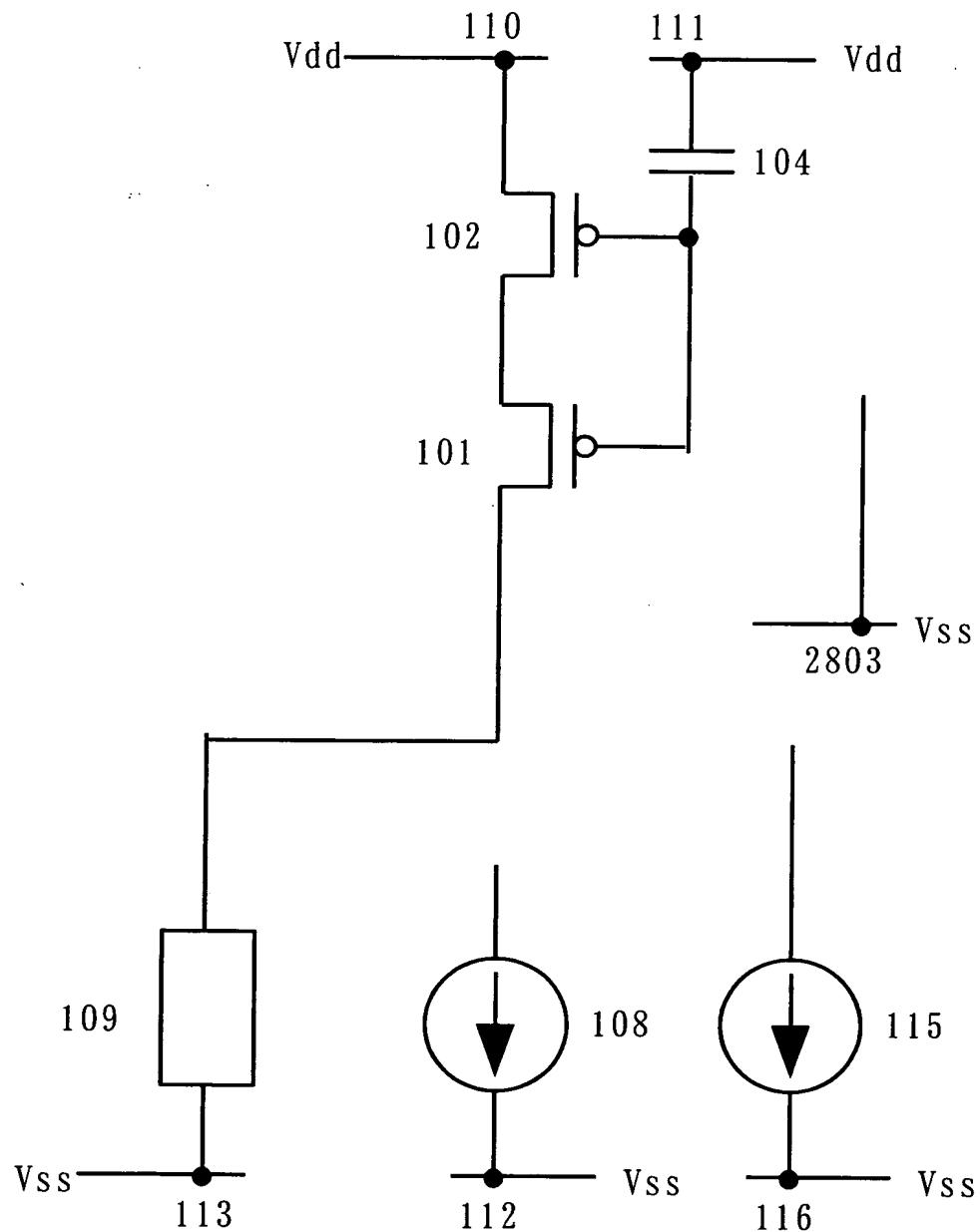


FIG. 34

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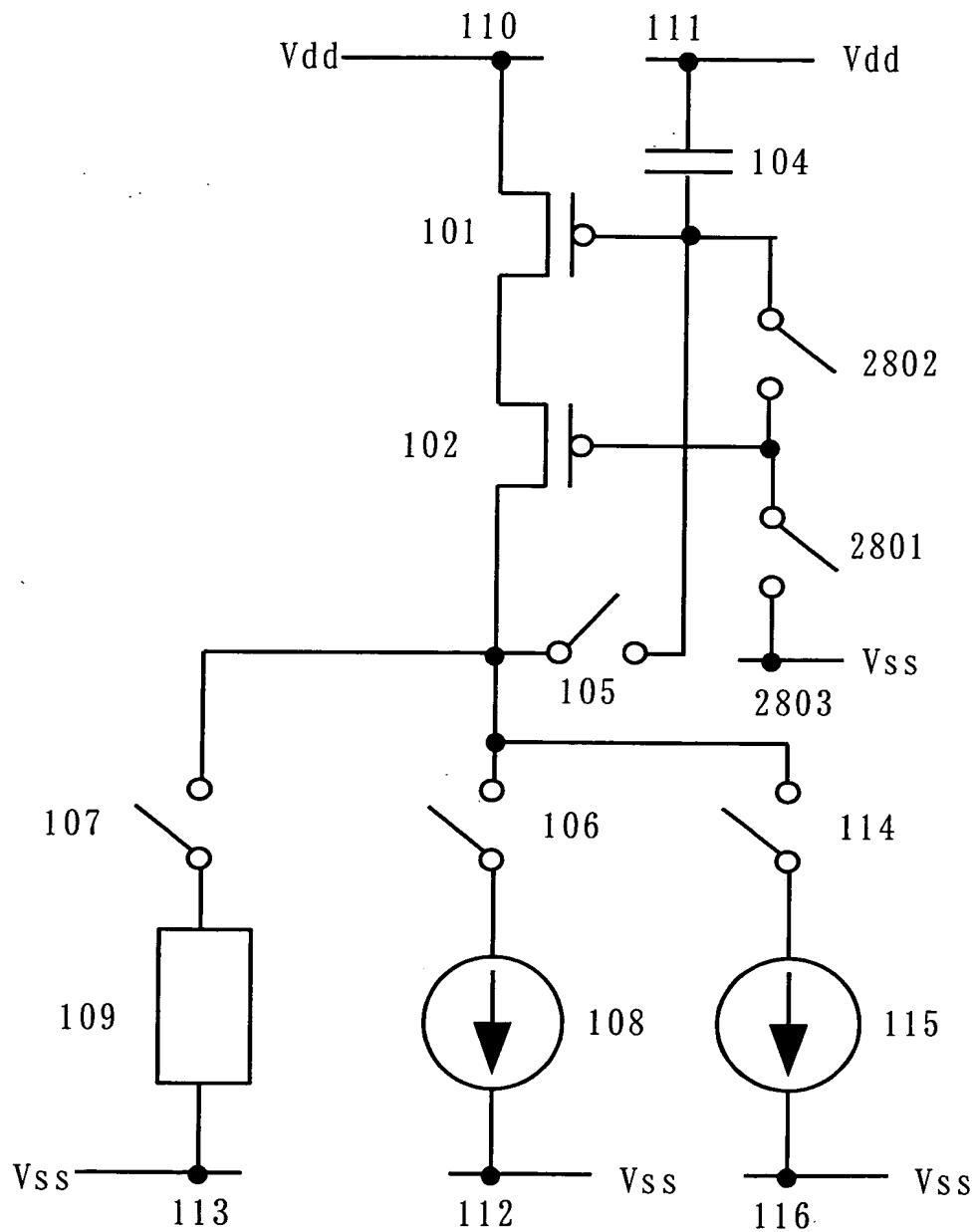


FIG. 35

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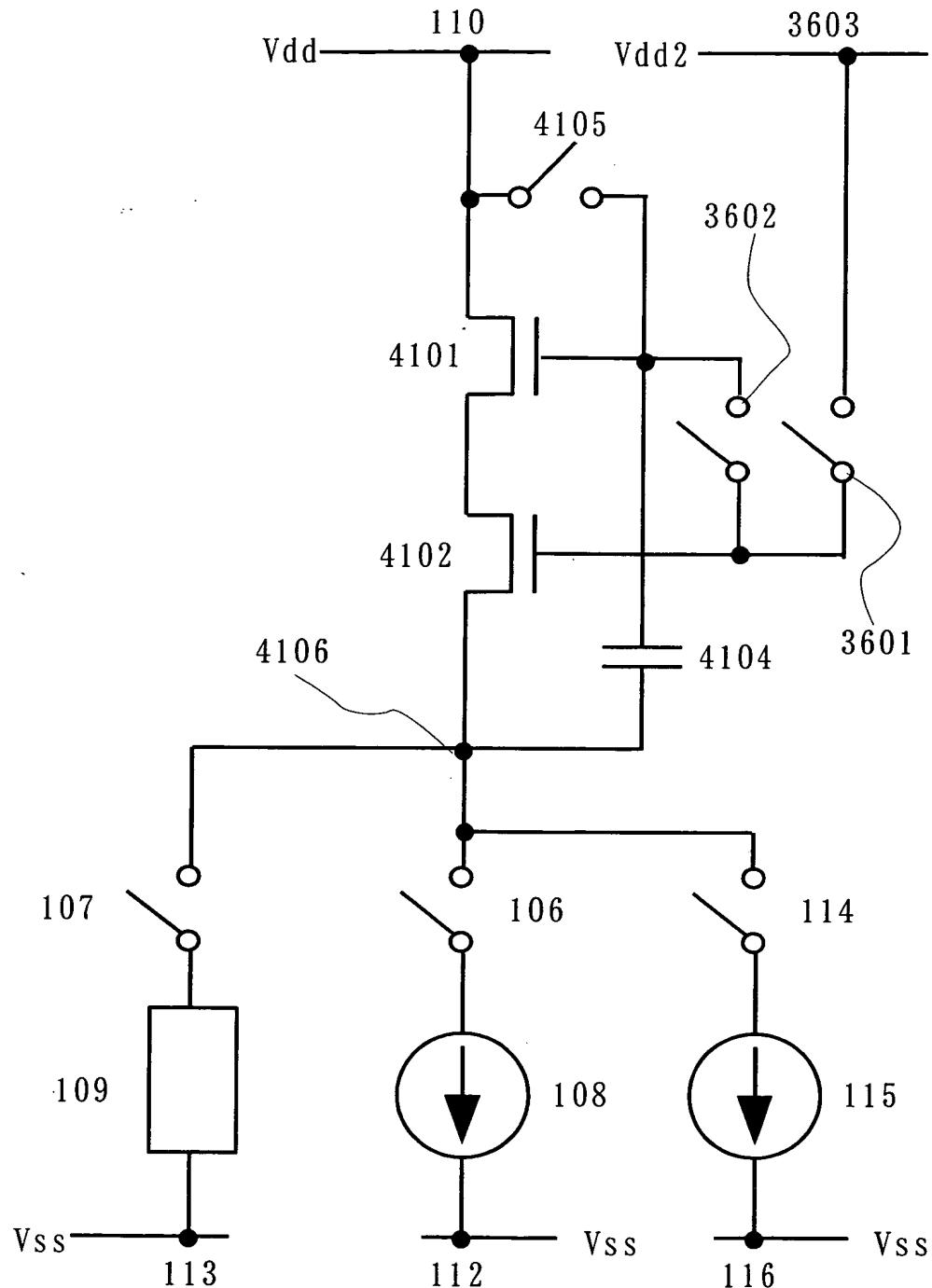


FIG. 36

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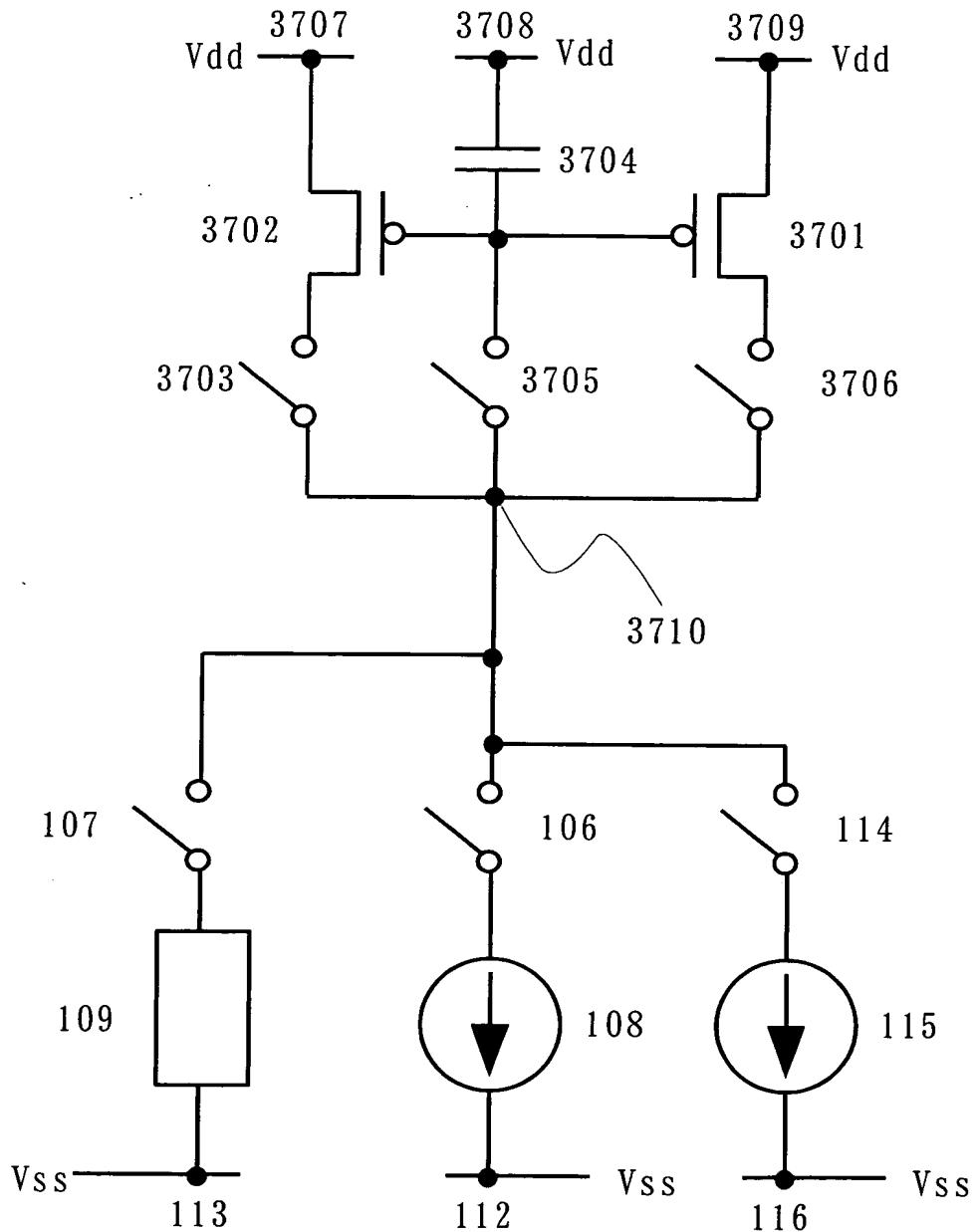


FIG. 37

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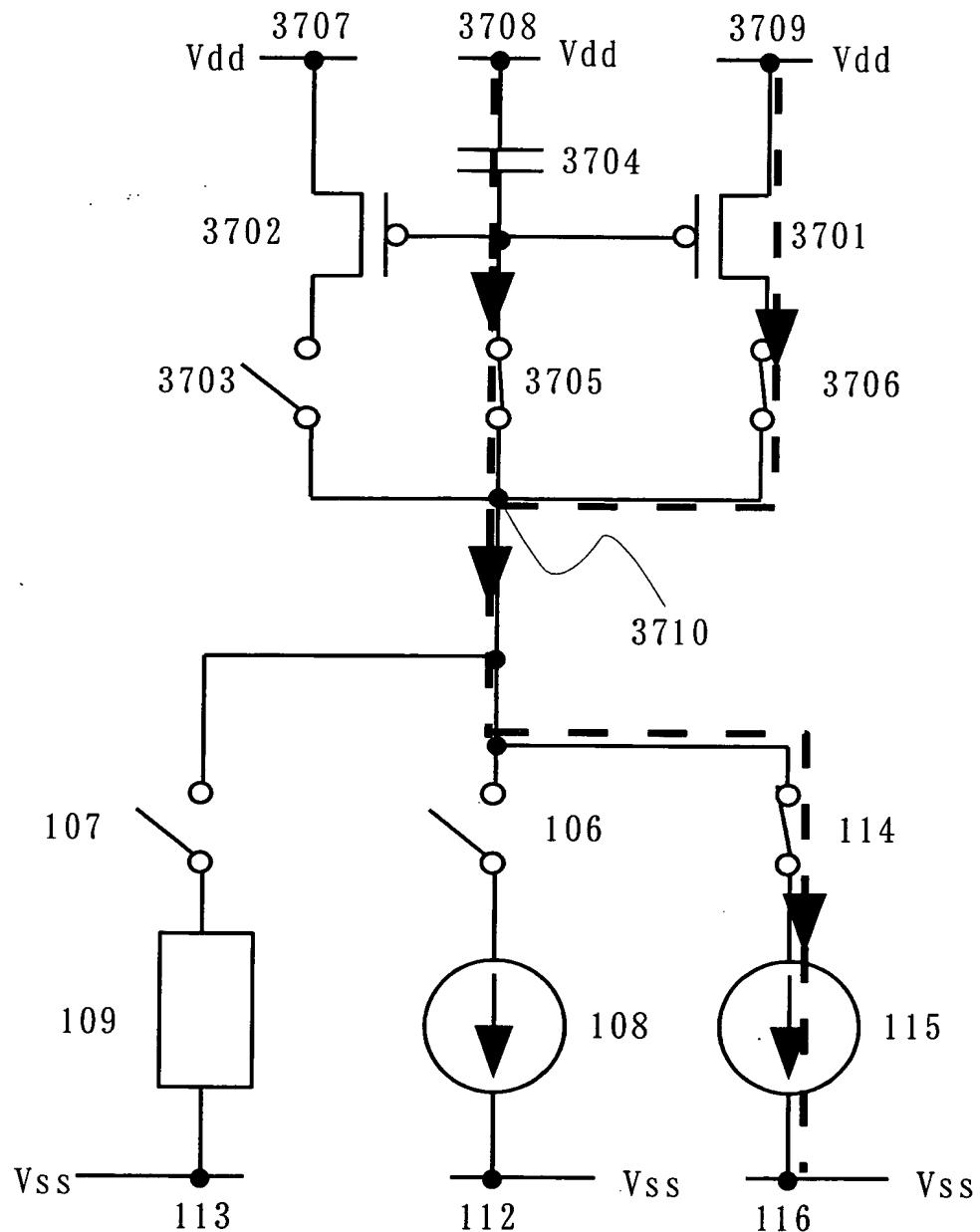


FIG. 38

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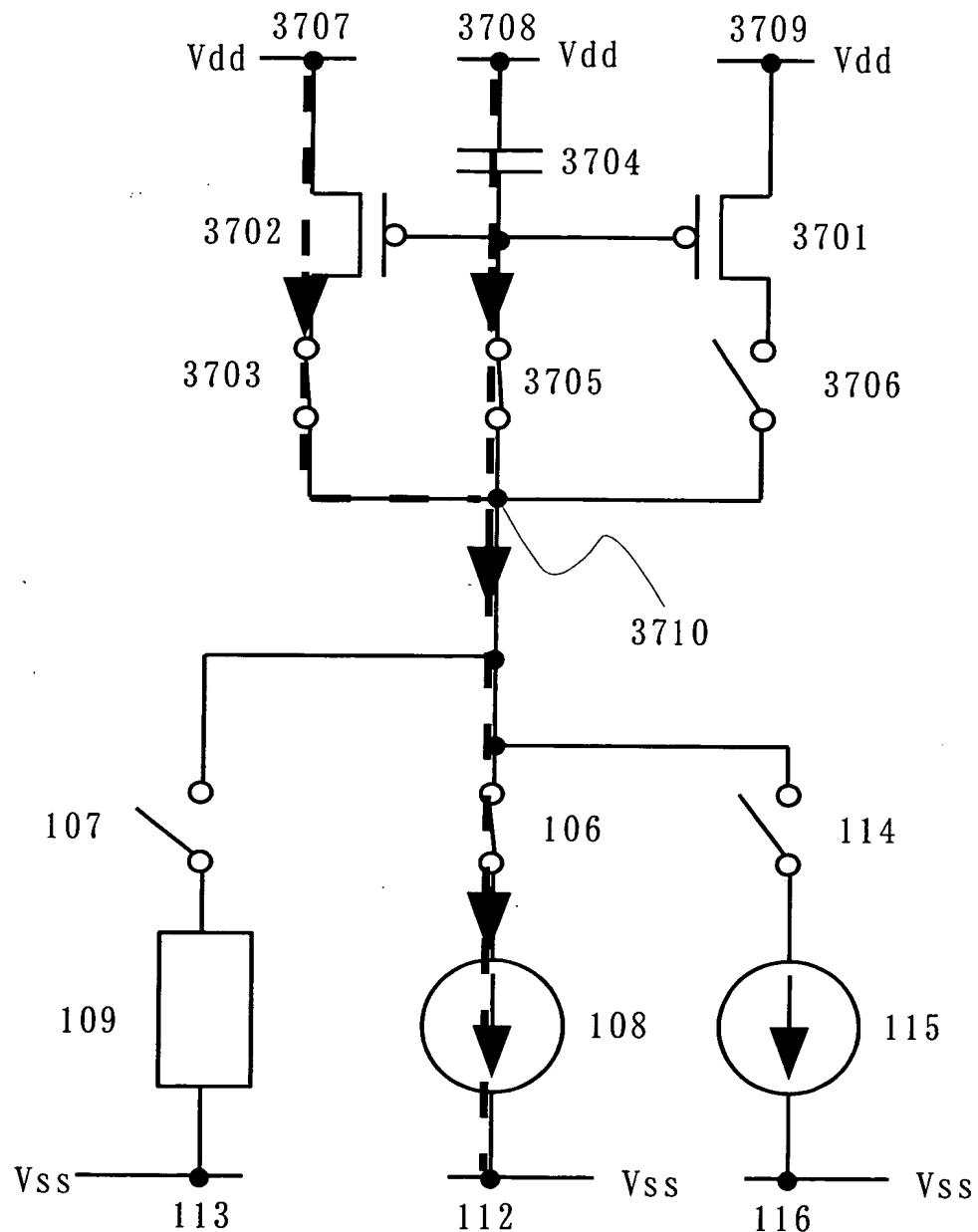


FIG. 39

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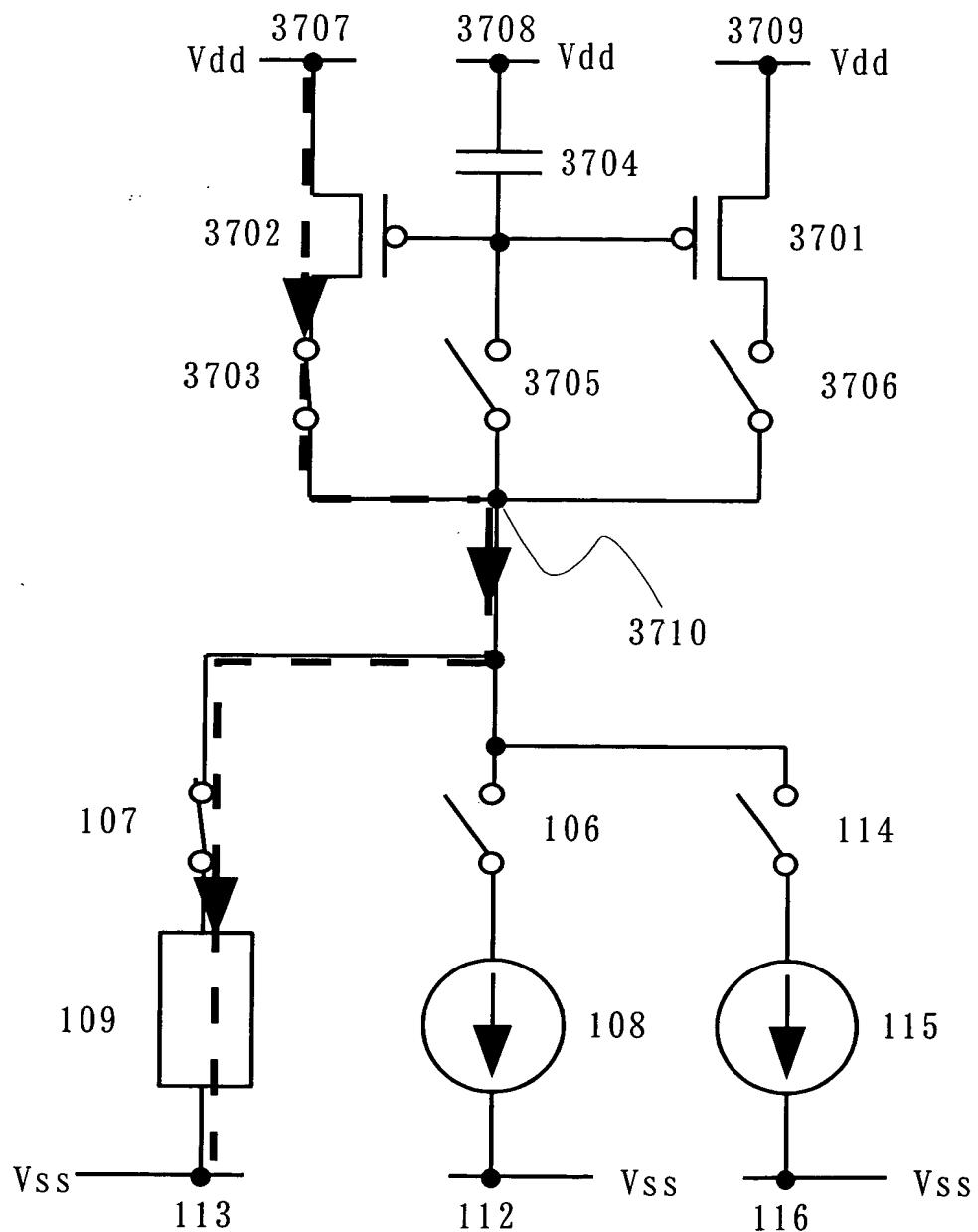


FIG. 40

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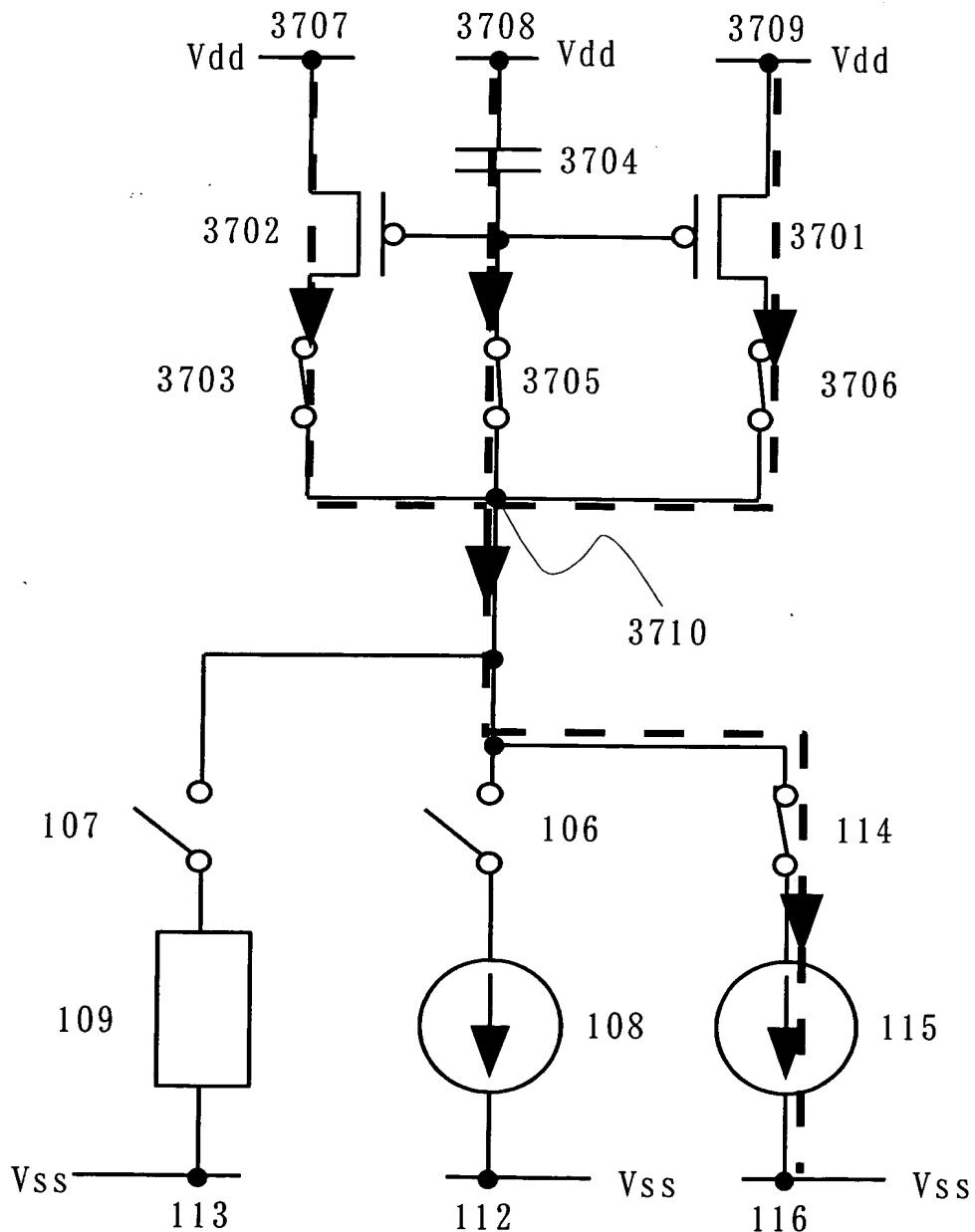


FIG. 41

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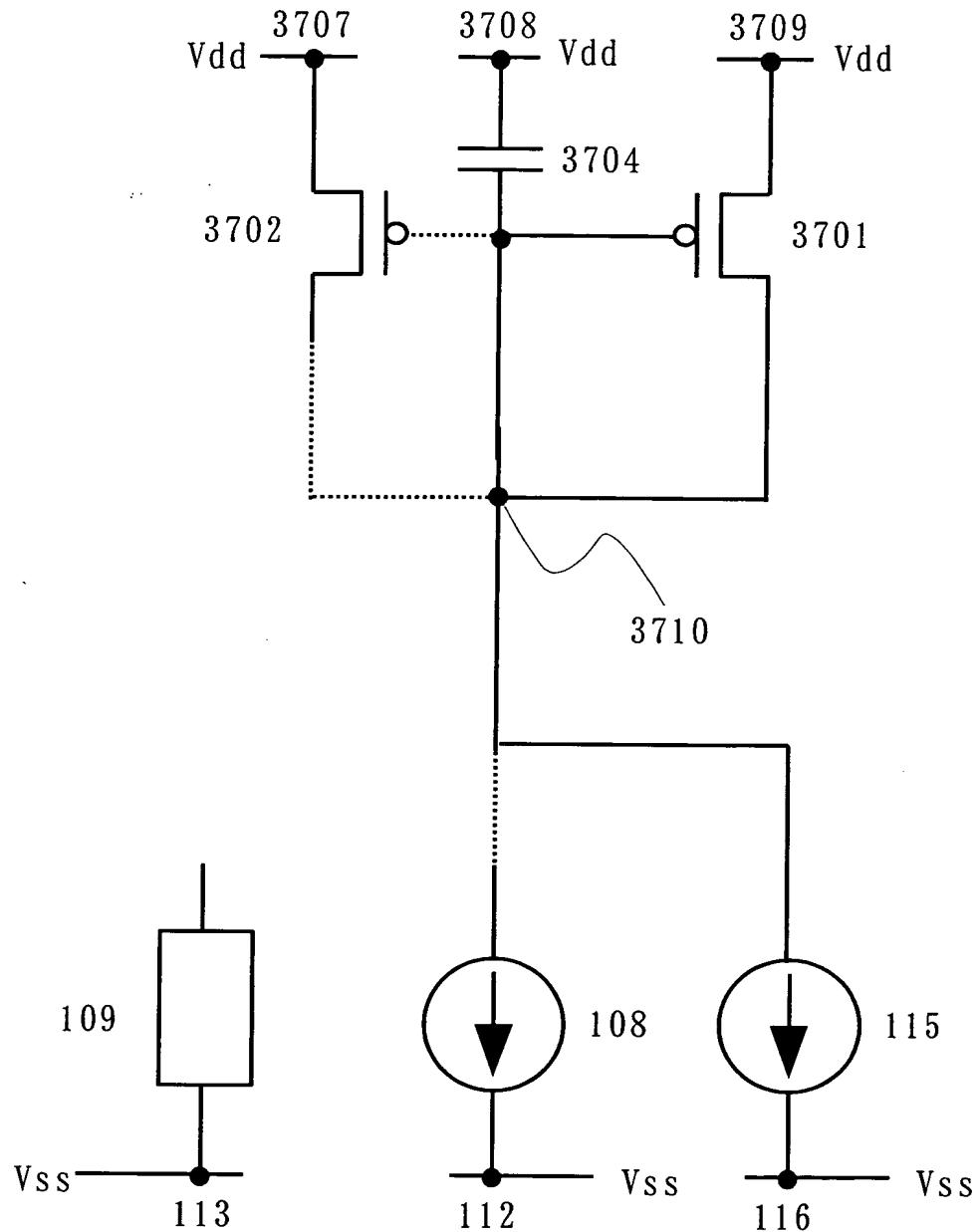


FIG. 42

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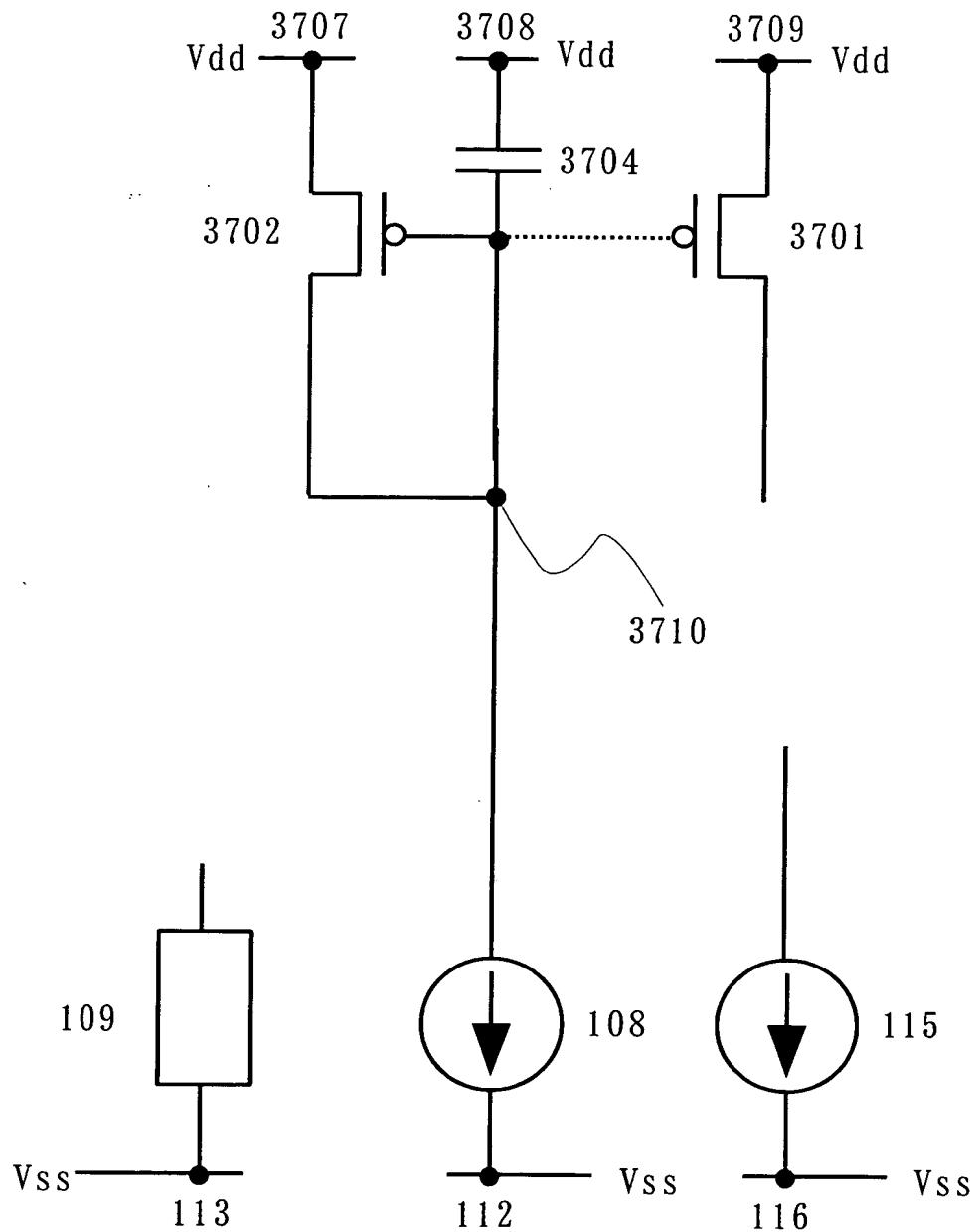


FIG. 43

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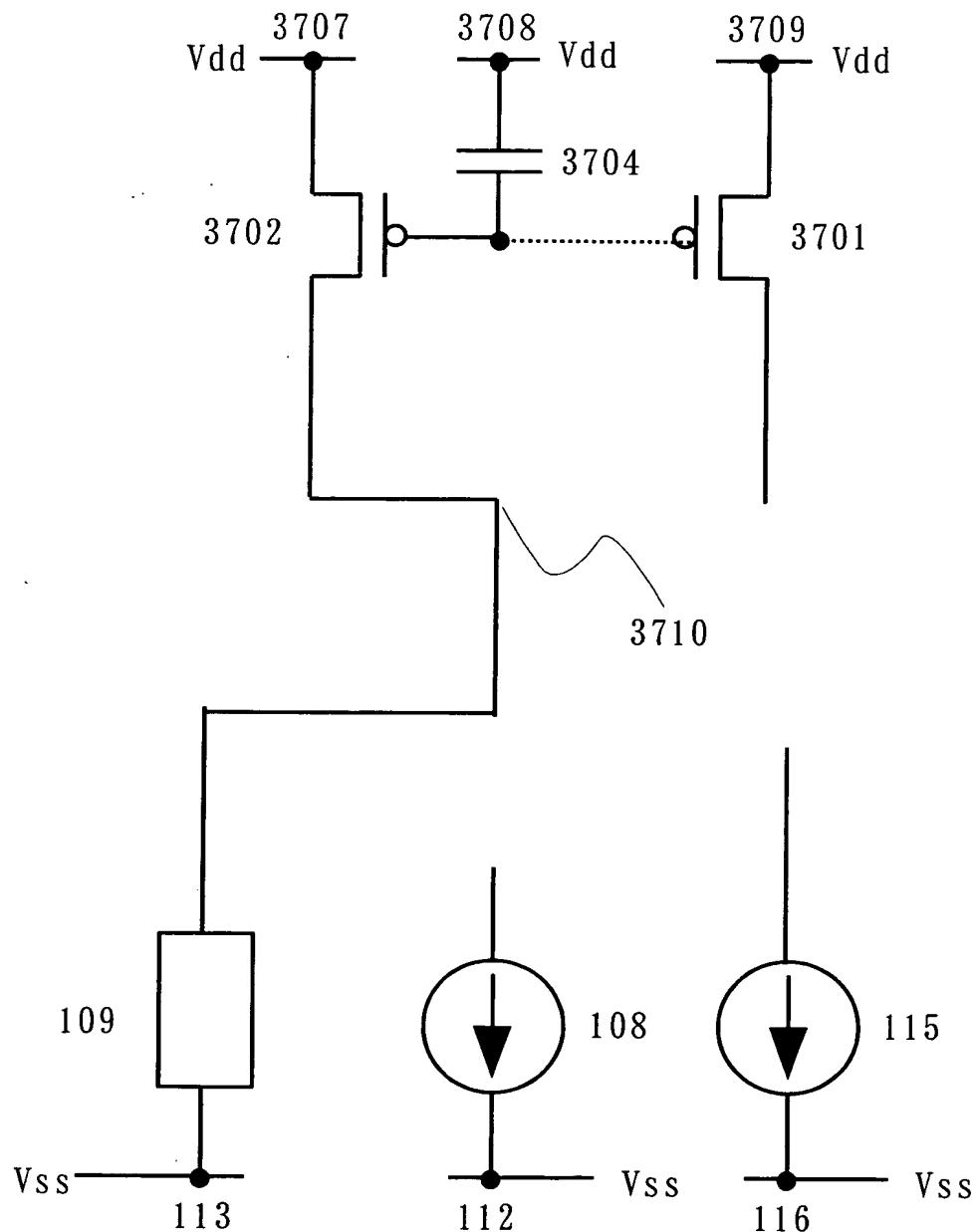


FIG. 44

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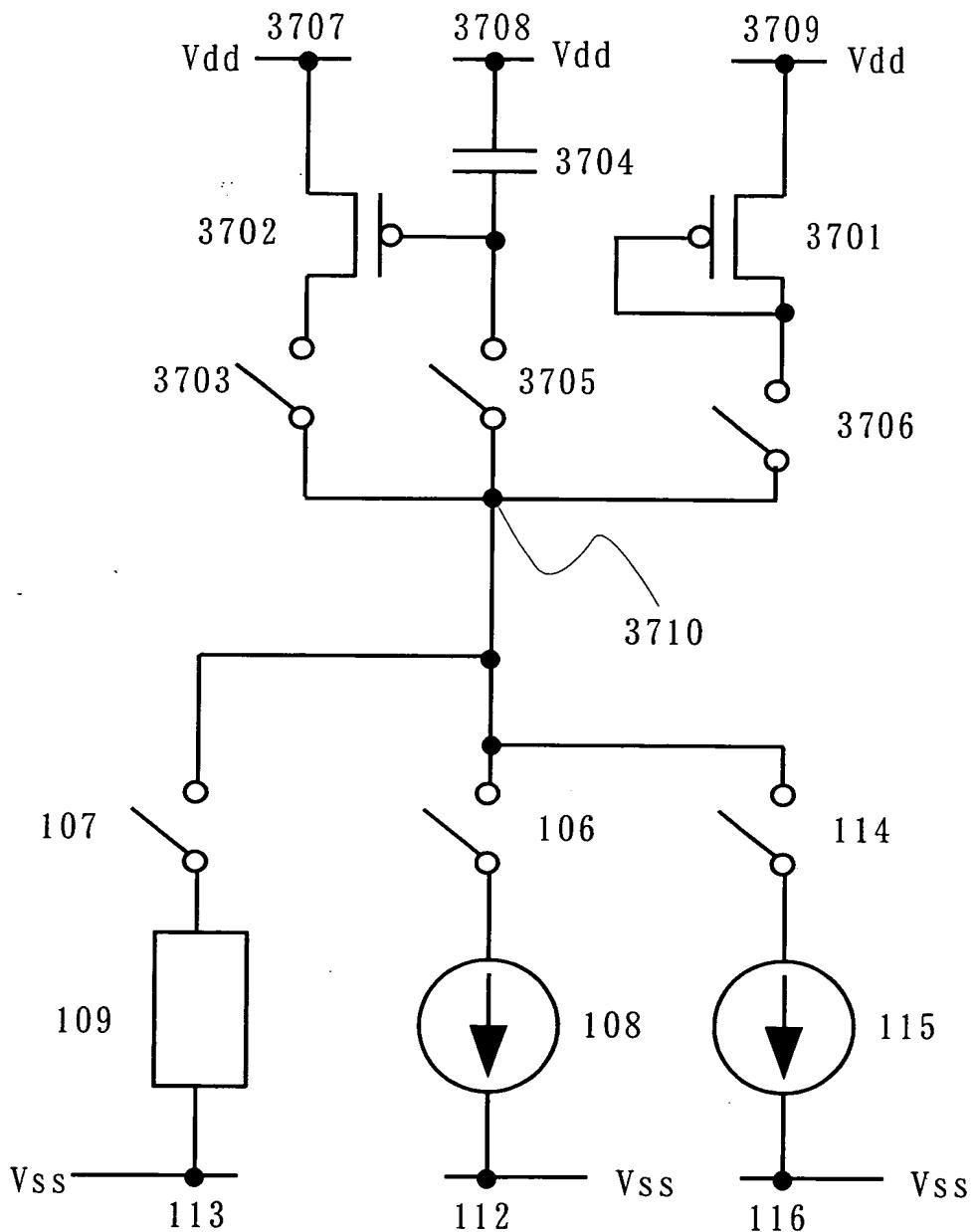


FIG. 45

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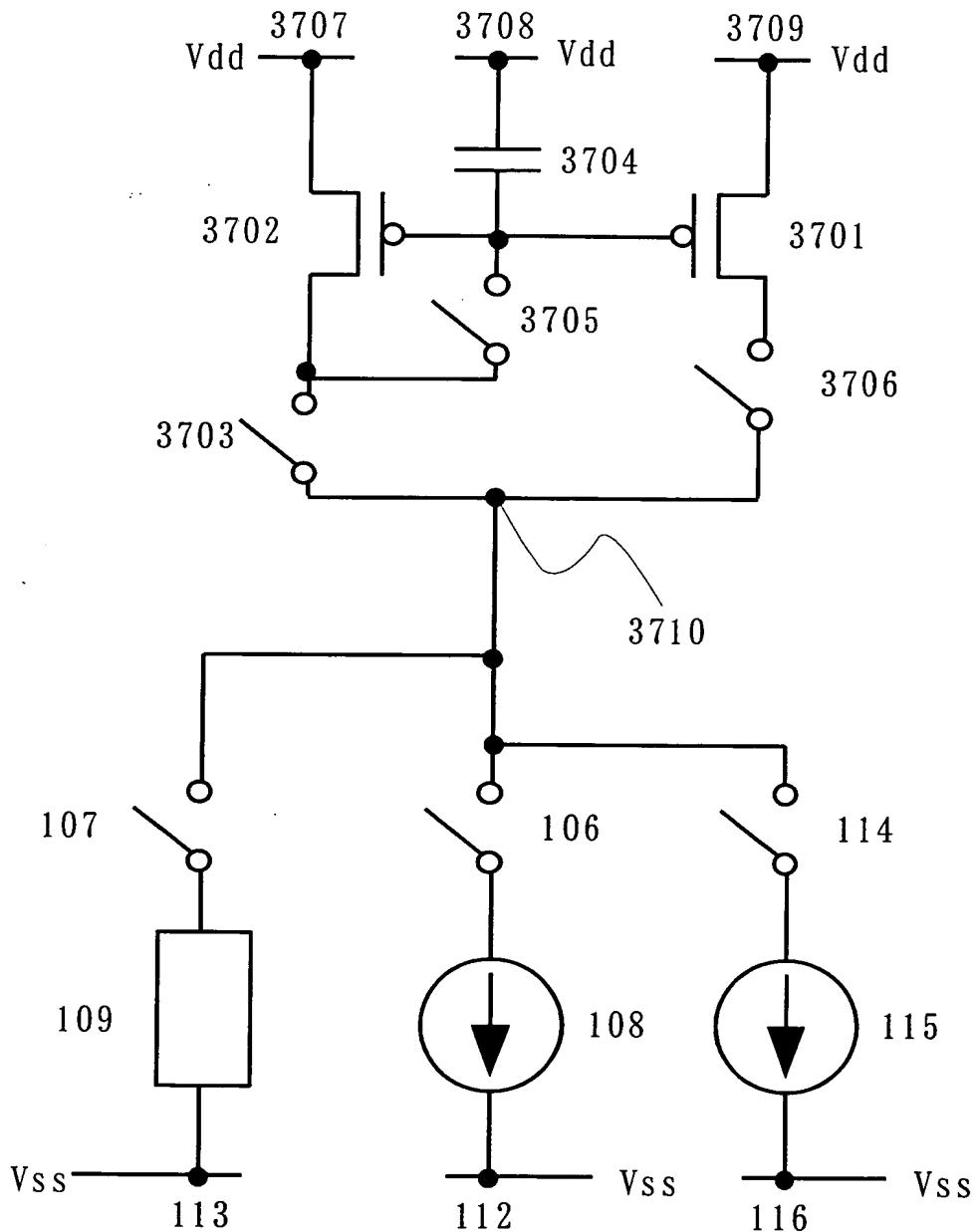


FIG. 46

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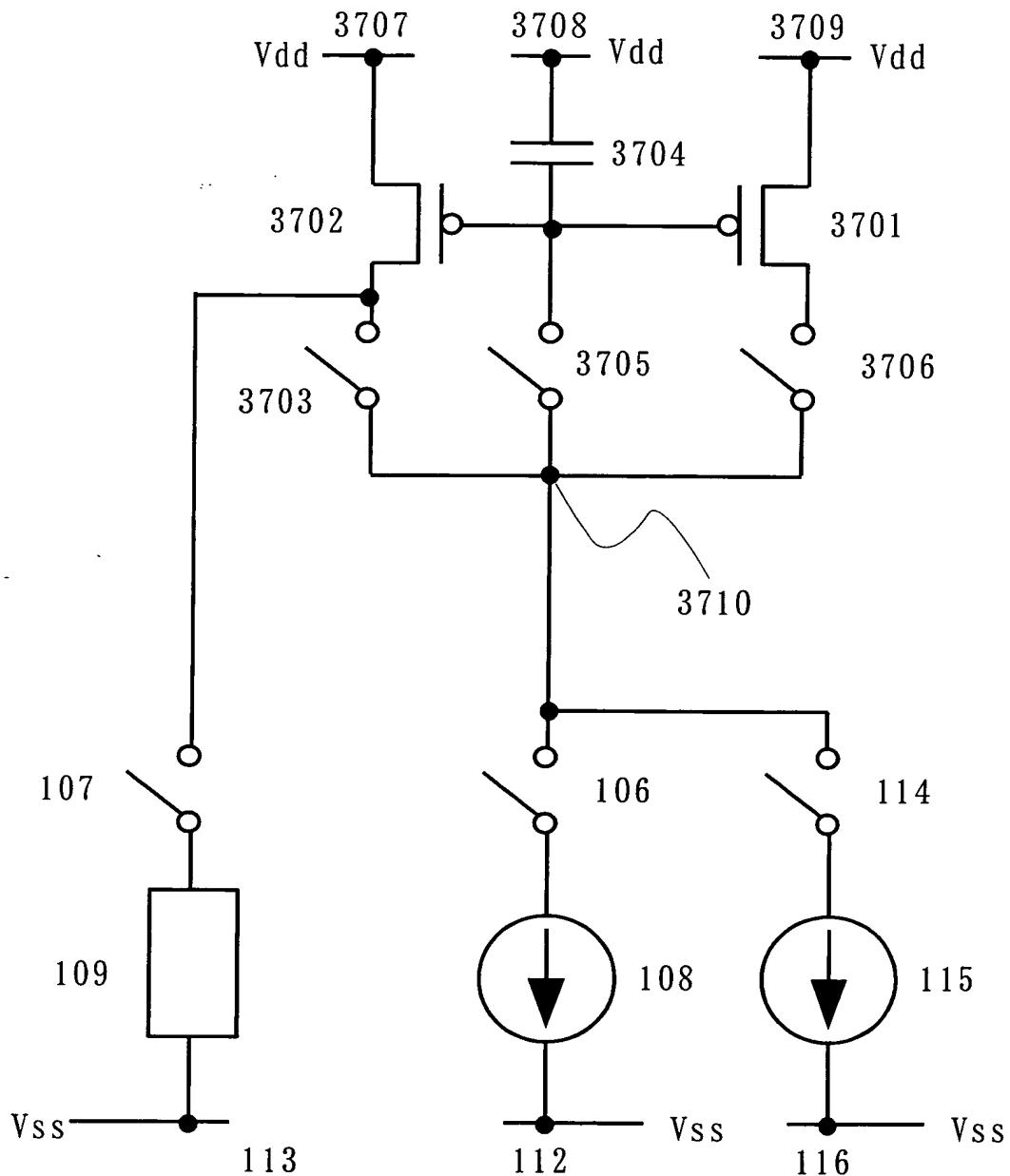


FIG. 47

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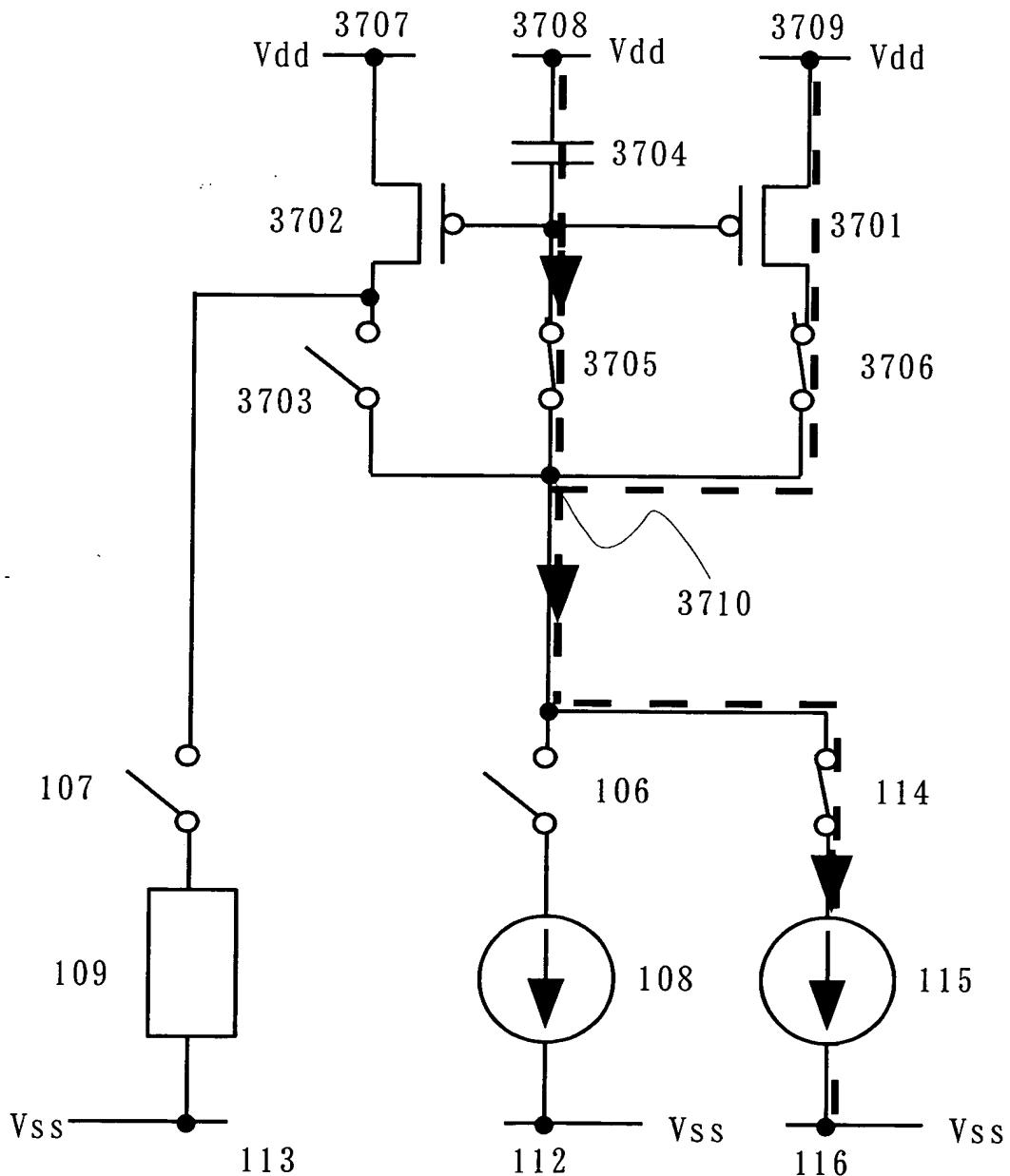


FIG. 48

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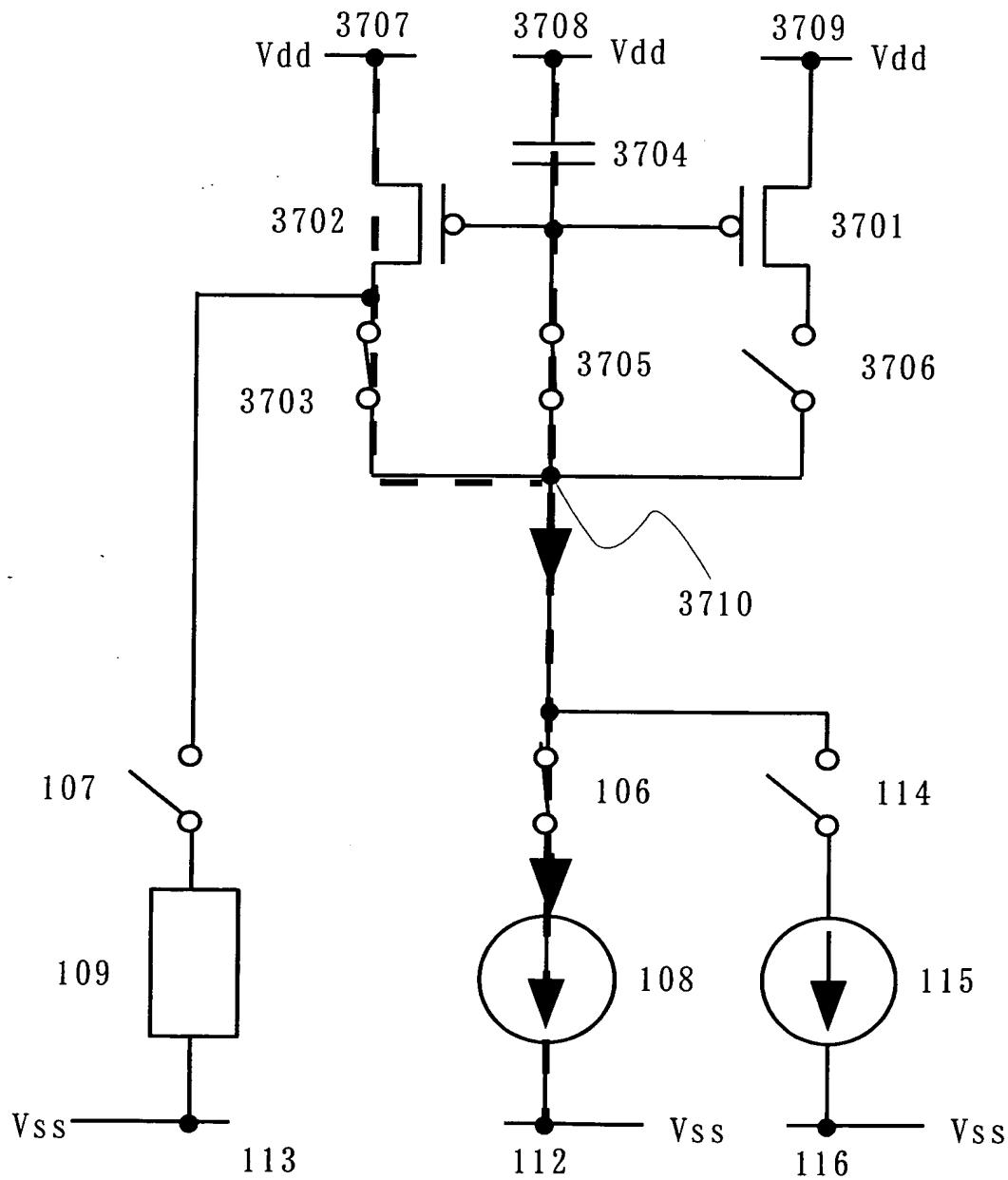


FIG. 49

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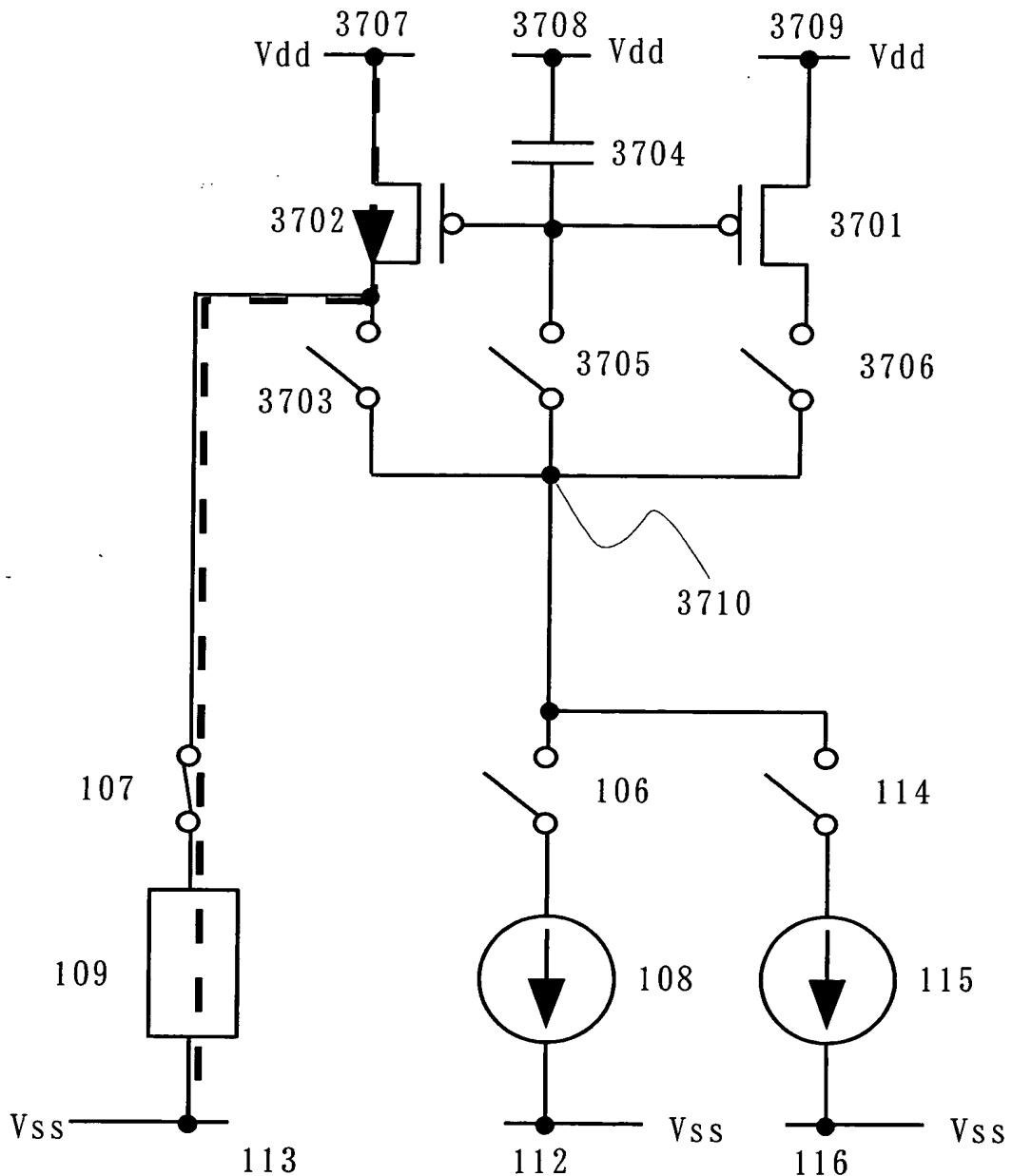


FIG. 50

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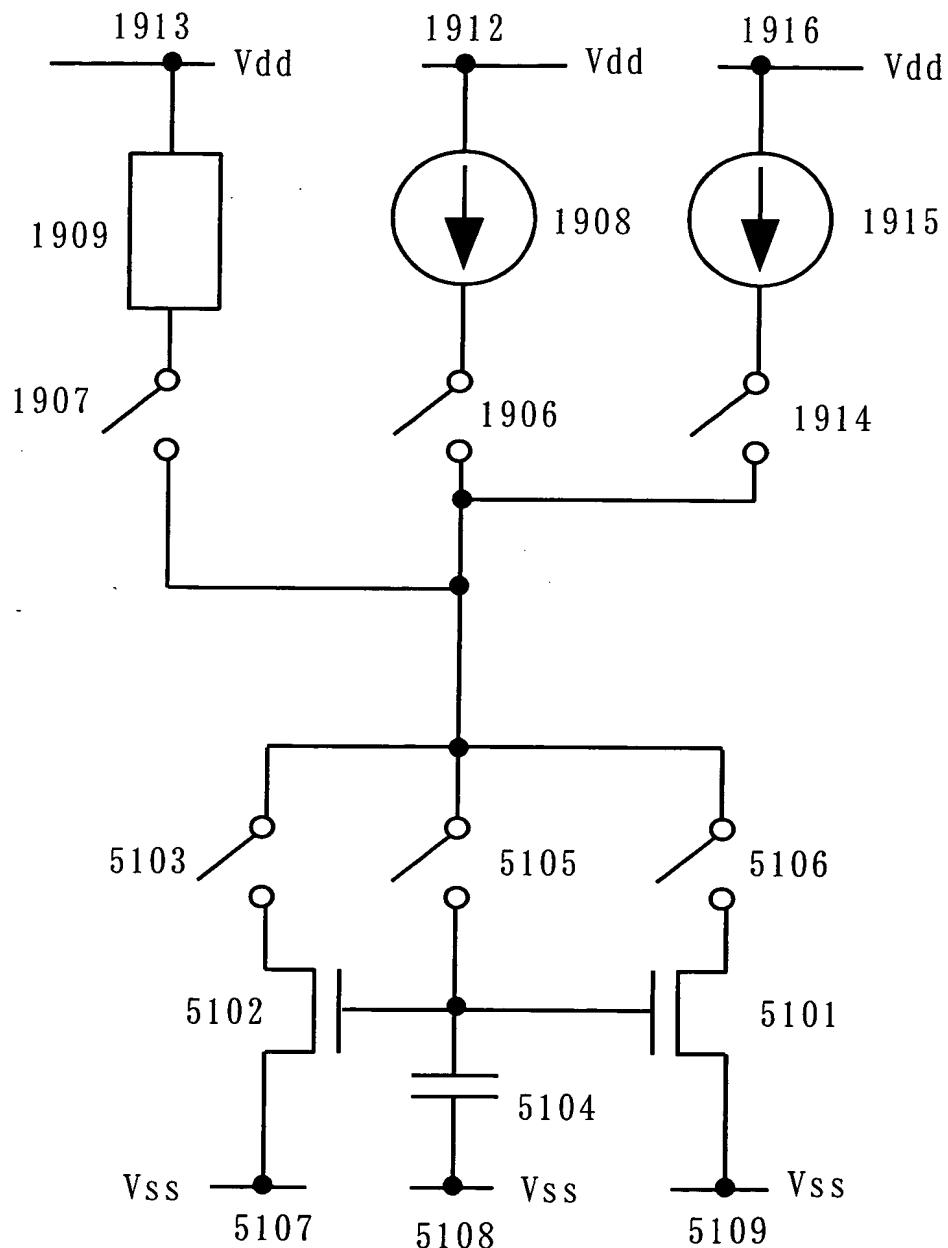


FIG. 51

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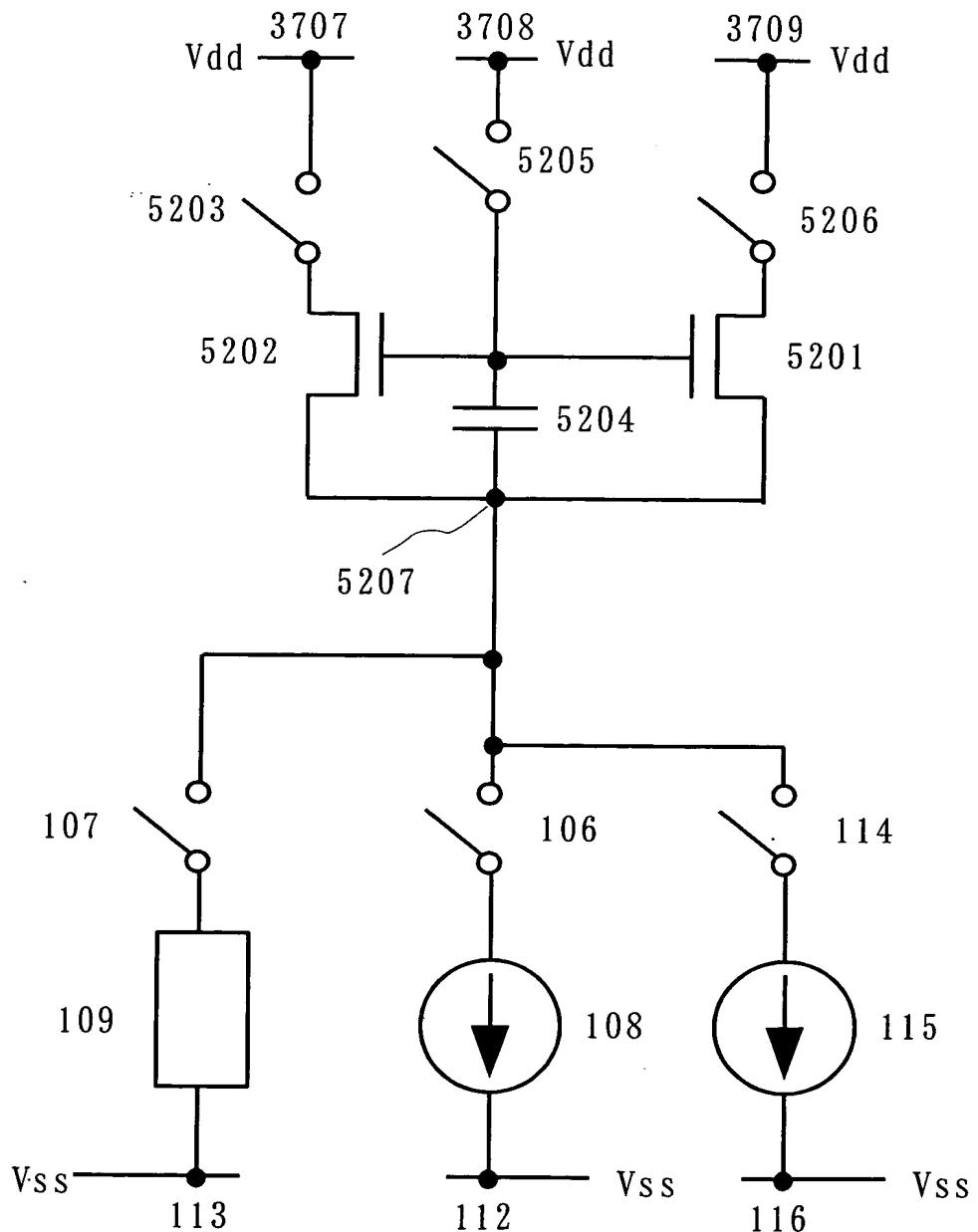


FIG. 52

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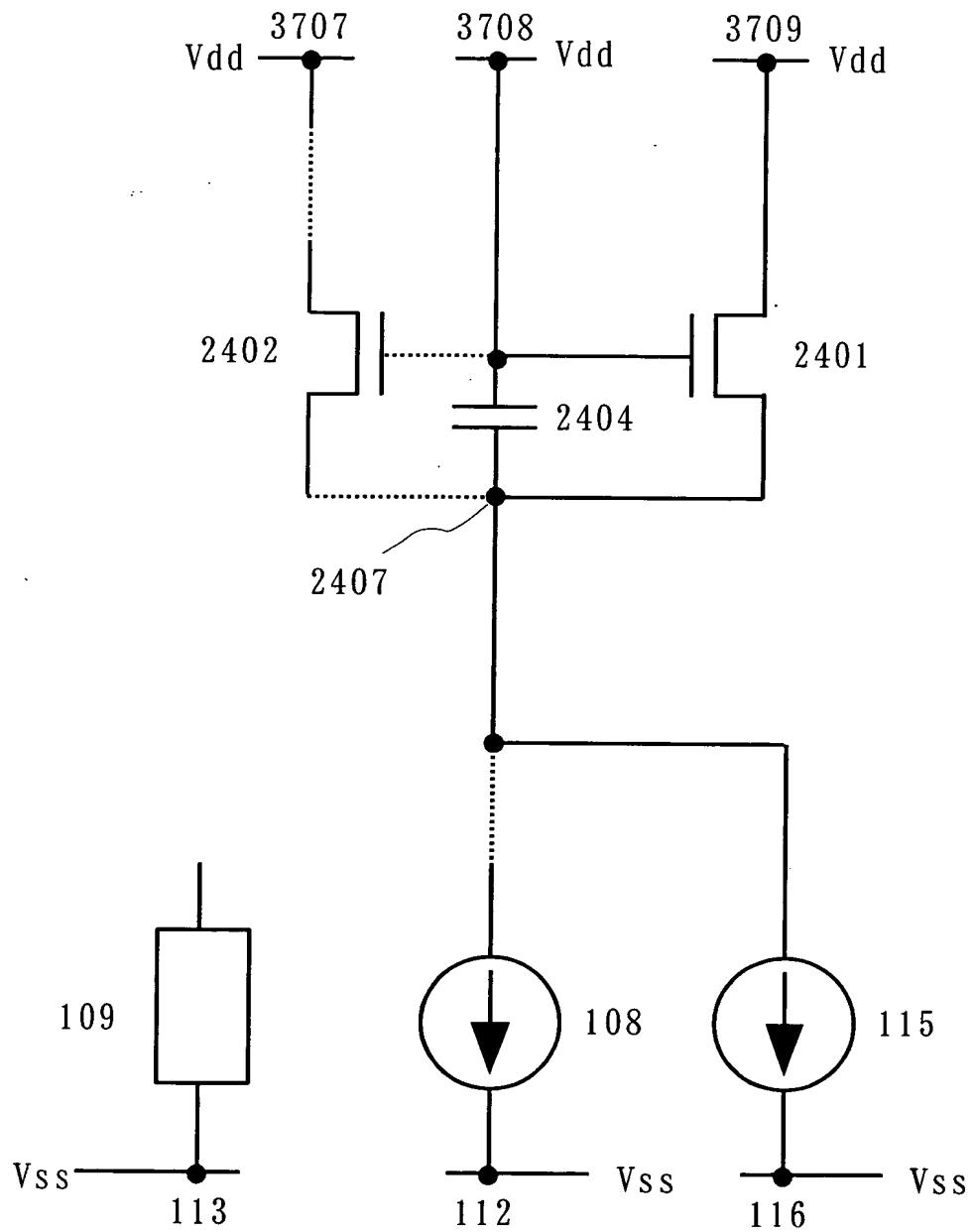


FIG. 53

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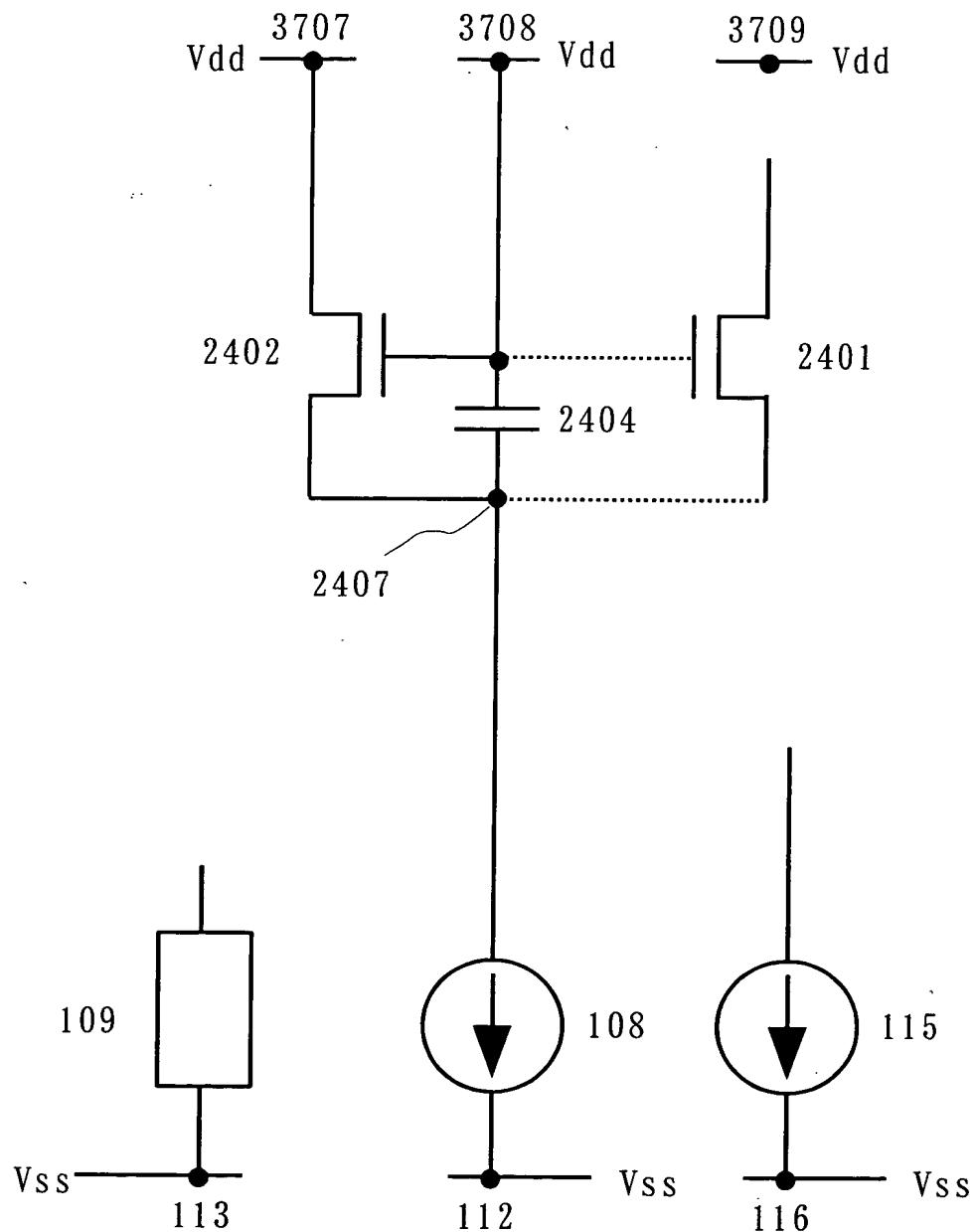


FIG. 54

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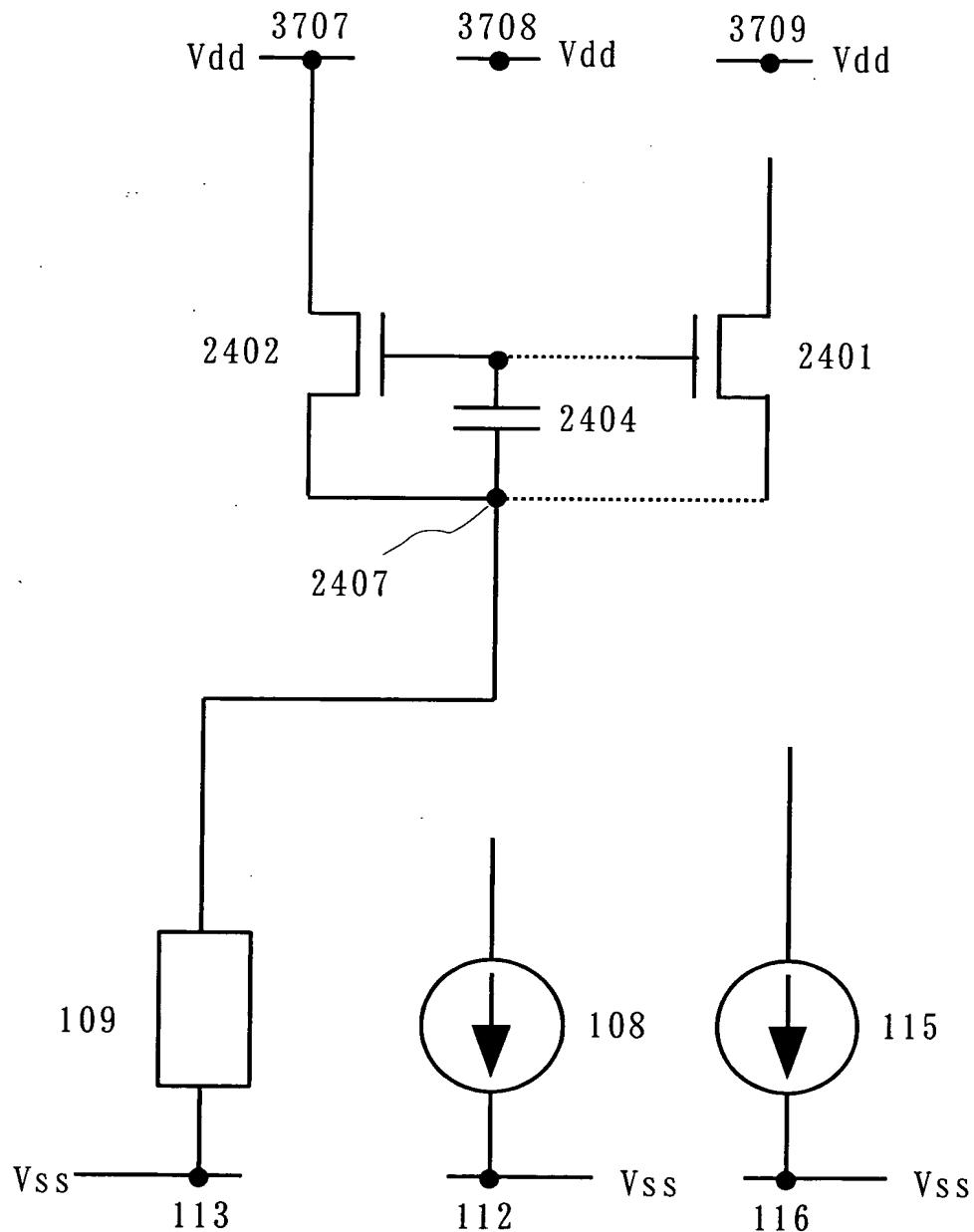


FIG. 55

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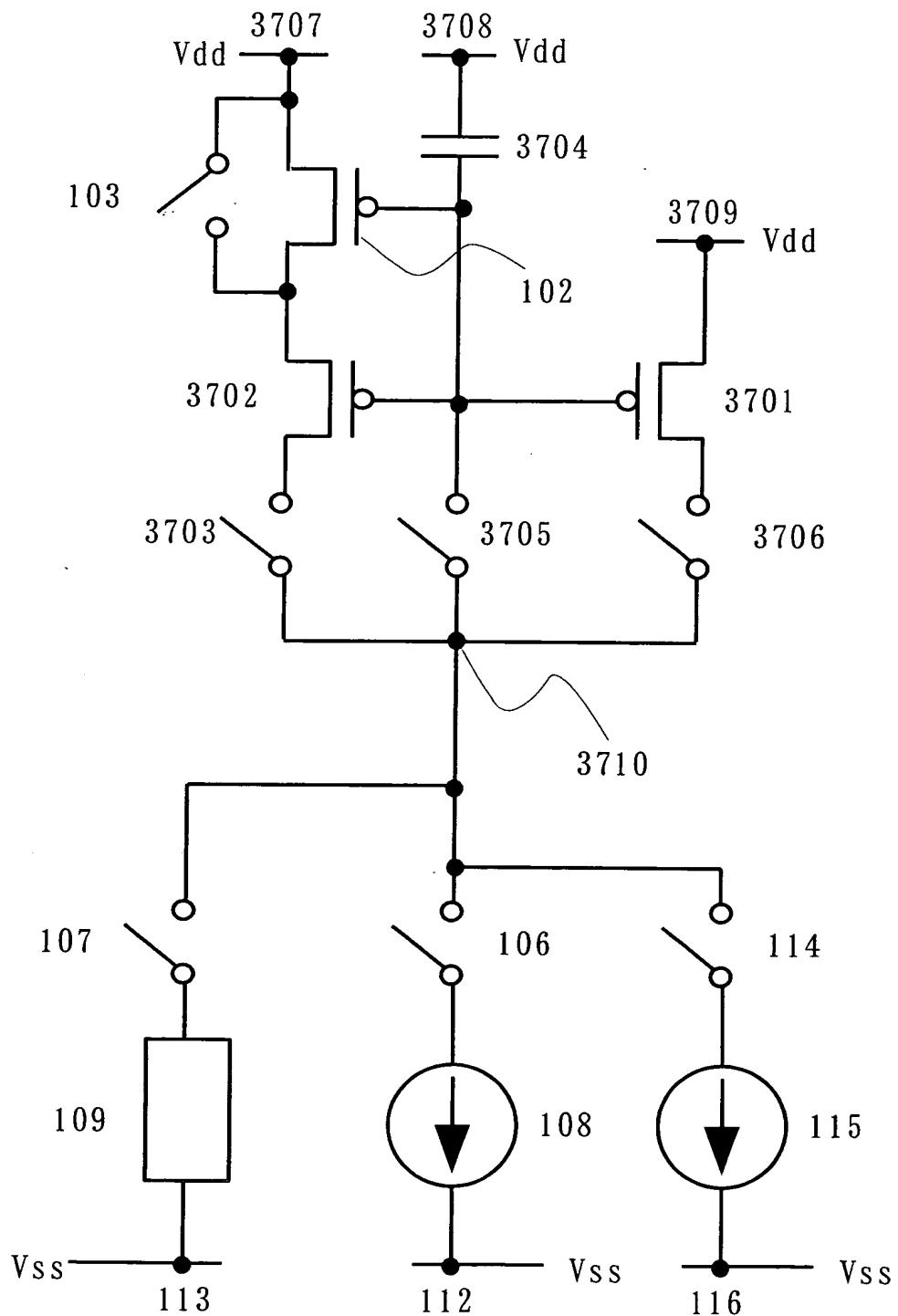


FIG. 56

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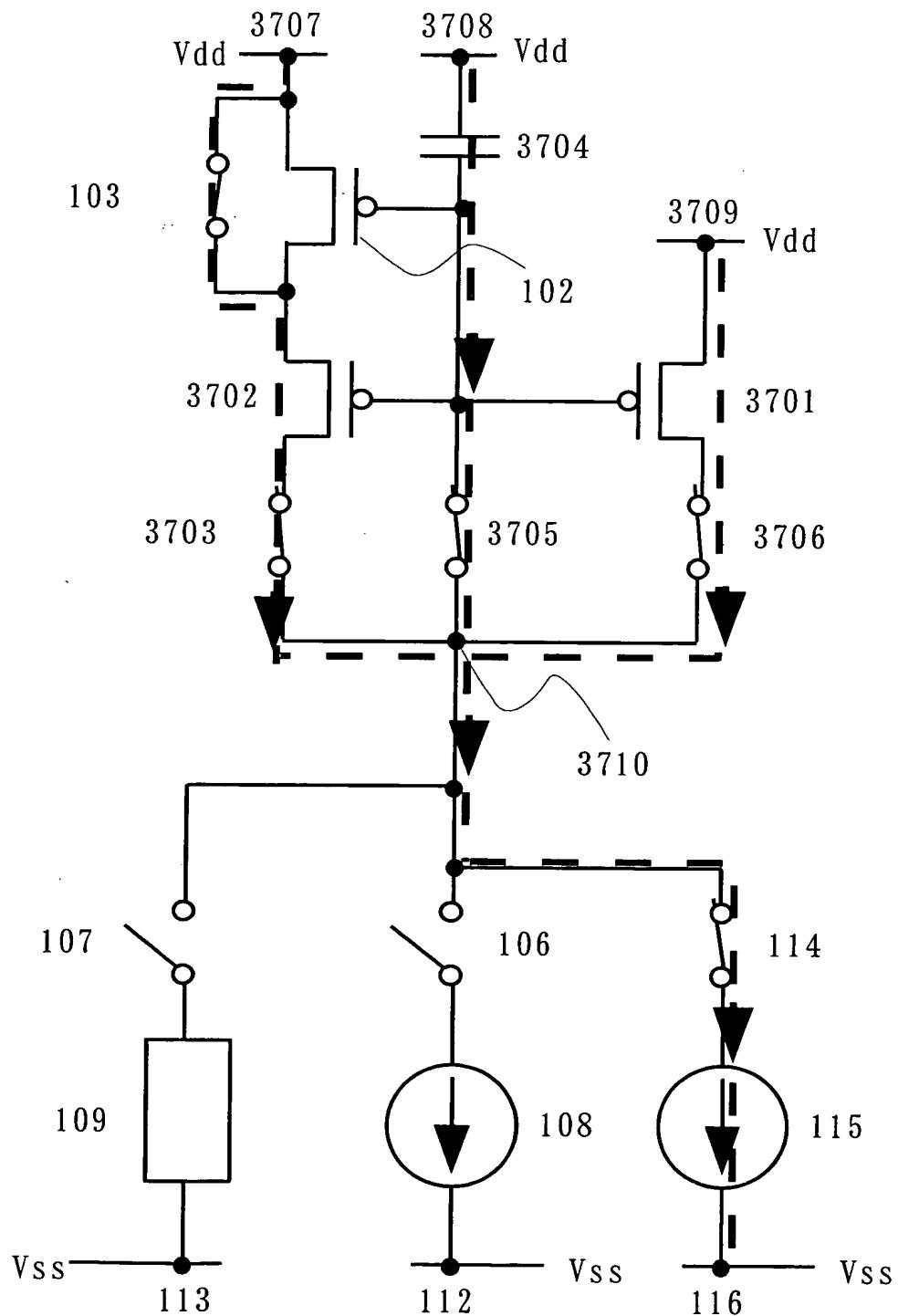


FIG. 57

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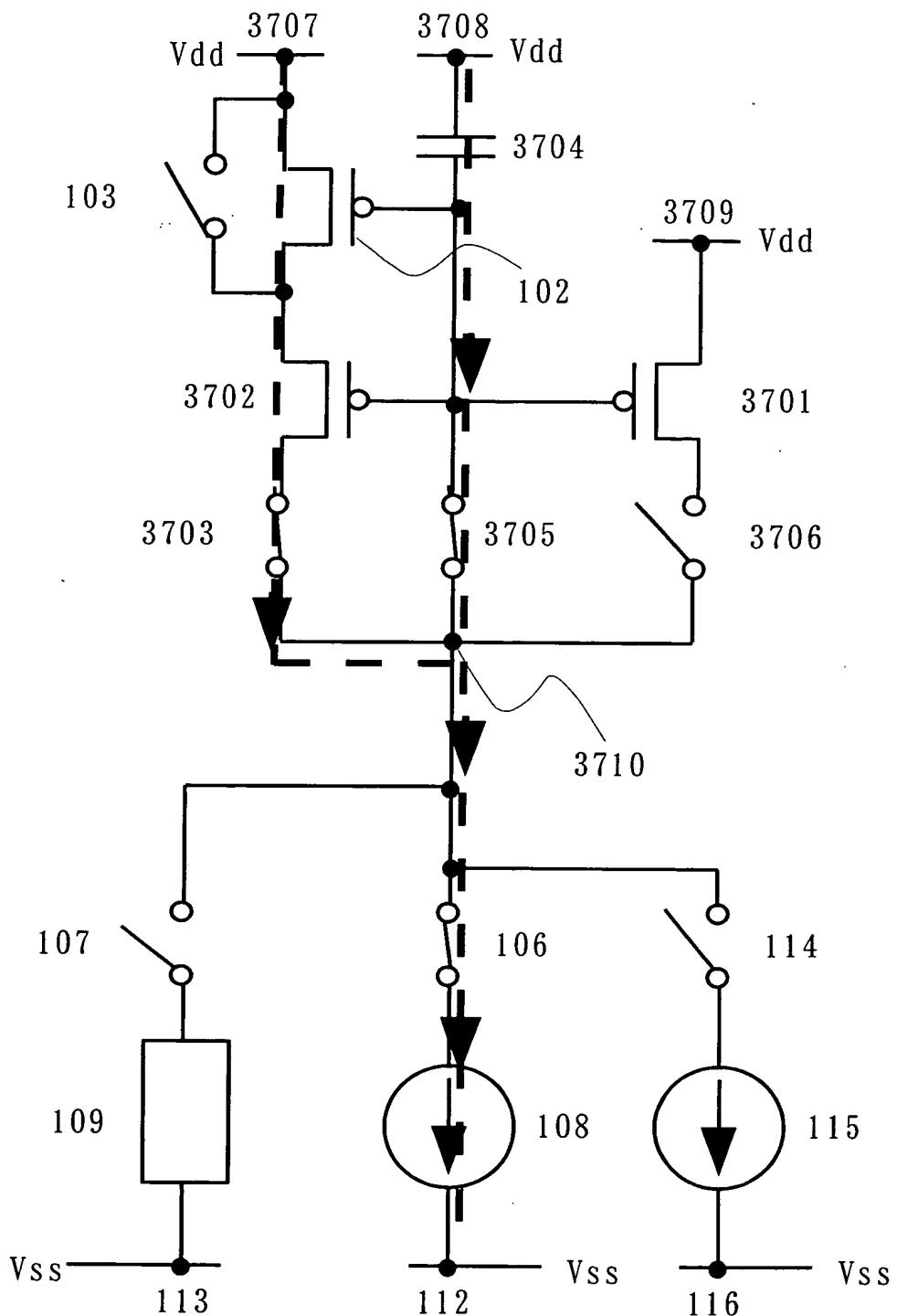


FIG. 58

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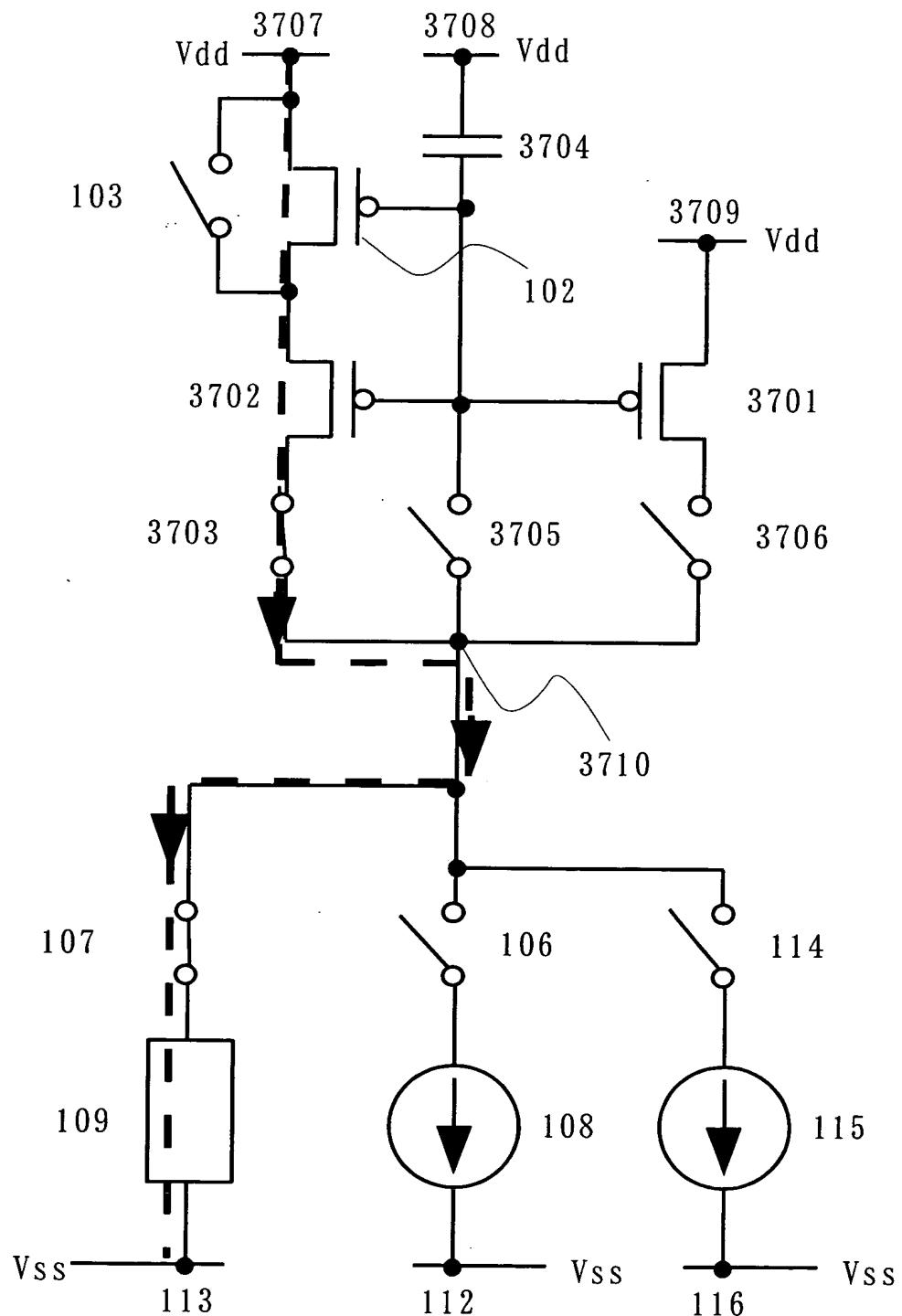


FIG. 59

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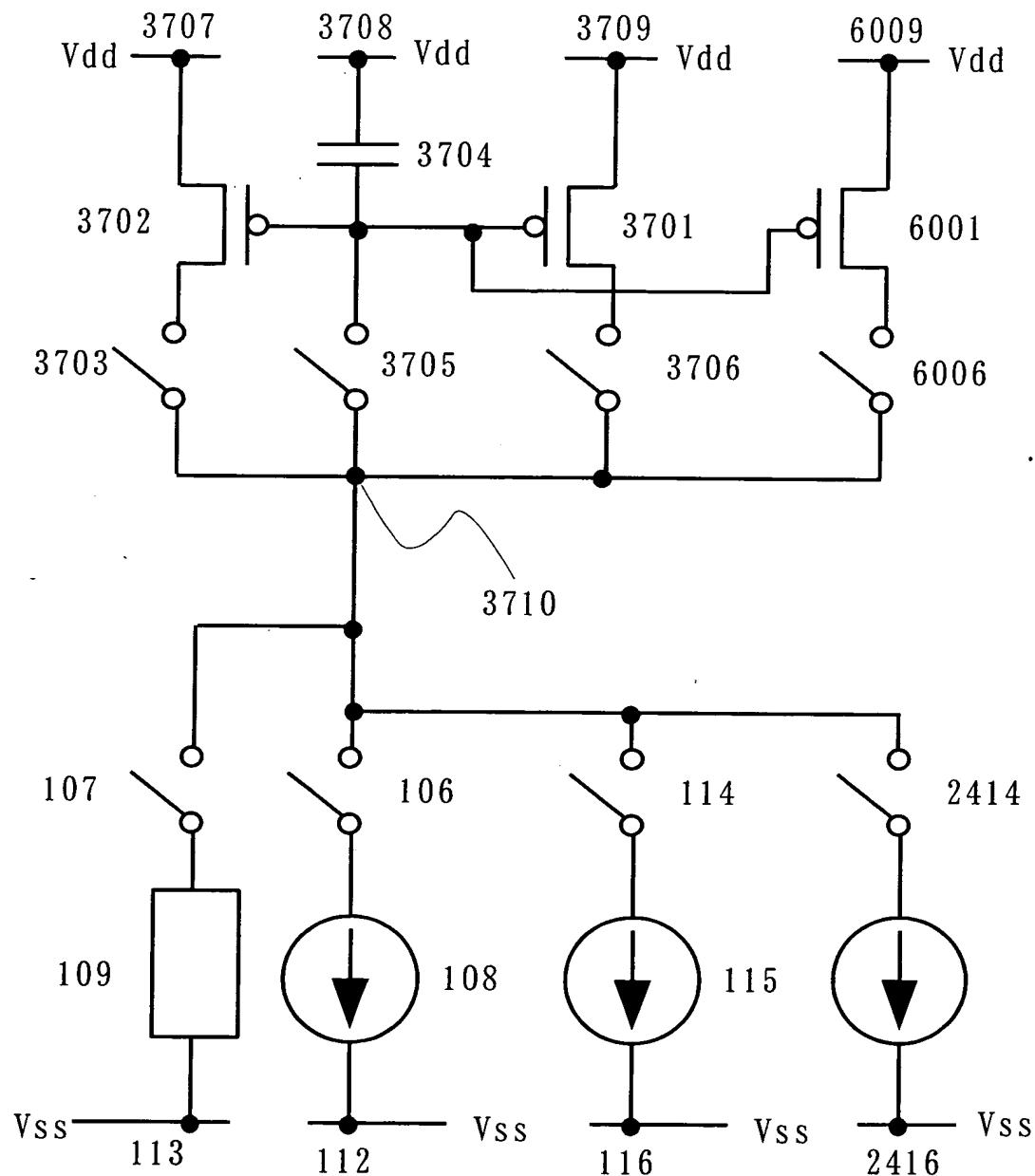


FIG. 60

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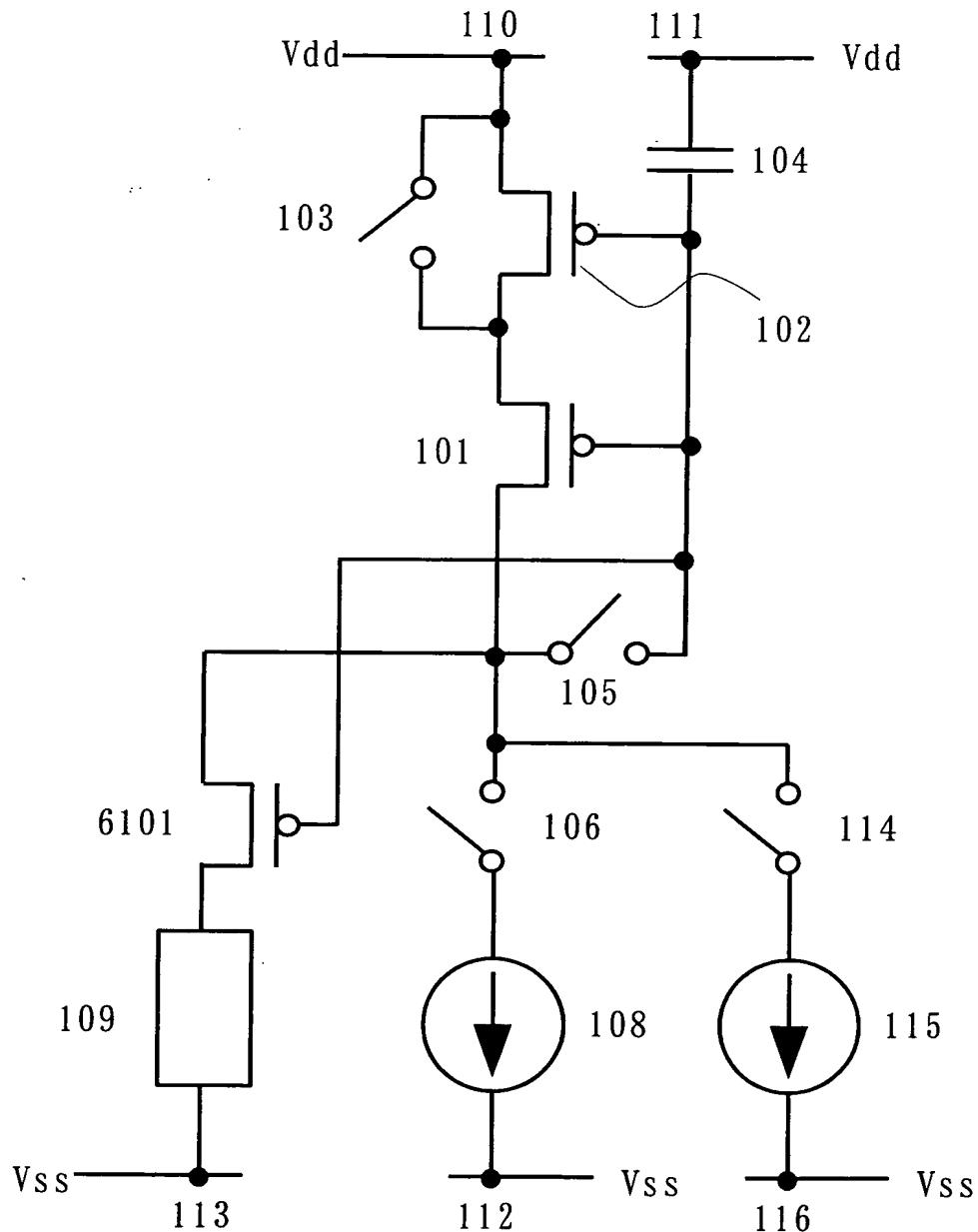


FIG. 61

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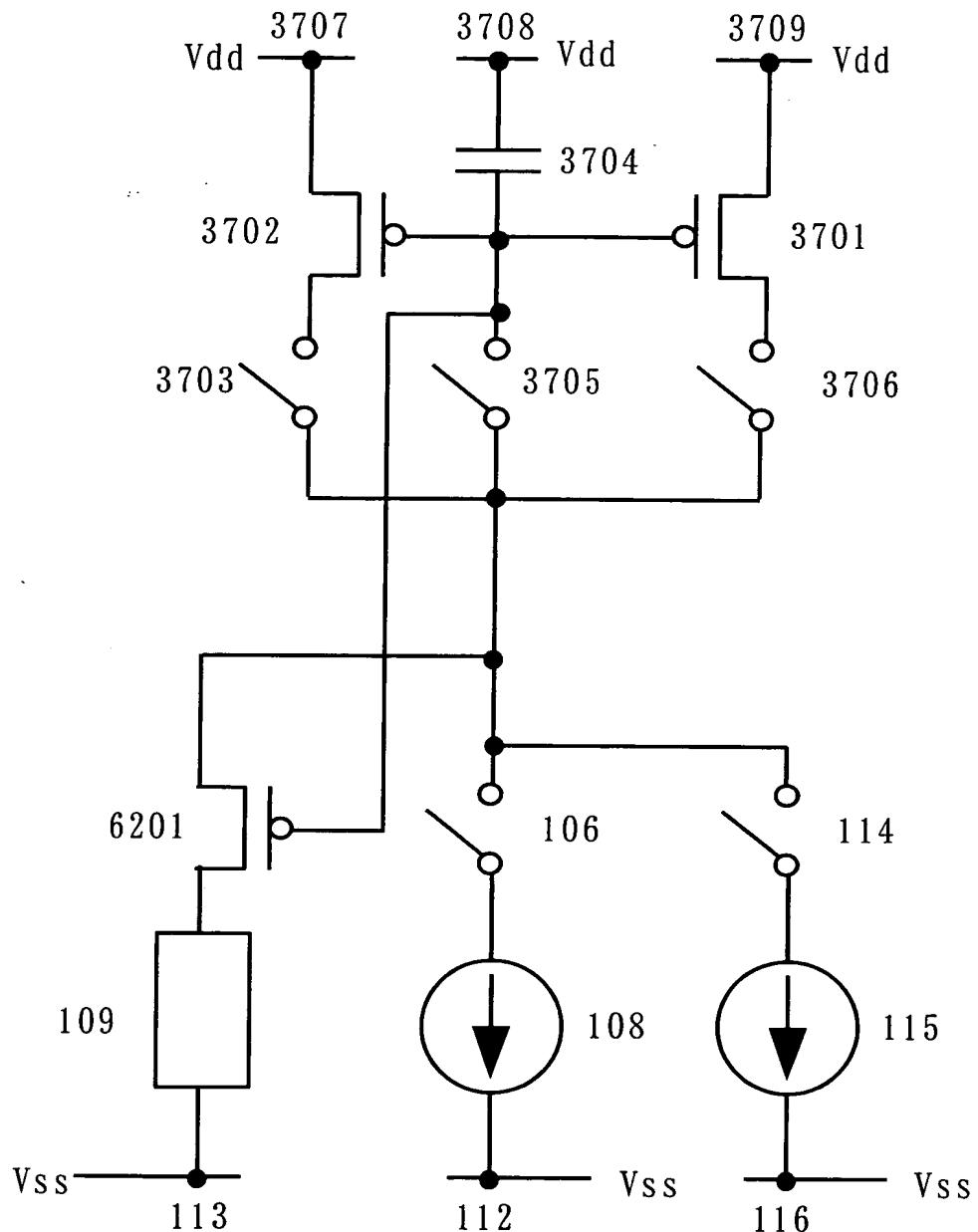


FIG. 62

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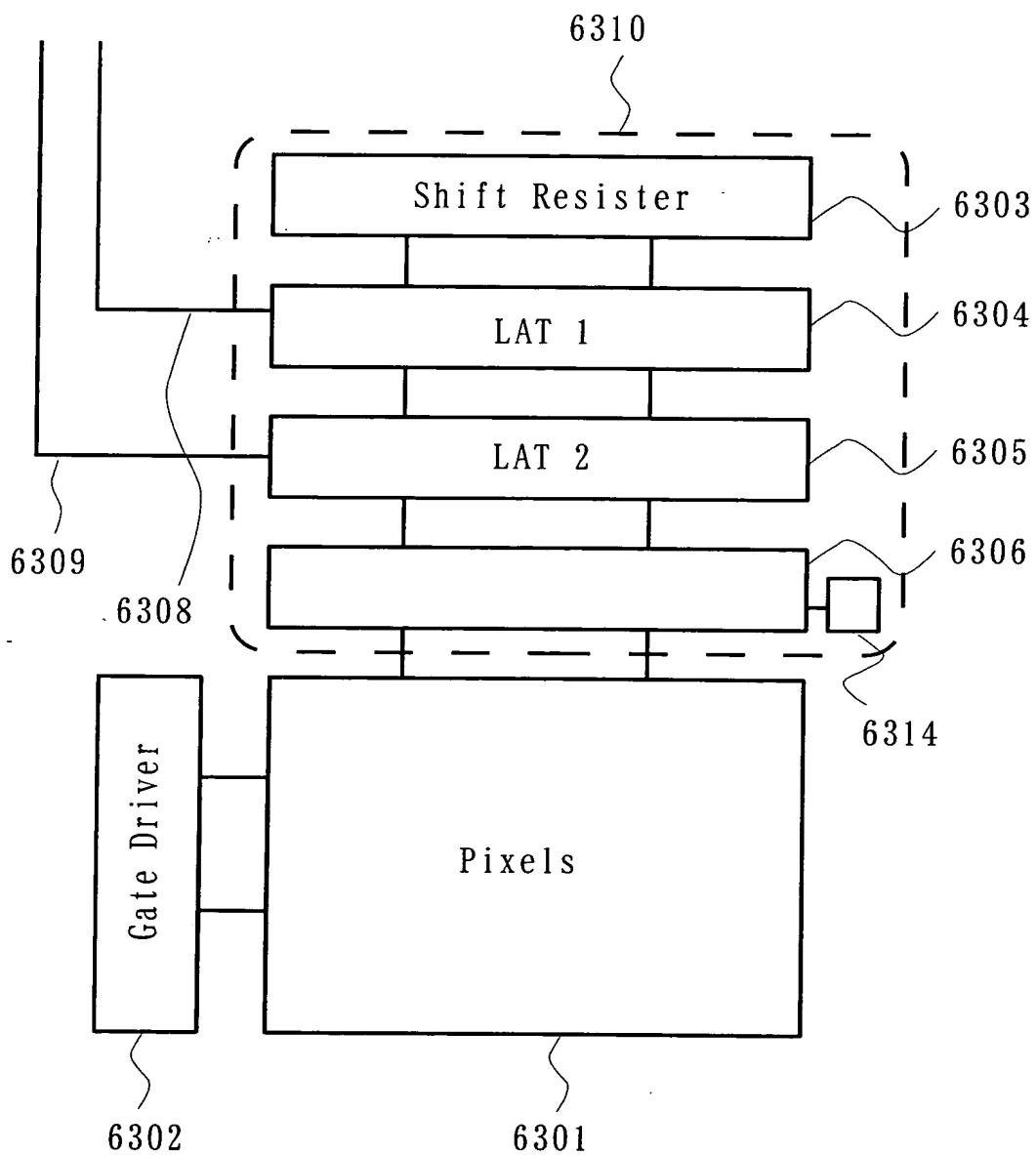


FIG. 63

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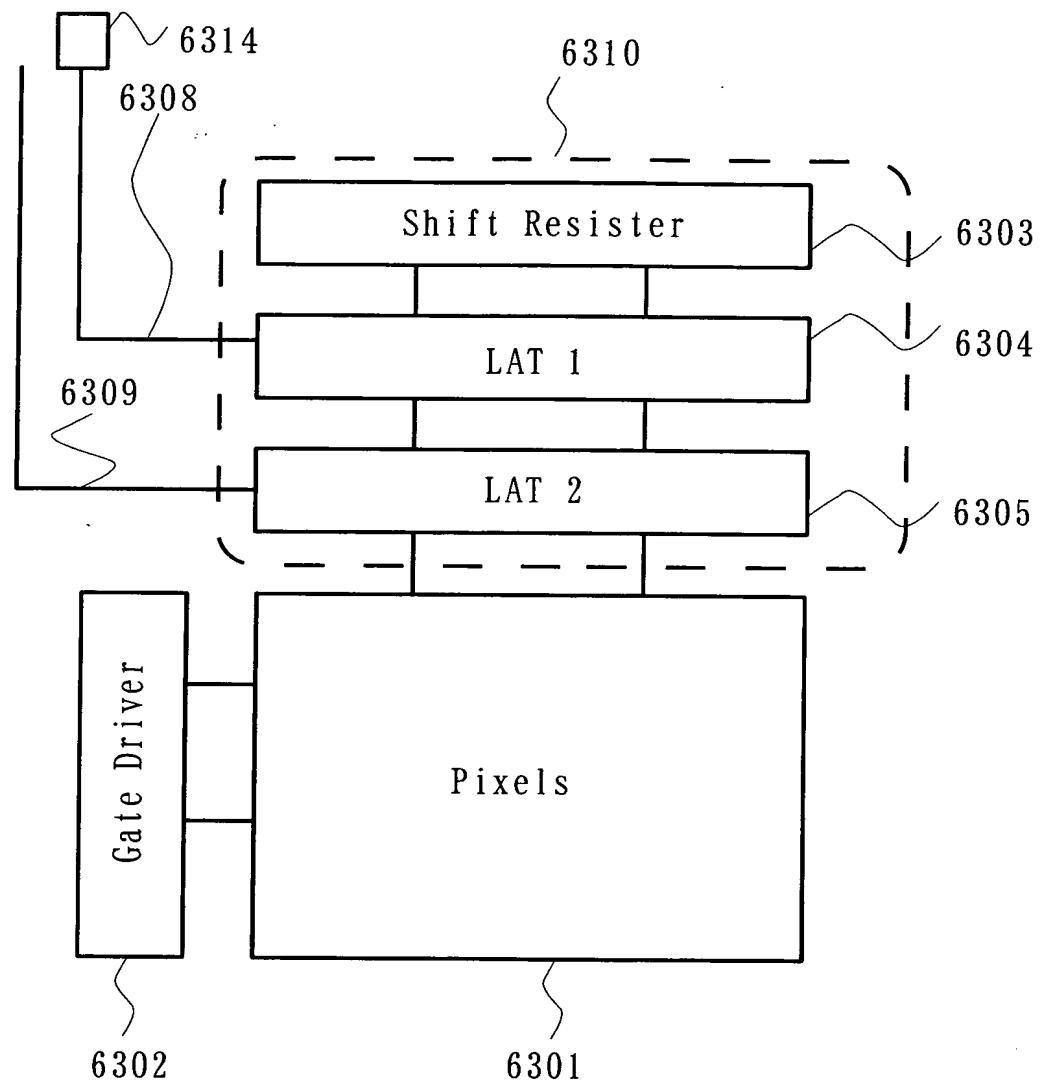


FIG. 64

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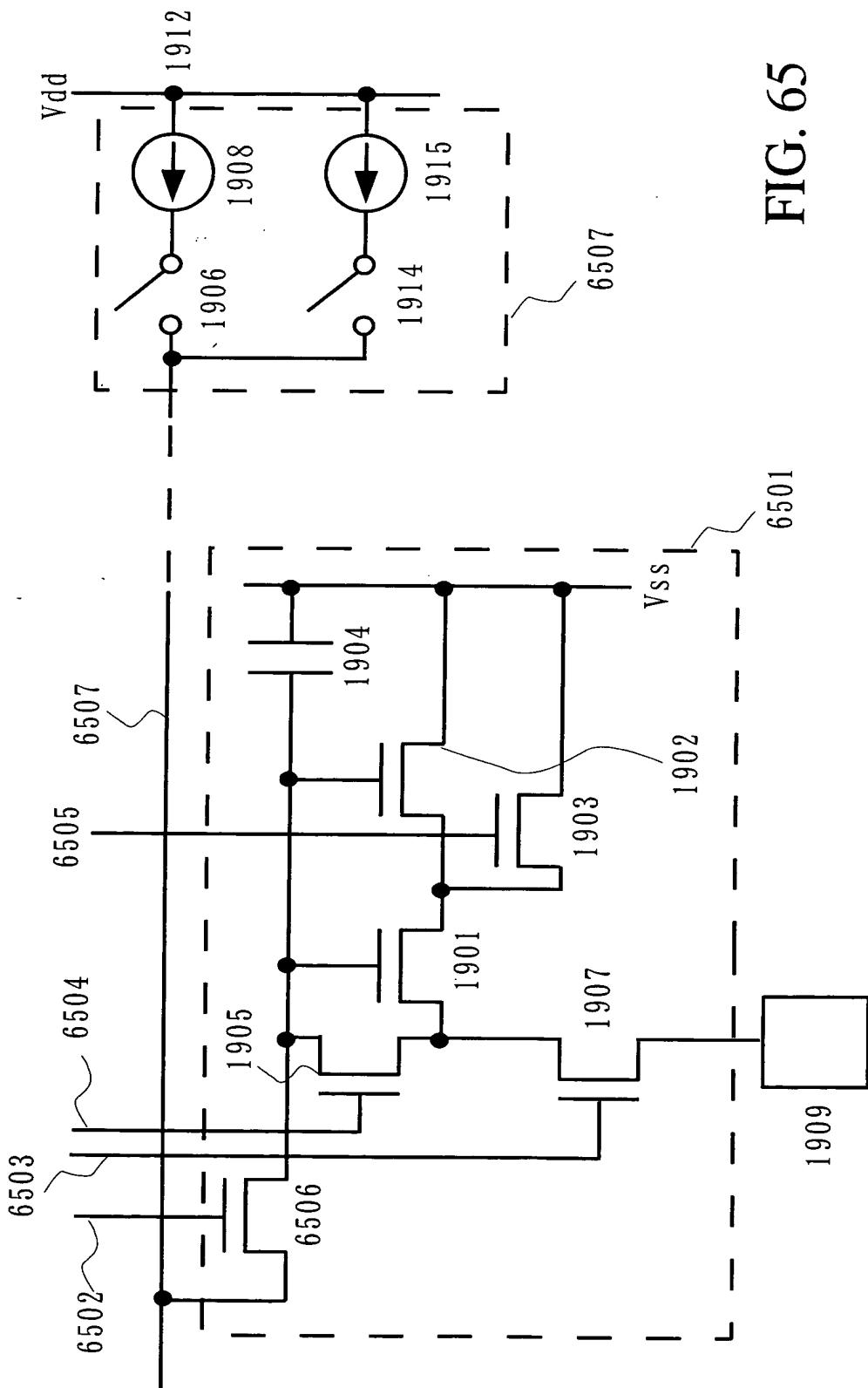


FIG. 65

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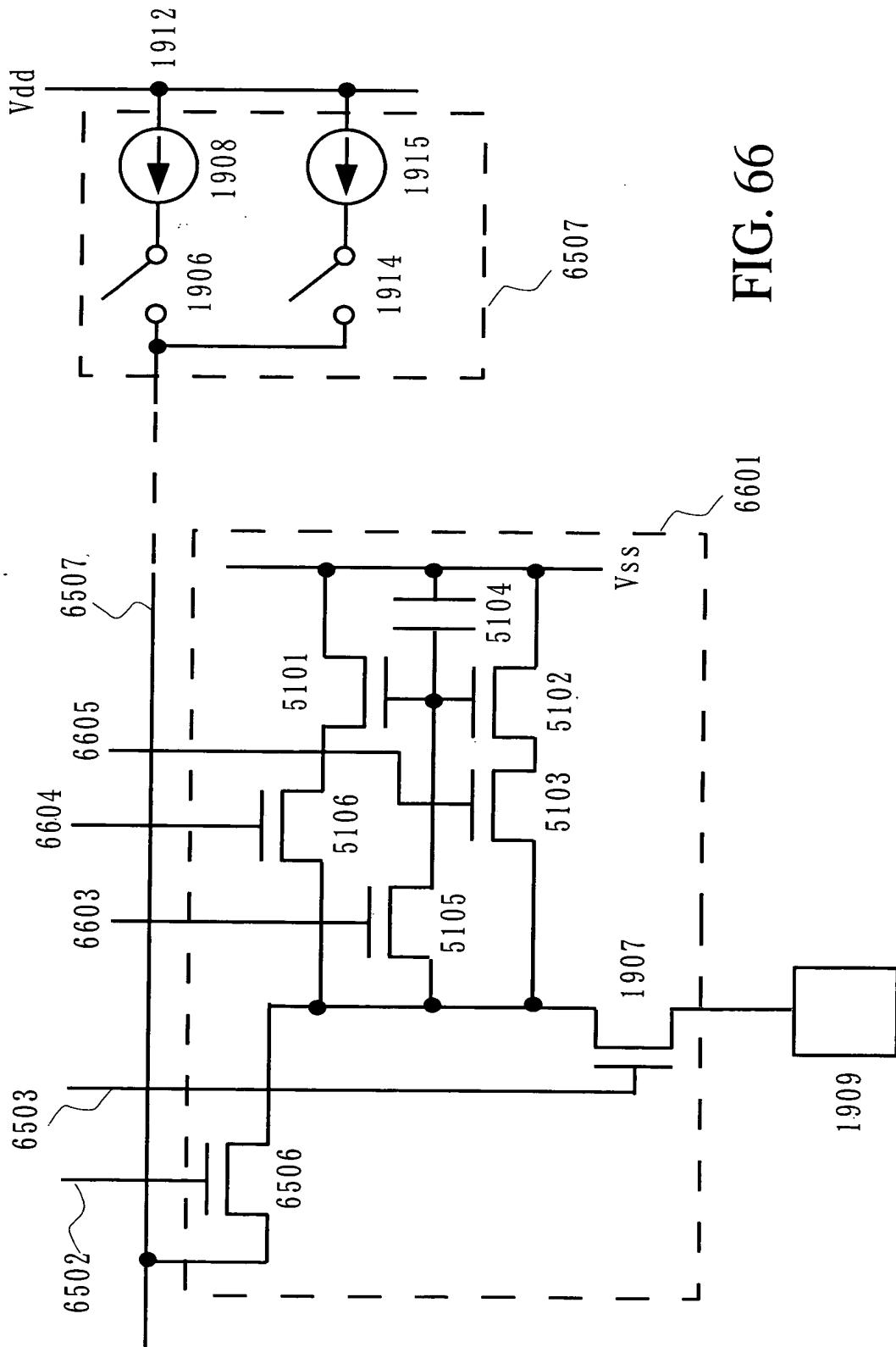
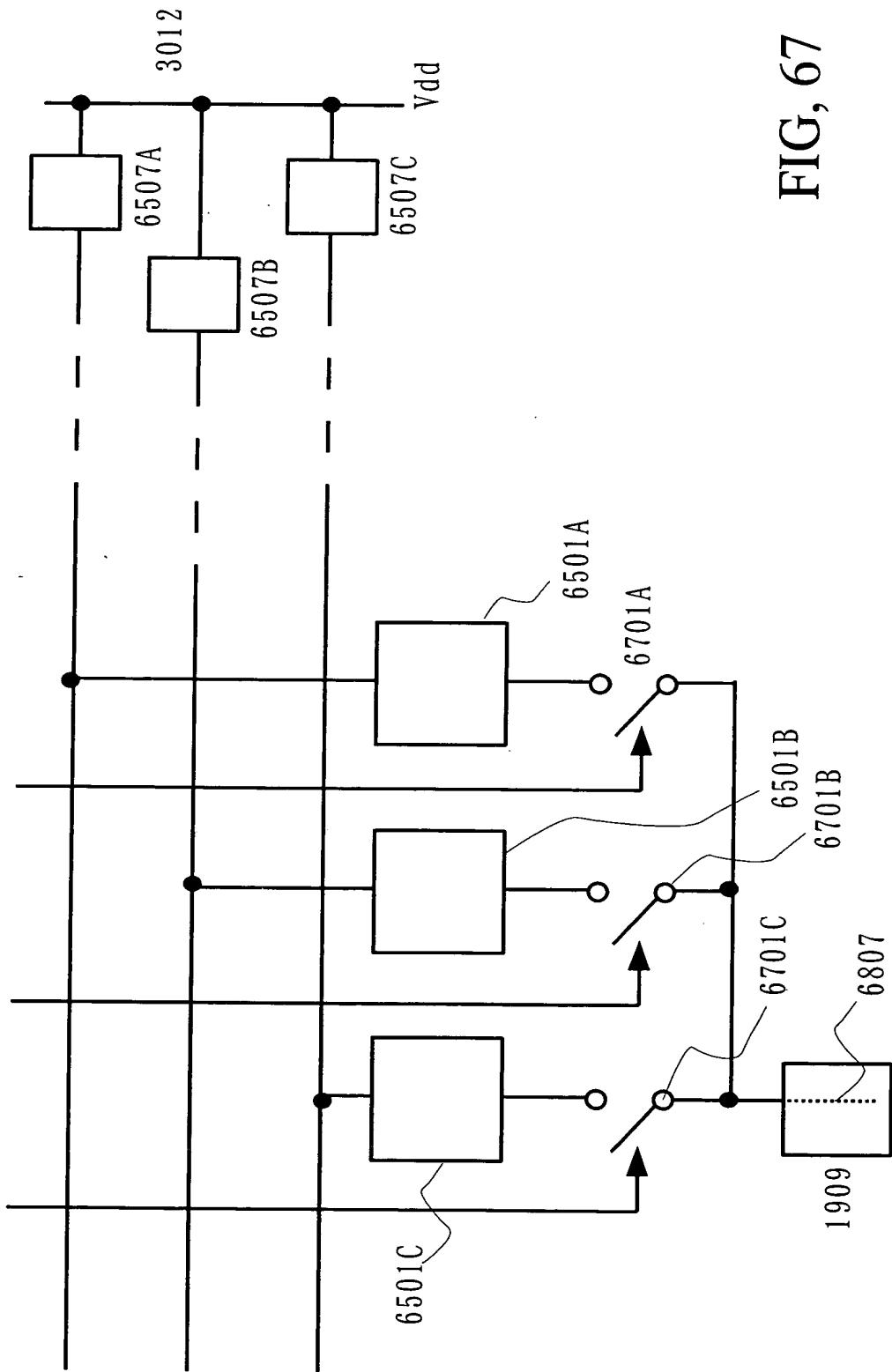


FIG. 66

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FIG, 67

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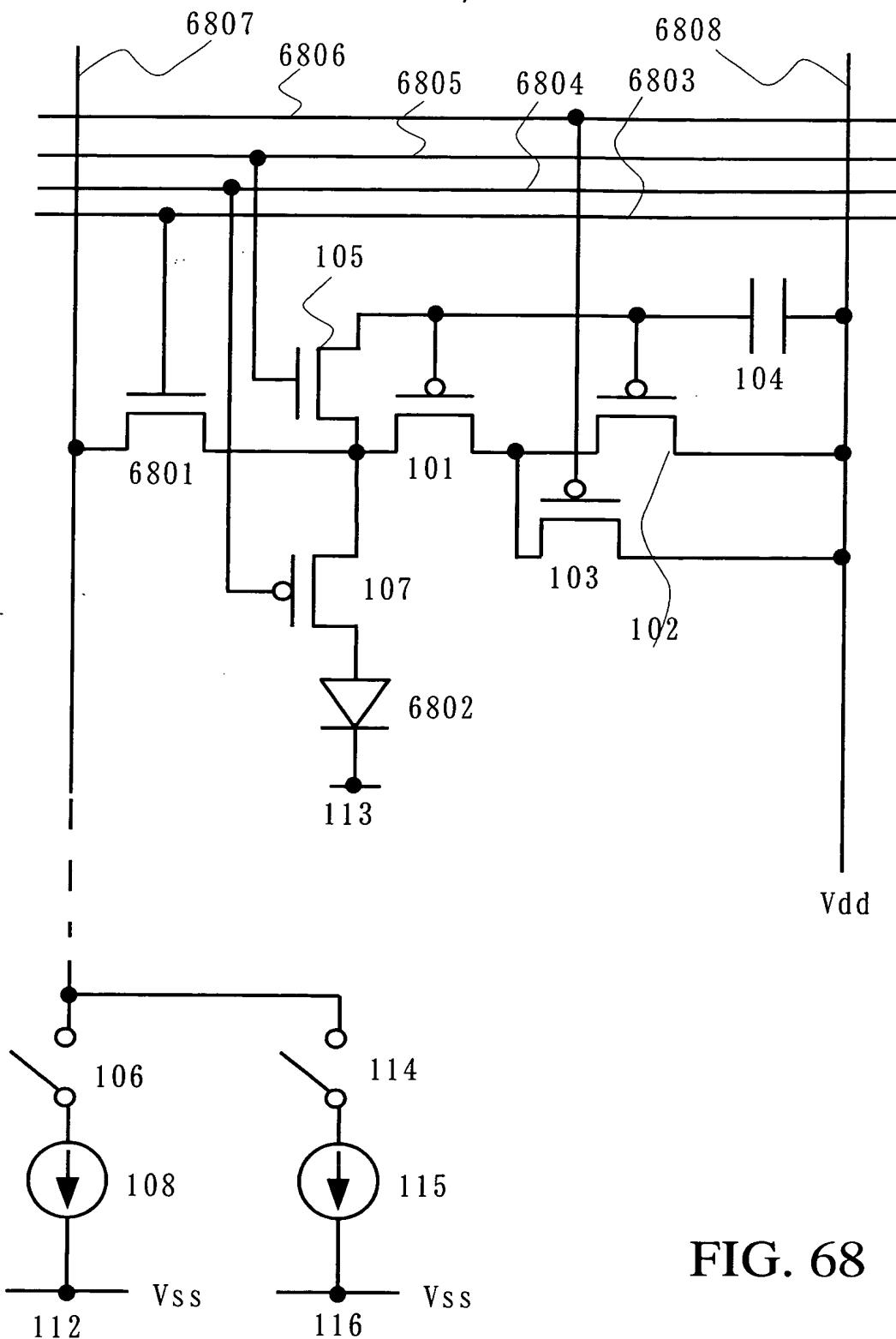


FIG. 68

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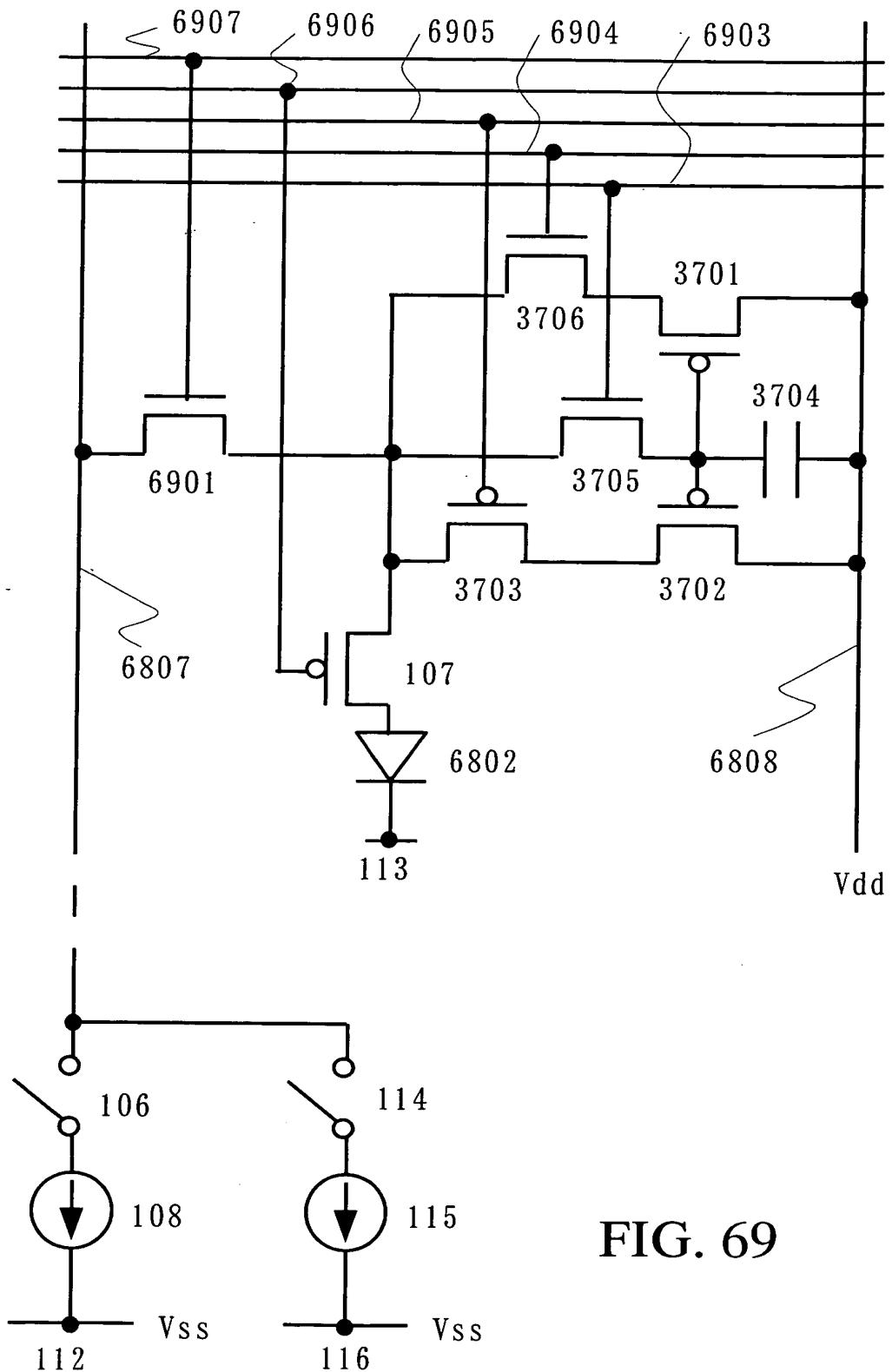


FIG. 69

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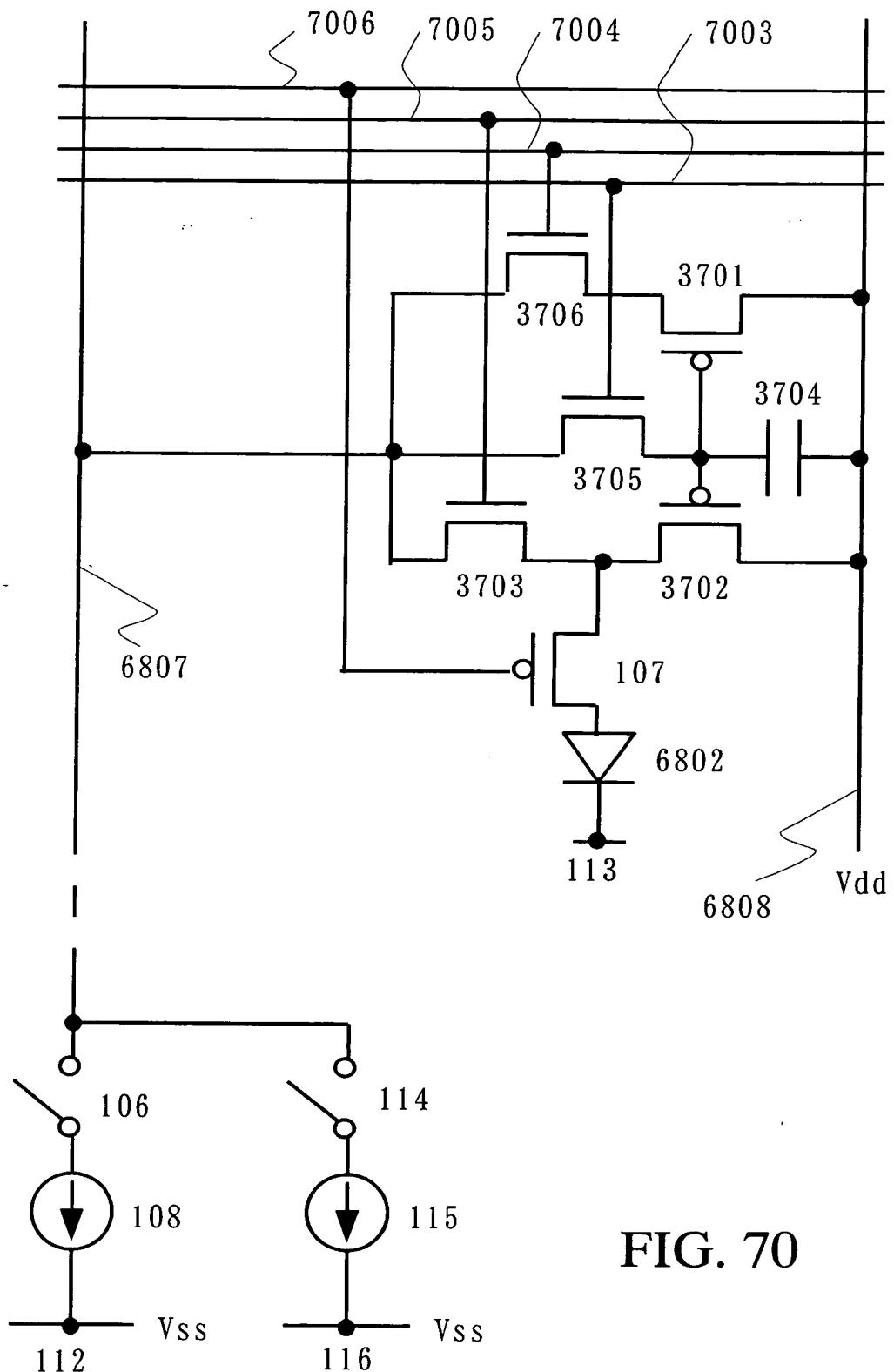


FIG. 70

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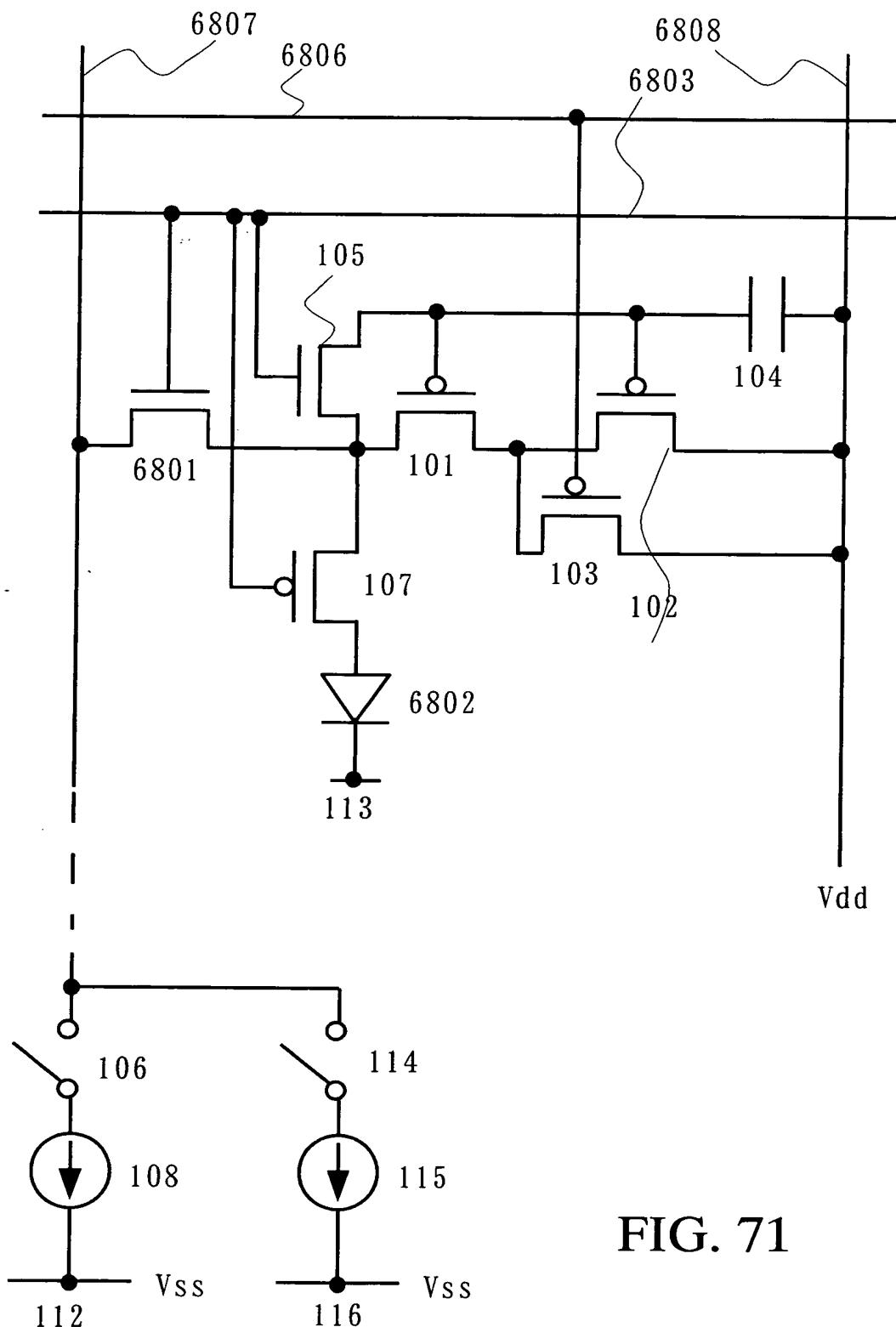


FIG. 71

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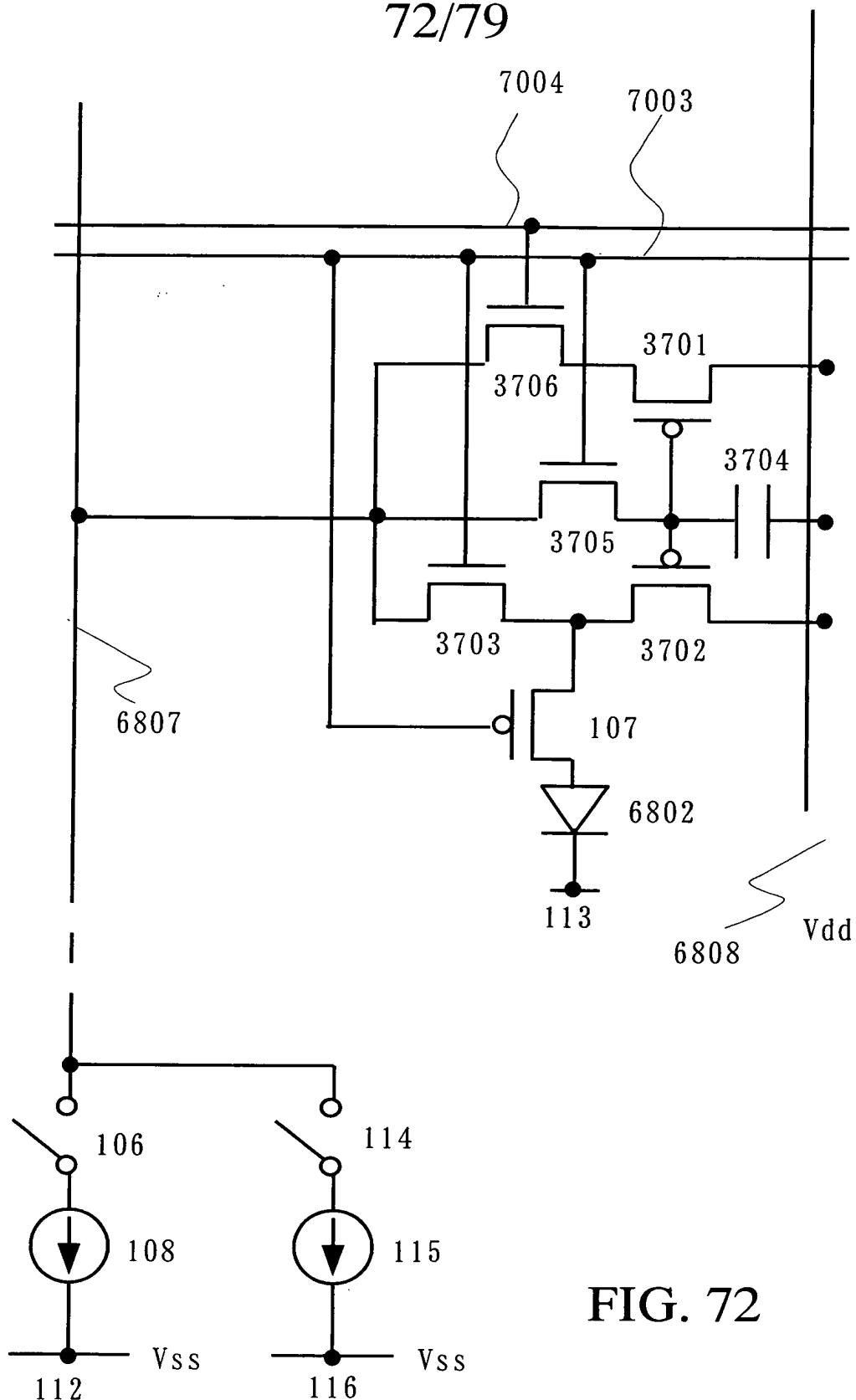


FIG. 72

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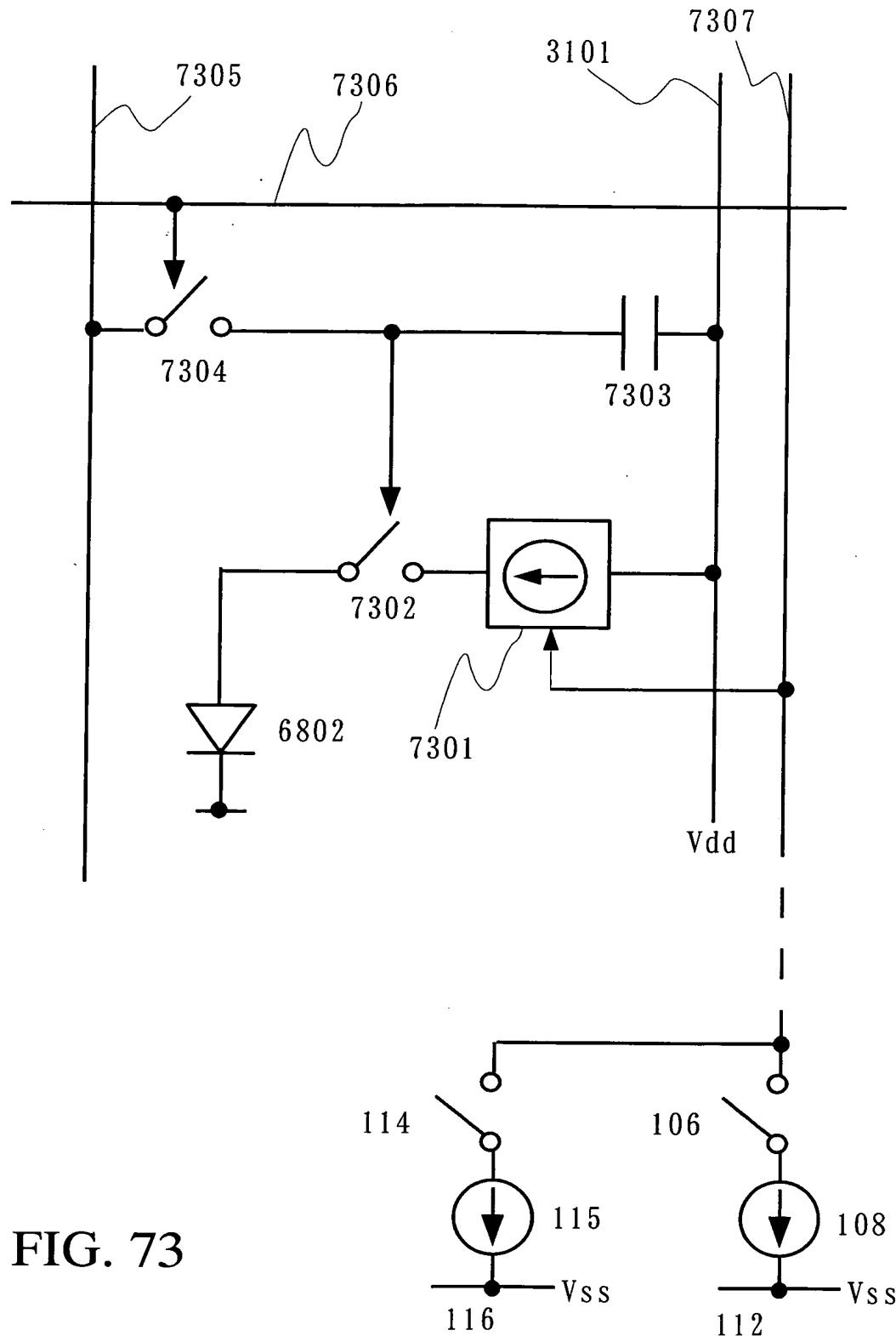


FIG. 73

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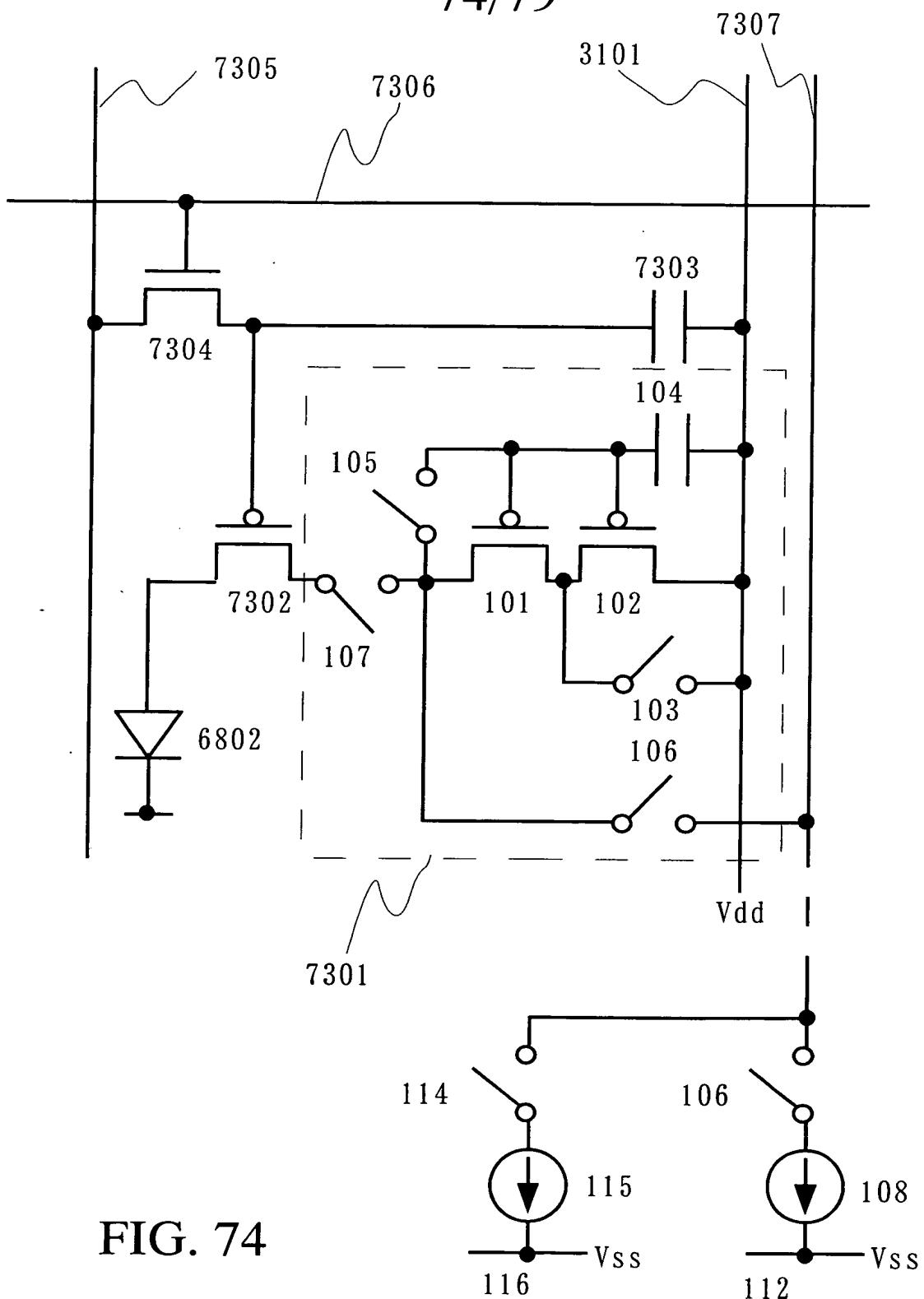


FIG. 74

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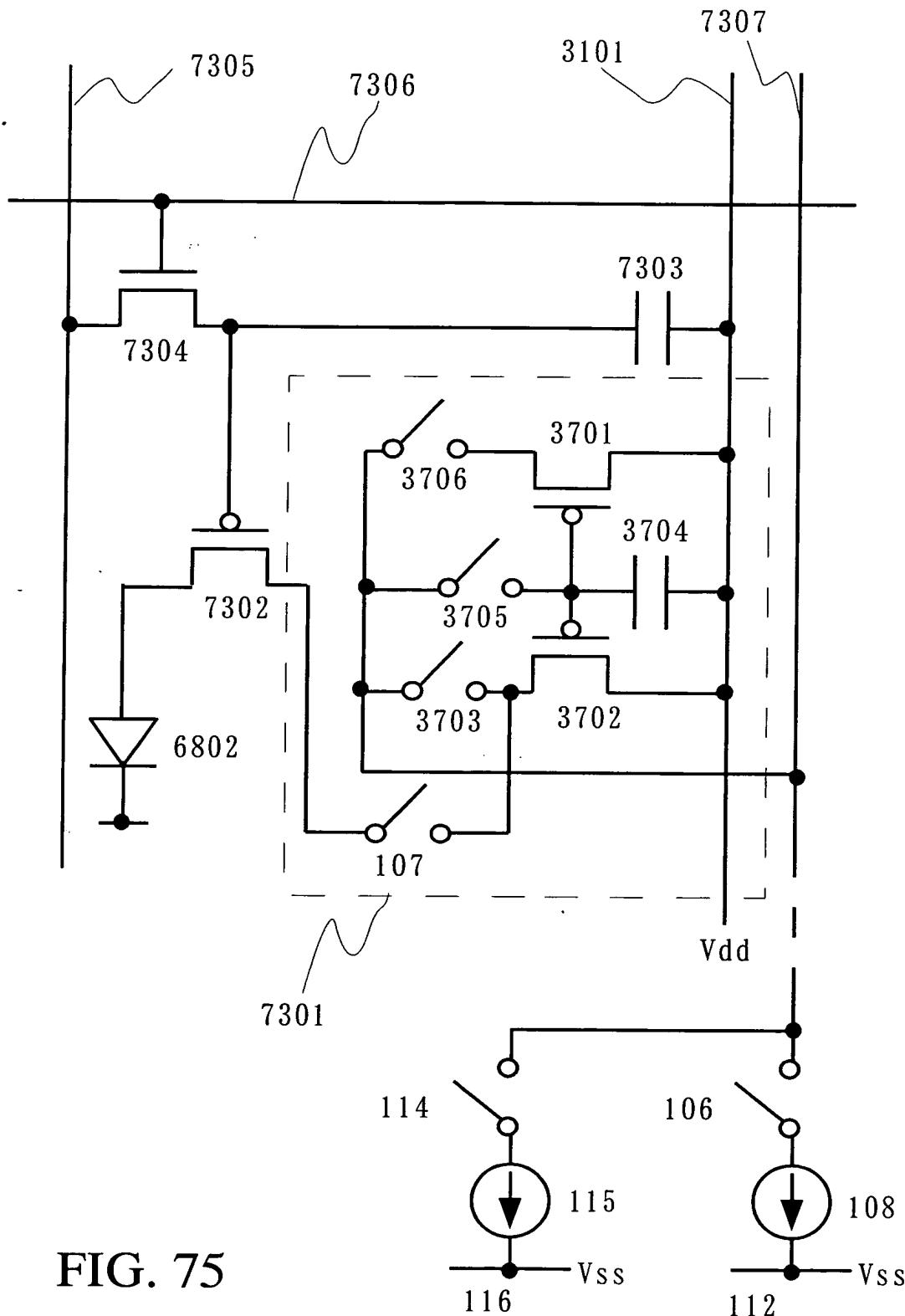
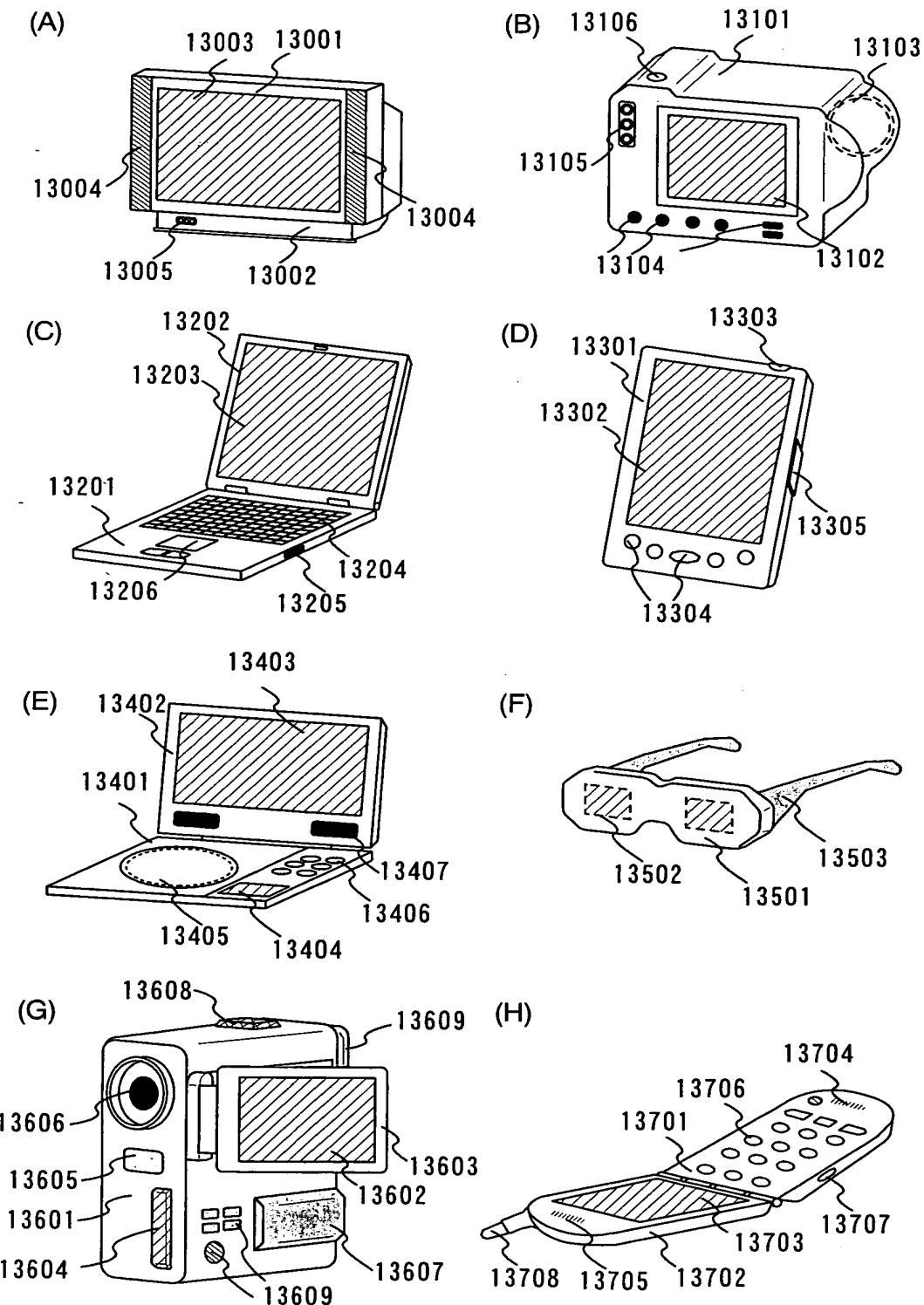


FIG. 75

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FIG. 76



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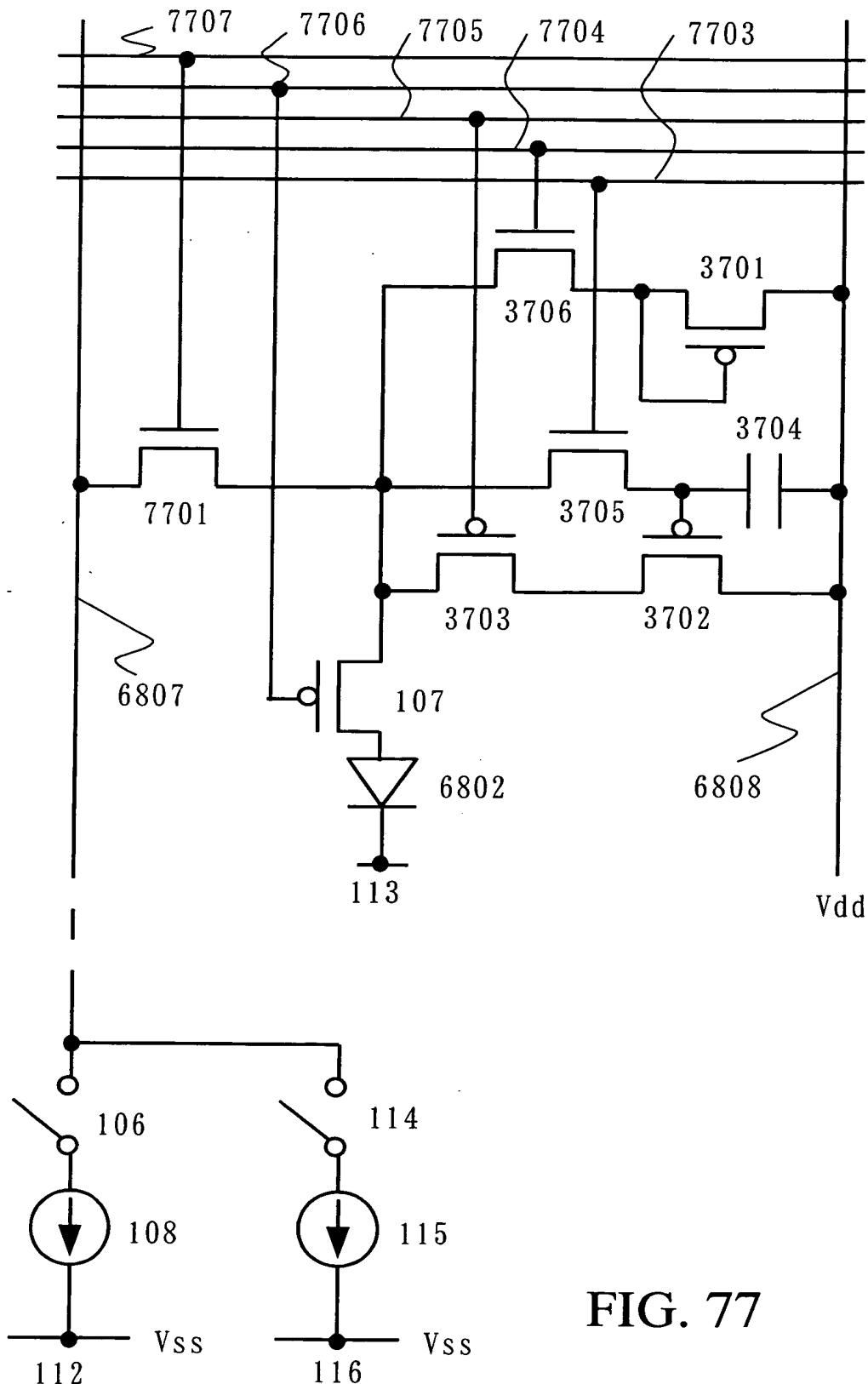


FIG. 77

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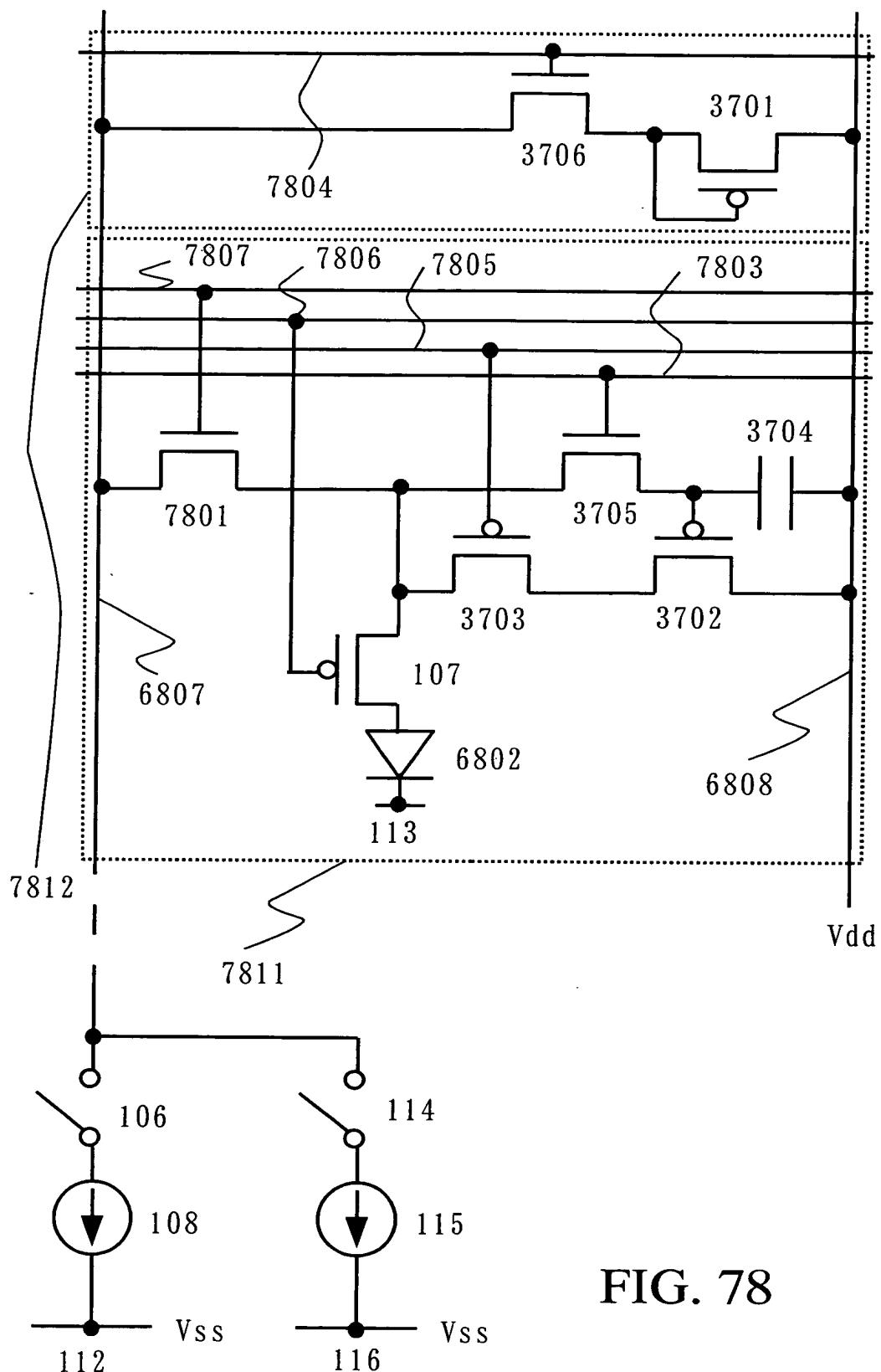


FIG. 78

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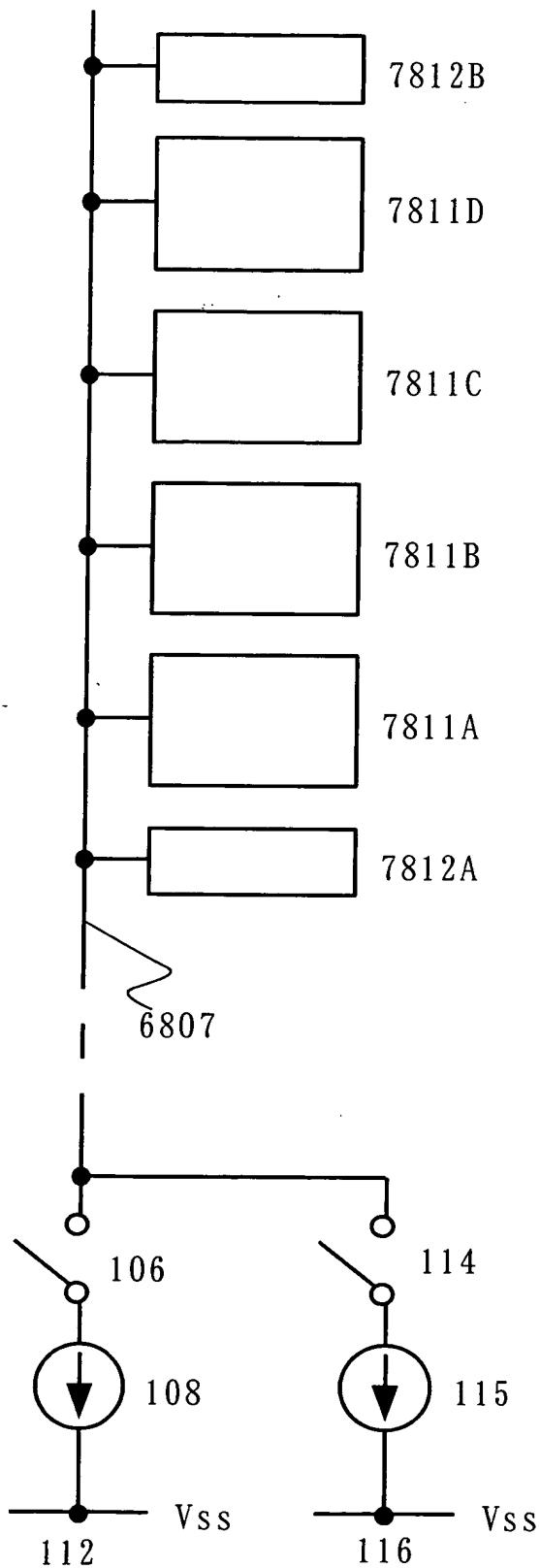


FIG. 79